

# NEC's 870 MHz GaAs CATV PUSH-PULL AMPLIFIER

MC-7831 MC-7832 MC-7833

## **FEATURES**

- · GaAs ACTIVE DEVICES
- · LOW DISTORTION
- · HIGH LINEAR GAIN:

MC-7831 - GL = 18 dB MIN at f = 870 MHz MC-7832 - GL = 22 dB MIN at f = 870 MHz MC-7833 - GL = 25 dB MIN at f = 870 MHz

- LOW RETURN LOSS
- · LOW GAIN CHANGE OVER TEMPERATURE
- SPECIFIED FOR 79, 110, and 132 CHANNELS PERFORMANCE
- HIGH RELIABILITY AND RUGGEDNESS:
   Withstands environmental extremes as well as Silicon devices (Surge, ESD, Etc.)

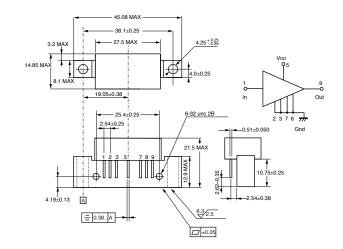
## **DESCRIPTION**

NEC's MC-7831, MC-7832, and MC-7833 are GaAs Multi-Chip Modules designed for use as input stages in CATV applications up to 870 MHz. The only difference between these devices is gain, which is 18 dB, 22 dB, and 25 dB respectively. Because these units are GaAs devices they have low distortion, low noise figure, and low return loss across the entire frequency band.

Like the previous generation of products, these devices survive such hazards as surge and ESD as well as their silicon competitors, but deliver superior performance with low DC current required. All devices are assembled and tested using fully automated equipment to maximize consistency in part to part performance, and reliability is assured by NEC's stringent quality and process control procedures. These parts come in industry compatible hybrid packages.

# **OUTLINE DIMENSIONS** (Units in mm)

### **PACKAGE OUTLINE H02**



### **APPLICATIONS**

- · CATV HEADEND SYSTEMS
- · CATV OPTICAL NODES
- CATV DISTRIBUTION AMPS

# ELECTRICAL CHARACTERISTICS (TA = 30±5 °C, VDD = 24 V, Zs = ZL = 75 Ω)

PART NUMBER		MC-7831		MC-7832		MC-7833		33				
SYMBOLS	CHARACTERISTICS	UNITS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	TEST CONDITIONS
BW	Frequency Range	MHz	50	_	870	50	-	870	50	_	870	
GL	Linear Gain	dB	18.0	_	19.0	22.0	_	23.0	25.0	-	26.0	f = 870 MHz
S	Gain Slope	dB	0.2	_	1.0	0.6	_	1.4	1.0	_	1.8	f = 40 to 870 MHz
Gf	Gain Flatness	dB	_	_	0.7	1	-	0.7	-	_	0.7	40 to 870 MHz; Peak to Valley
NF	Noise Figure 1	dB	_	_	6.5	ı	-	6.0	-	-	5.5	f = 50 MHz
	Noise Figure 2		_	_	7.0	-	_	6.5	-	_	6.0	f = 870 MHz
RL	Input/Output Return Loss	dB	20.0	_	-	20.0	-	_	20.0	-	_	40 to 160MHz
			19.0	_	_	19.0	_	_	19.0	_	-	160 to 320 MHz
			17.5	_	_	17.5	ı	_	17.5	-	_	320 to 640 MHz
			16.0	_	_	16.0	_	_	16.0	_	-	640 to 870 MHz
IDD	Operating Current	mA	180	_	240	180	-	240	180	-	240	RF OFF
СТВ	Composite Triple Beat	dBc	_	-59	-57	-	-59	-57	-	-59	-57	f = 40 to 870 MHz
XMod	Cross Modulation <sup>1</sup> dBc	dBc	-	-51	-50	1	-53	-50	-	-53	-50	110 Channels,
CSO	Composite Second Order	dBc	_	-63	-57	ı	-60	-57	ı	-61	-57	Vout = 44 dBmV, Flat

<sup>1.</sup> Measured per US standard methods and procedures (using selective level meter).

# ABSOLUTE MAXIMUM RATINGS<sup>1</sup> (TCASE= 30 °C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
VDD	Supply Voltage	V	30
Vı	Input Voltage <sup>2</sup>	dBmV	65
Tc	Operating Case Temperature	°C	-30 to +100
Тѕтс	Storage Temperature	°C	-40 to +100

#### Note:

- Operation in excess of any one of these parameters may result in permanent damage.
- 2. Maximum single channel power applied to the input for 1 minute with no measurable degradation in performance.

# RECOMMENDED OPERATING CONDITIONS (Zs = ZL = 75Ω)

SYMBOLS	PARAMETERS		UNITS	MIN	TYP	MAX
V <sub>DD</sub>	Supply Voltage,	MC-7831	V	23.5	24.0	24.5
		MC-7832		23.5	24.0	24.5
		MC-7833		23.5	24.0	24.5
Vi	Input Voltage <sup>1</sup> ,	MC-7831	dBmV	_	21.0	27.5
		MC-7832		_	21.0	27.5
		MC-7833		_	19.0	25.5
Tc	Operating Case,	MC-7831	°C	-30	+25	+85
	Temperature	MC-7832		-30	+25	+85
		MC-7833		-30	+25	+85

Note:

1. Test Conditions: 110 Channels, Flat

### ORDERING INFORMATION

PART NUMBER	PACKAGE	QUANTITY		
MC-7831-AZ	7-pin special with heatsink	50pcs max/ Tray		
MC-7832-AZ	7-pin special with heatsink	50pcs max/ Tray		
MC-7833-AZ	7-pin special with heatsink	50pcs max/ Tray		

### **NOTES ON CORRECT USE**

1. The space between PC board and root of the lead should be kept more than 1 mm to prevent undesired stress on the lead and also should be kept less than 4 mm to prevent undesired parasitic inductance.

Recommended space is 2.0 to 3.0 mm typical.

- 2. Recommended torque strength of the screw is 59 to 78 Ncm.
- 3. Form the ground pattern as wide as possible to minimize ground impedance. (to prevent undesired oscillation)

All the ground pins must be connected together with wide ground pattern to decrease impedance difference.

## RECOMMENDED SOLDERING CONDITIONS

This product should be soldered in the following recommended conditions. Other soldering methods and conditions than the recommended conditions are to be consulted with our sales representatives.

Soldering	Soldering	Condition
Method	Conditions	Symbol
Pin Part Heating	Pin area temperature: less than 260°C¹ Hour: Within 2 sec./pin	

Note

1. The point of pin part heating must be kept at a distance of more than 1.2 mm from the root of lead.

Life Support Applications

These NEC products are not intended for use in life support devices, appliances, or systems where the malfunction of these products can reasonably be expected to result in personal injury. The customers of CEL using or selling these products for use in such applications do so at their own risk and agree to fully indemnify CEL for all damages resulting from such improper use or sale.

California Eastern Laboratories, Your source for NEC RF, Microwave, Optoelectronic, and Fiber Optic Semiconductor Devices.
4590 Patrick Henry Drive • Santa Clara, CA 95054-1817 • (408) 988-3500 • FAX (408) 988-0279 • www.cel.com

DATA SUBJECT TO CHANGE WITHOUT NOTICE

07/08/2003





4590 Patrick Henry Drive Santa Clara, CA 95054-1817 Telephone: (408) 919-2500

Facsimile: (408) 988-0279

Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (\*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices		
Lead (Pb)	< 1000 PPM	-A -AZ Not Detected (*)		
Mercury	< 1000 PPM	Not Detected		
Cadmium	< 100 PPM	Not Detected		
Hexavalent Chromium	< 1000 PPM	Not Detected		
PBB	< 1000 PPM	Not Detected		
PBDE	< 1000 PPM	Not Detected		

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

Important Information and Disclaimer: Information provided by CEL on its website or in other communications concerting the substance content of its products represents knowledge and belief as of the date that it is provided. CEL bases its knowledge and belief on information provided by third parties and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. CEL has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. CEL and CEL suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall CEL's liability arising out of such information exceed the total purchase price of the CEL part(s) at issue sold by CEL to customer on an annual basis.

See CEL Terms and Conditions for additional clarification of warranties and liability.