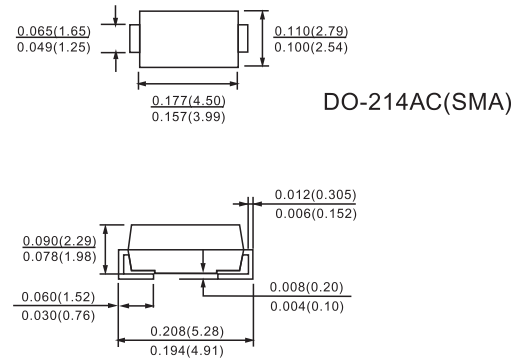


FEATURES

- ◇ Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- ◇ For surface mounted applications
- ◇ Low profile package
- ◇ Built-in strain relief
- ◇ Metal silicon junction, majority carrier conduction
- ◇ High surge capability
- ◇ High current capability, low forward voltage drop
- ◇ Low power loss, high efficiency
- ◇ For use in low voltage high frequency inverters, free wheeling and polarity protection applications
- ◇ Guardring for overvoltage protection
- ◇ High temperature soldering guaranteed: 250°C/10 seconds at terminals

MECHANICAL DATA

- ◇ Case: JEDEC DO-214AC, molded plastic over passivated chip
- ◇ Terminals: Solder Plated, solderable per MIL-STD-750, Method 2026
- ◇ Polarity: Color band denotes cathode end
- ◇ Weight: 0.002 ounces, 0.064 gram



Maximum Ratings and Electrical Characteristics

Rating 25°C Ambient Temperature Unless Otherwise Specified.
 Single Phase Half Wave, 60Hz , Resistive or Inductive Load.
 For Capacitive Load, Derate Current by 20%.

Characteristics	Symbol	B220A	B230A	B240A	B250A	B260A	Unit
Maximum Recurrent Peak Reverse Voltage	VRRM	20	30	40	50	60	V
Maximum RMS Voltage	VRMS	14	21	28	35	42	V
Maximum DC Blocking Voltage	VDC	20	30	40	50	60	V
Maximum Average Forward Rectified Current @TC=100°C	IF(AV)	2.0					A
Peak Forward Surge Current, 8.3 ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	IFSM	50					A
Maximum Instantaneous At 2.0A DC	VF	0.5		0.7		V	
Maximum DC Reverse Current @Tj=25°C At Rated DC Blocking Voltage @Tj=100°C	IR	0.5 20					mA
Typical Junction Capacitance (Note 1)	C _J	200					PF
Typical Thermal Resistance (Note 2)	RθJL	15					°C/W
Operating Temperature Range	T _J	-55 to +125					°C
Storage Temperature Range	TSTG	-55 to +150					°C

NOTES: 1. Measured at 1.0MHz applied reverse voltage of 4.0V DC.
 2. Thermal Resistance Junction to case.

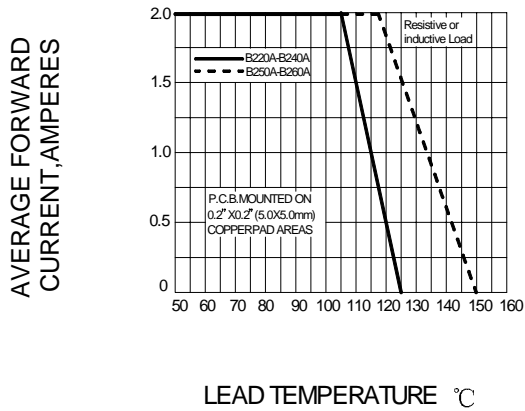


FIG.1 Forward Derating Curve

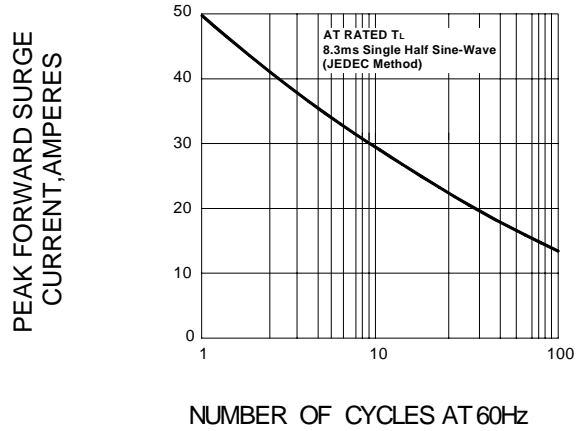


FIG.2 Peak Forward Surge Current

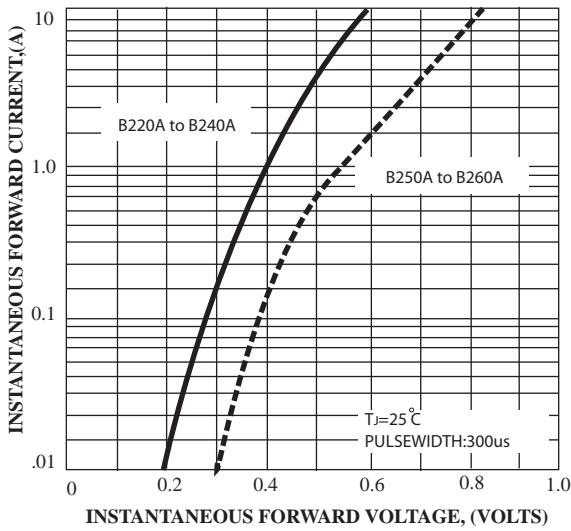


FIG.3 Typical Forward Characteristics

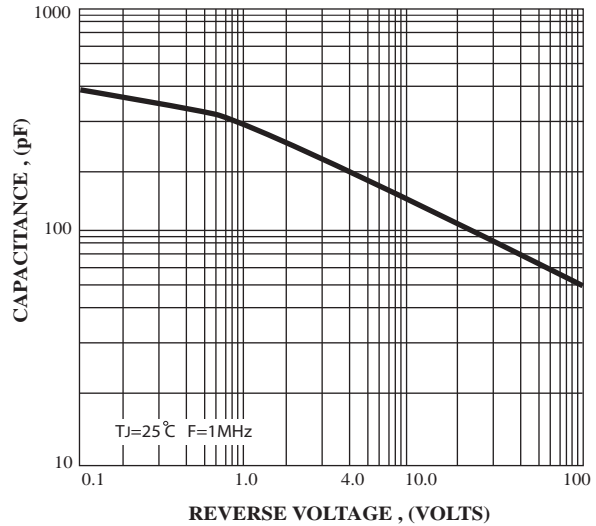


FIG.4 Typical Junction Capacitance

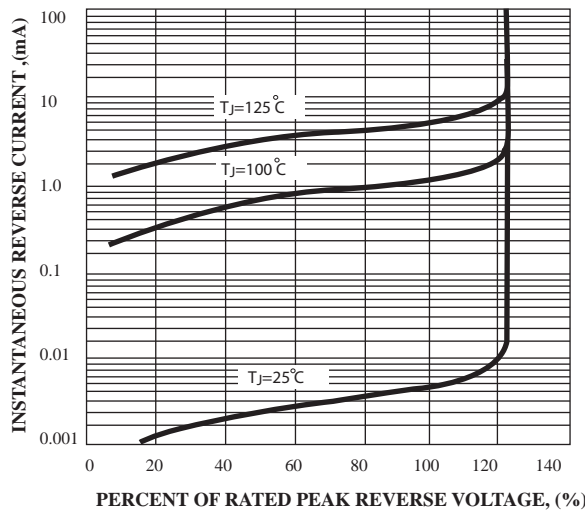


FIG.5 Typical Reverse Characteristics