



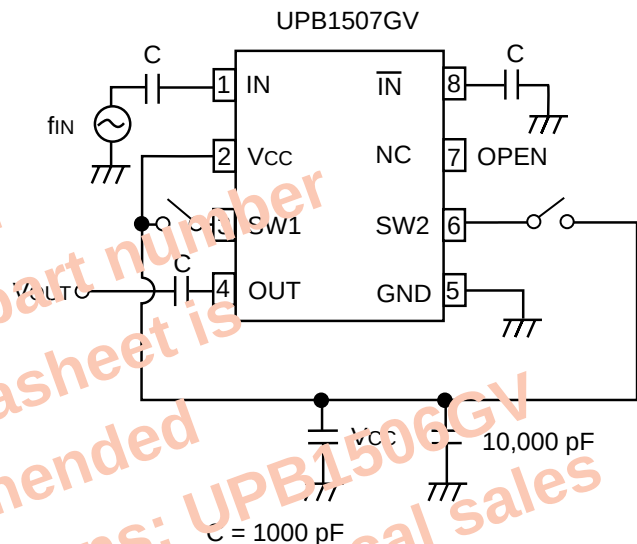
NEC's 3.0 GHz DIVIDE BY 64/128/256 PRESCALER

~~UPB1506GV~~
UPB1507GV

FEATURES

- HIGH FREQUENCY OPERATION TO 3 GHz
- SELECTABLE DIVIDE RATIO: $\div 64$, $\div 128$, $\div 256$
- LOW CURRENT CONSUMPTION: 19 mA at 5 V
- SMALL PACKAGE: 8 pin SSOP
- AVAILABLE IN TAPE AND REEL

TEST CIRCUIT



DESCRIPTION

NEC's UPB1506GV and UPB1507GV are Silicon MMIC digital prescalers manufactured with the NESAT™ IV silicon bipolar process. They feature high frequency response to 3 GHz, selectable divide-by-64, 128, or 256 modes, and operate on a 5 volt supply while drawing only 19 mA. The devices are housed in a small 8 pin SSOP package that contributes to system miniaturization. These devices are designed for use in a PLL synthesizer for DBS and CATV settop and WLAN applications.

ELECTRICAL CHARACTERISTICS (TA = -40 to +85°C, Vcc = 4.5 to 5.5 V, Zs = 50Ω)

PART NUMBER PACKAGE OUTLINE			UPB1506GV,UPB1507GV S08		
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX
Icc	Circuit Current	mA	12.5	19	26.5
f _{IN(U)}	Upper Limit Operating Frequency, P _{IN} = -15 to +6 dBm	GHz		3.0	
f _{IN(L)1}	Lower Limit Operating Frequency, P _{IN} = -10 to +6 dBm	GHz			0.5
f _{IN(L)2}	Lower Limit Operating Frequency, P _{IN} = -15 to +6 dBm	GHz			1.0
P _{IN1}	Input Power, f _{IN} = 1.0 to 3.0 GHz	dBm	-15		+6
P _{IN2}	Input Power, f _{IN} = 0.5 to 1.0 GHz	dBm	-10		+6
V _{OUT}	Output Voltage, C _L = 0.8 pF	V _{P-P}	1.2	1.6	
V _{IN(H)}	Division Ratio Control Input High	V		Vcc	
V _{IN(L)}	Division Ratio Control Input Low	V		OPEN or GND	

ABSOLUTE MAXIMUM RATINGS¹ (T_A = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
V _{CC}	Supply Voltage	V	-0.5 to 6.0
V _{IN}	Input Voltage	V	-0.5 to V _{CC} + 0.5
P _{IN}	Input Power	dBm	+10
P _D	Power Dissipation ²	mW	250
T _{OP}	Operating Temperature	°C	-45 to +85
T _{STG}	Storage Temperature	°C	-55 to +150

- Notes:
1. Operation in excess of any one of these parameters may result in permanent damage.
 2. Mounted on a double-sided copper clad 50x50x1.6 mm epoxy glass PWB (T_A = +85°C).

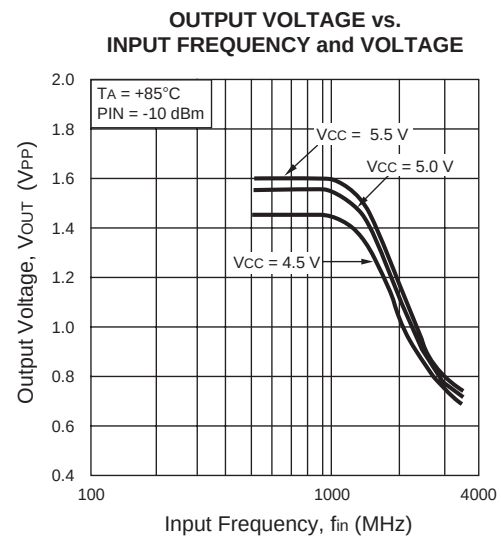
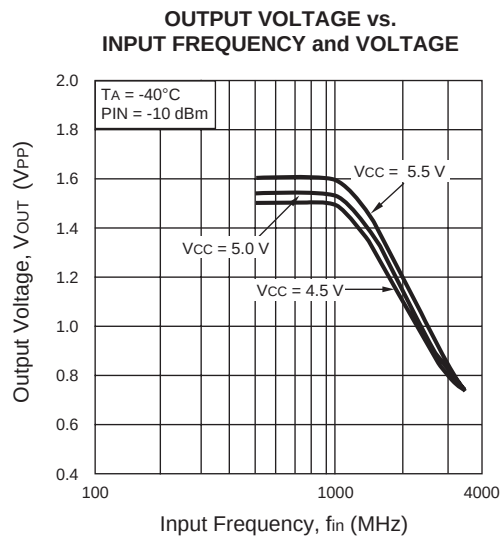
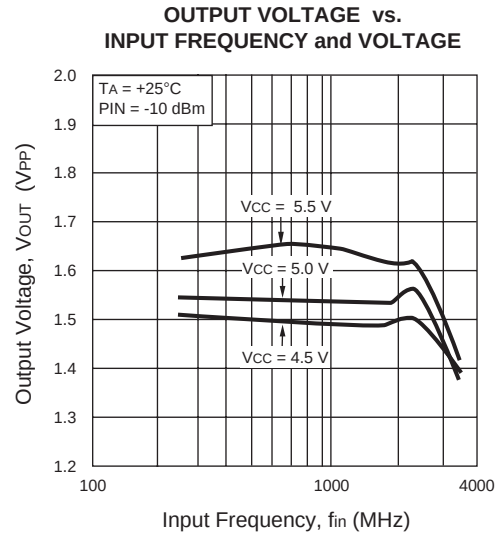
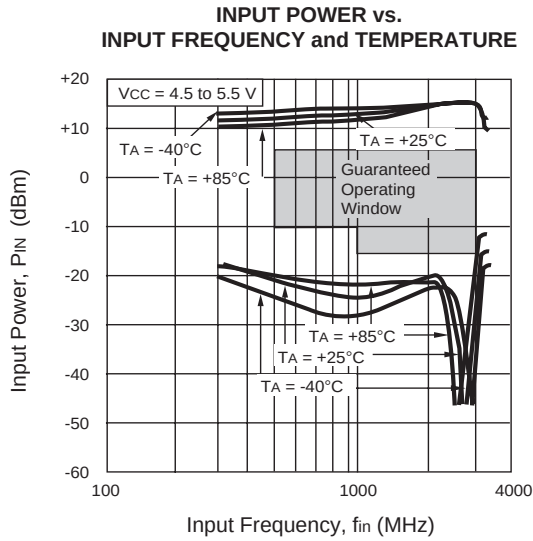
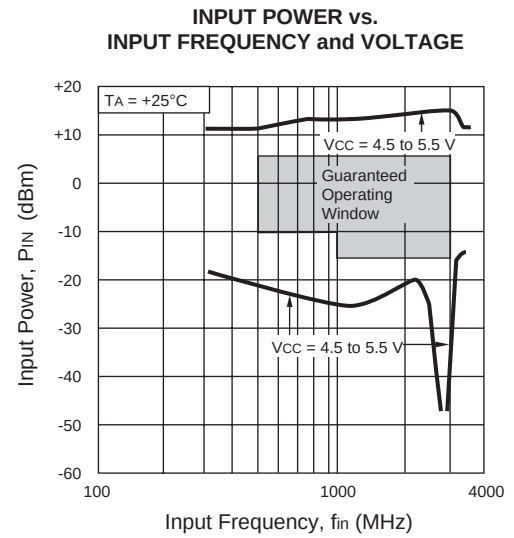
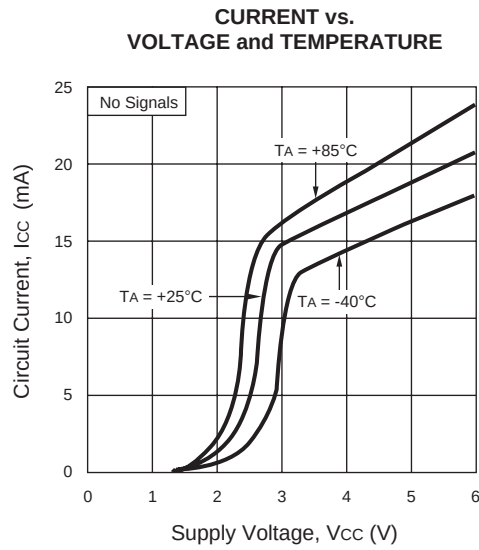
RECOMMENDED
OPERATING CONDITIONS

SYMBOL	PARAMETER	UNITS	MIN	TYP	MAX
V _{CC}	Supply Voltage	V	4.5	5.0	5.5
T _{OP}	Operating Temperature	°C	-40	+25	+85

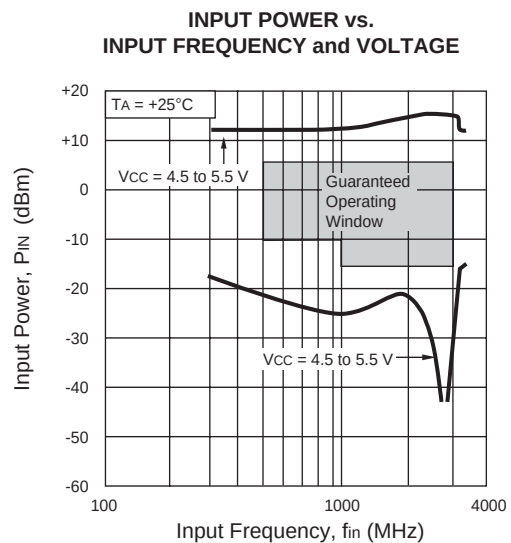
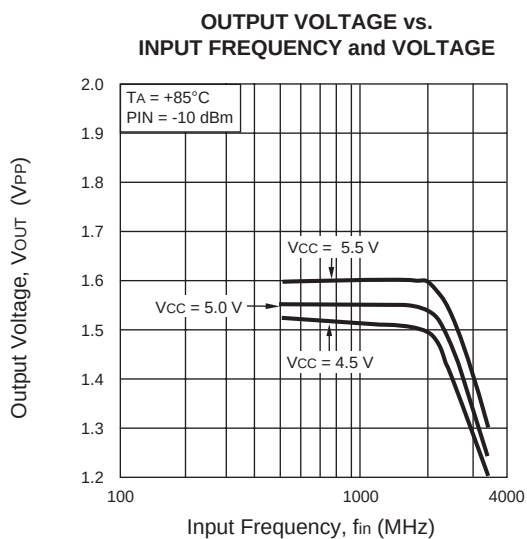
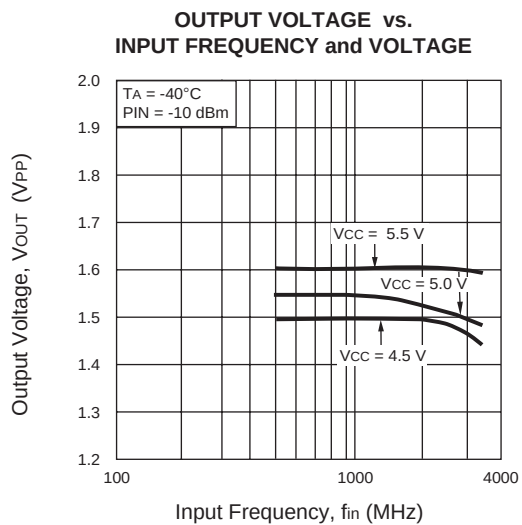
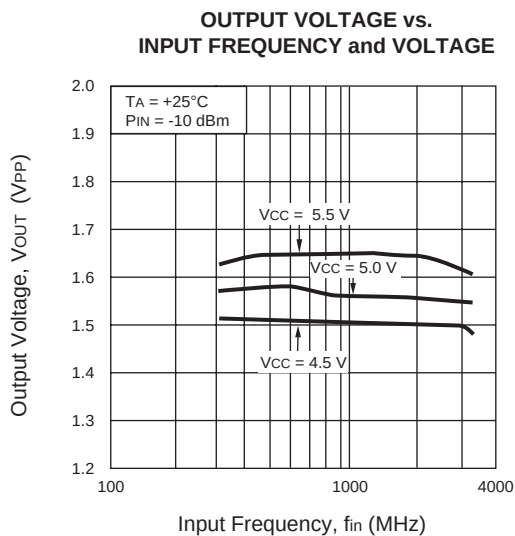
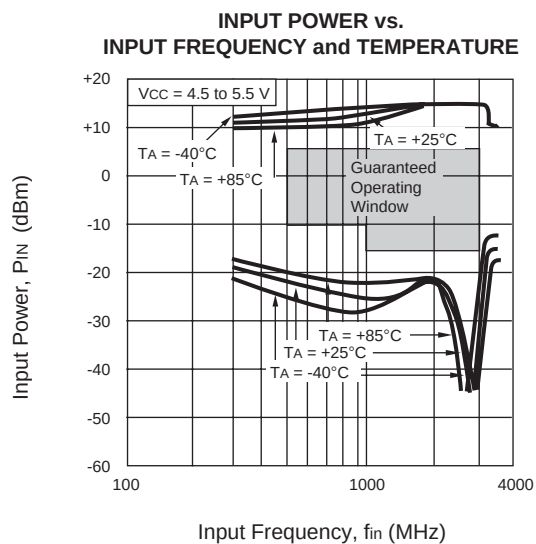
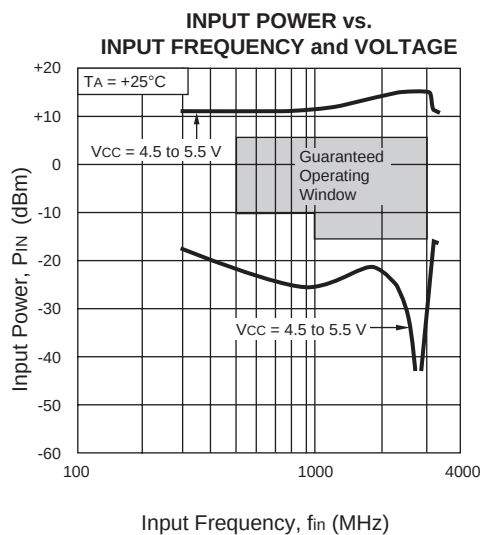
PIN DESCRIPTIONS

Pin No.		Pin Name	Applied Voltage (V)	Pin Voltage (V)	Description													
UPB1506GV	UPB1507GV																	
2	1	IN	–	2.9	Signal input pin. This pin should be coupled to the source with a capacitor (eg 1000 pF).													
3	8	$\overline{\text{IN}}$	–	2.9	Signal input bypass pin. This pin must be equipped with a bypass capacitor (eg 1000 pF) to ground.													
4	5	GND	0	–	Ground pin. Ground pattern on the board should be formed as wide as possible to minimize ground impedance.													
1	3	SW1	H/L	–	Divided ratio input pin. The ratio can be controlled by the following input data to these pins. <table><tr><td colspan="2" rowspan="2"></td><td colspan="2">SW2</td></tr><tr><td>H</td><td>L</td></tr><tr><td rowspan="2">SW1</td><td>H</td><td>+64</td><td>+128</td></tr><tr><td>L</td><td>+128</td><td>+256</td></tr></table>			SW2		H	L	SW1	H	+64	+128	L	+128	+256
		SW2																
		H	L															
SW1	H	+64	+128															
	L	+128	+256															
6	6	SW2			These pins should be equipped with a bypass capacitor (e.g. 1000 pF) to ground.													
8	2	VCC	4.5 to 5.5	–	Power supply pin. This pin must be equipped with bypass capacitor (eg 1000 pF) to ground.													
7	4	OUT	–	2.6 to 4.7	Divided frequency output pin. This pin is designed as an emitter follower output. This pin can be connected to CMOS input due to 1.2 Vp-p MIN output.													
5	7	NC	–	–	No connection. This pin must be opened.													

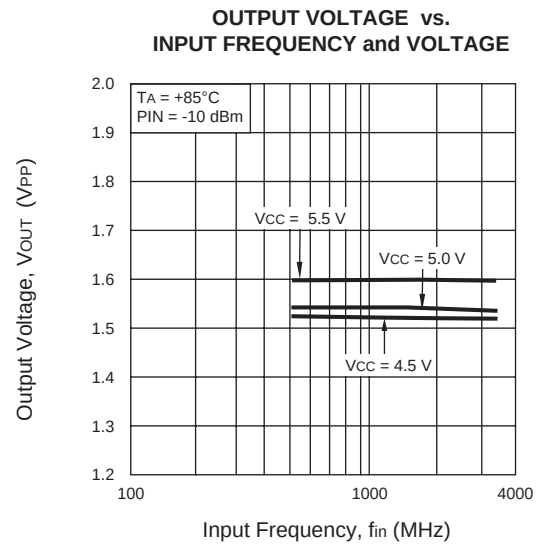
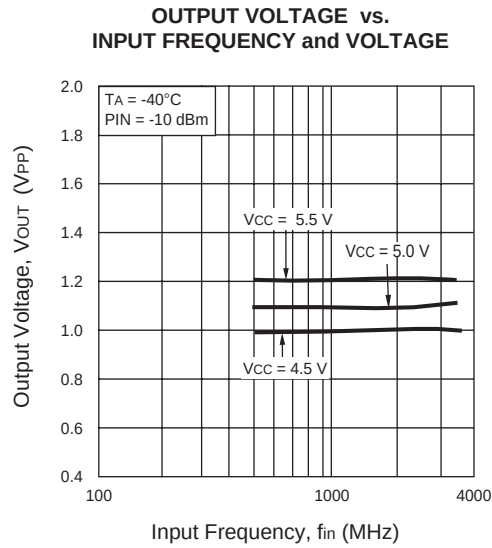
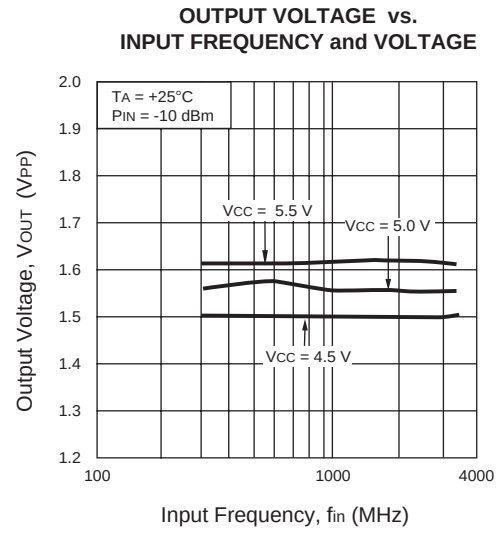
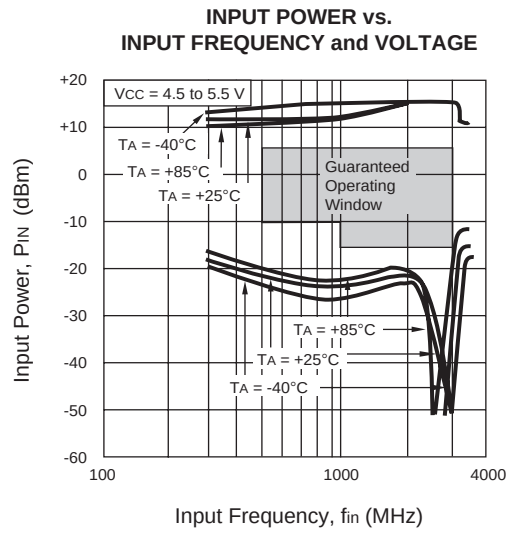
TYPICAL PERFORMANCE CURVES ($T_A = +25^\circ\text{C}$ unless otherwise noted)



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TYPICAL PERFORMANCE CURVES (TA = +25°C unless otherwise noted)

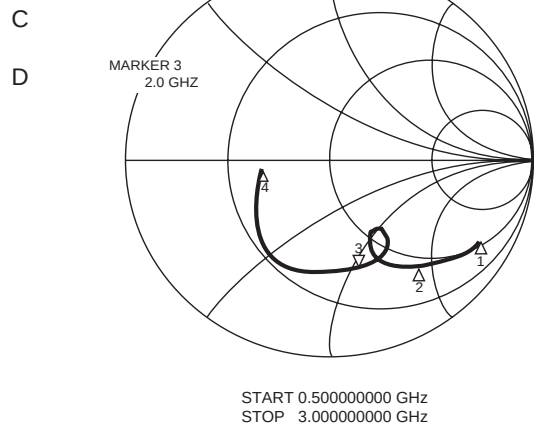


TYPICAL SCATTERING PARAMETERS (TA = 25°C)

UPB1506GV

S11 vs. INPUT FREQUENCY
Vcc = 5.0 V

S11 Z
REF 1.0 Units
3 200.0 mUnits/
▽ 33.881 Ω -52.875 Ω
hp



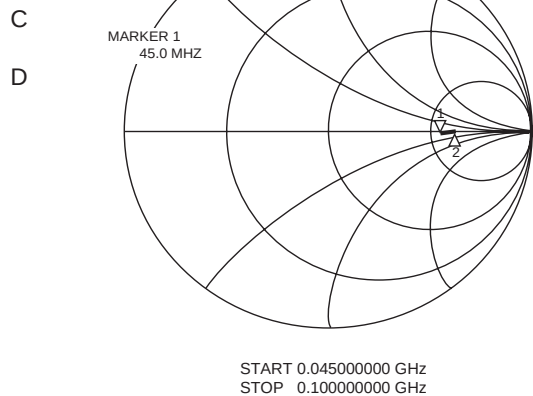
Δ₁ : 0.5 GHz
Δ₂ : 1.0 GHz
Δ₃ : 2.0 GHz
Δ₄ : 3.0 GHz

FREQUENCY		S11	
GHz		MAG	ANG
0.5		0.868	-26.6
0.6		0.828	-32.6
0.7		0.794	-37.4
0.8		0.761	-41.9
0.9		0.721	-46.5
1.0		0.706	-49.3
1.1		0.662	-54.0
1.2		0.629	-57.2
1.3		0.595	60.2
1.4		0.554	-62.9
1.5		0.516	-64.8
1.6		0.440	-61.9
1.7		0.428	51.0
1.8		0.543	-61.5
1.9		0.555	-68.4
2.0		0.560	-74.7
2.1		0.558	-79.5
2.2		0.564	-84.9
2.3		0.570	-90.9
2.4		0.574	-98.3
2.5		0.574	-107.9
2.6		0.564	-118.3
2.7		0.530	-131.4
2.8		0.476	-144.6
2.9		0.411	-159.1
3.0		0.331	-175.8

UPB1506GV

S22 vs. OUTPUT FREQUENCY
Divide by 64 mode, Vcc = 5.0 V

S22 Z
REF 1.0 Units
1 200.0 mUnits/
▽ 171.22 Ω -04.438 Ω
hp

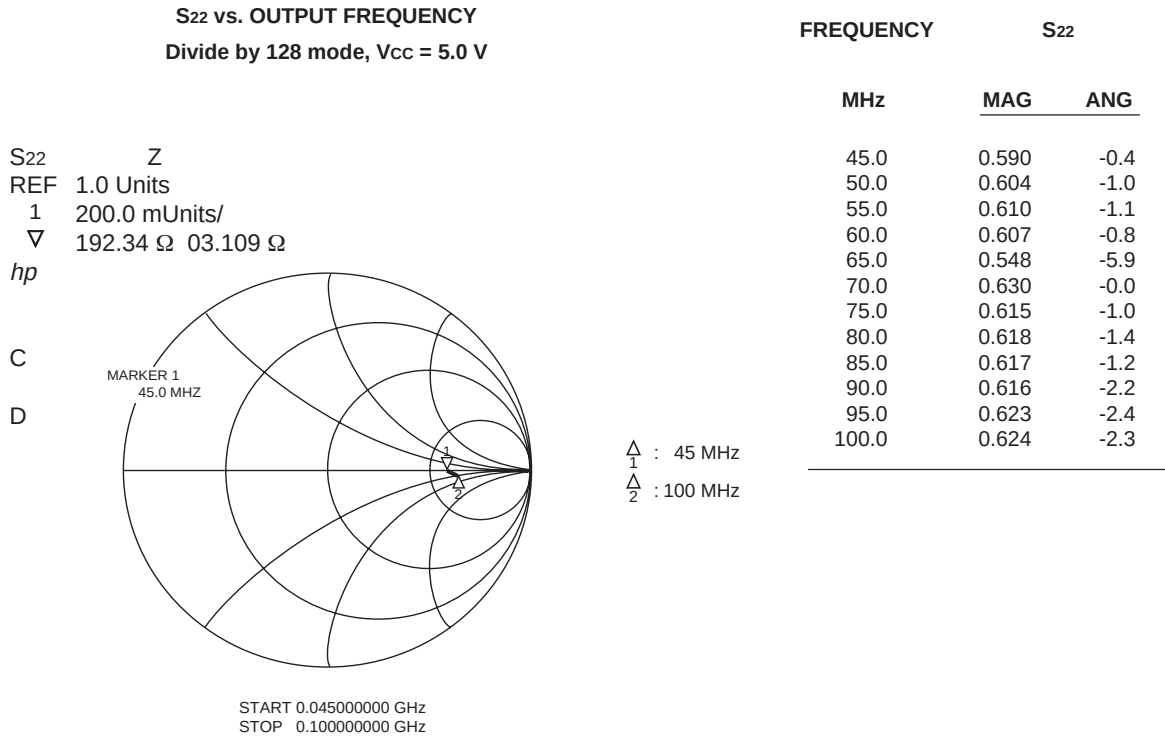


Δ₁ : 45 MHz
Δ₂ : 100 MHz

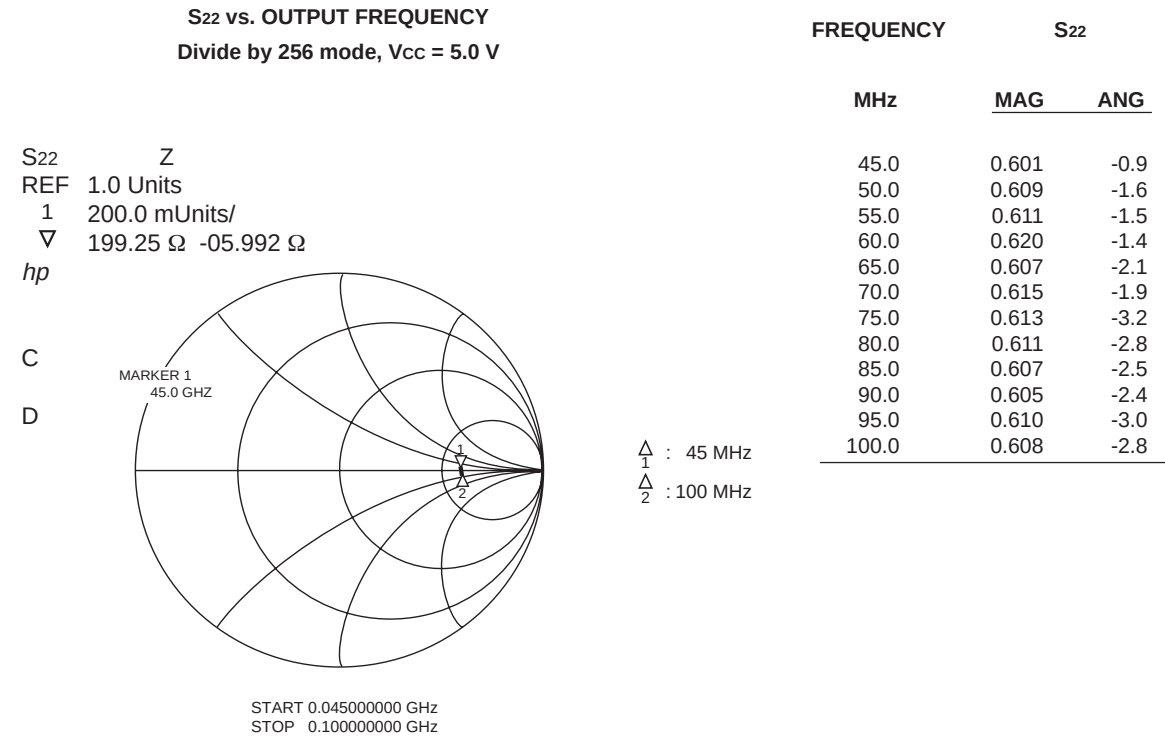
FREQUENCY		S22	
MHz		MAG	ANG
45.0		0.542	-1.4
50.0		0.602	-0.3
55.0		0.616	0.0
60.0		0.605	1.1
65.0		0.609	0.7
70.0		0.616	0.3
75.0		0.620	0.1
80.0		0.622	0.0
85.0		0.619	0.6
90.0		0.610	0.9
95.0		0.626	-0.7
100.0		0.623	-1.7

TYPICAL SCATTERING PARAMETERS (TA = 25°C)

UPB1506GV

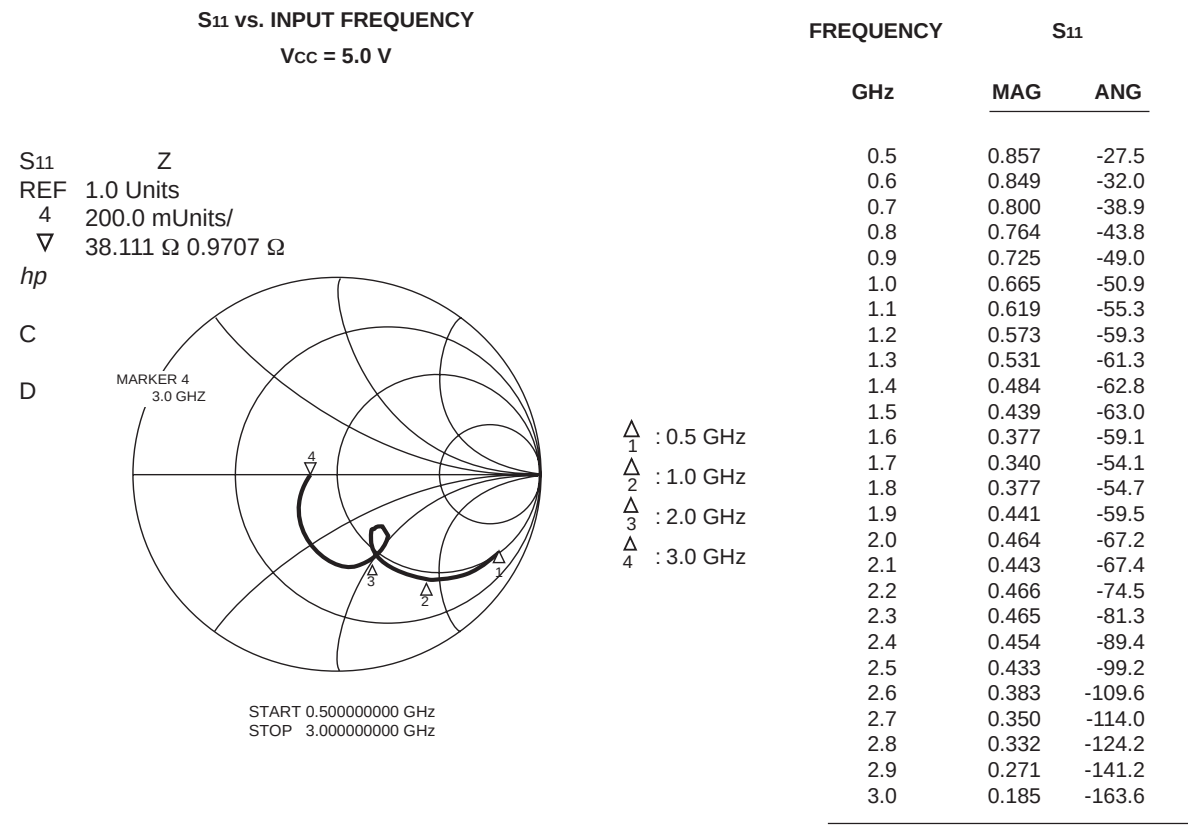


UPB1506GV

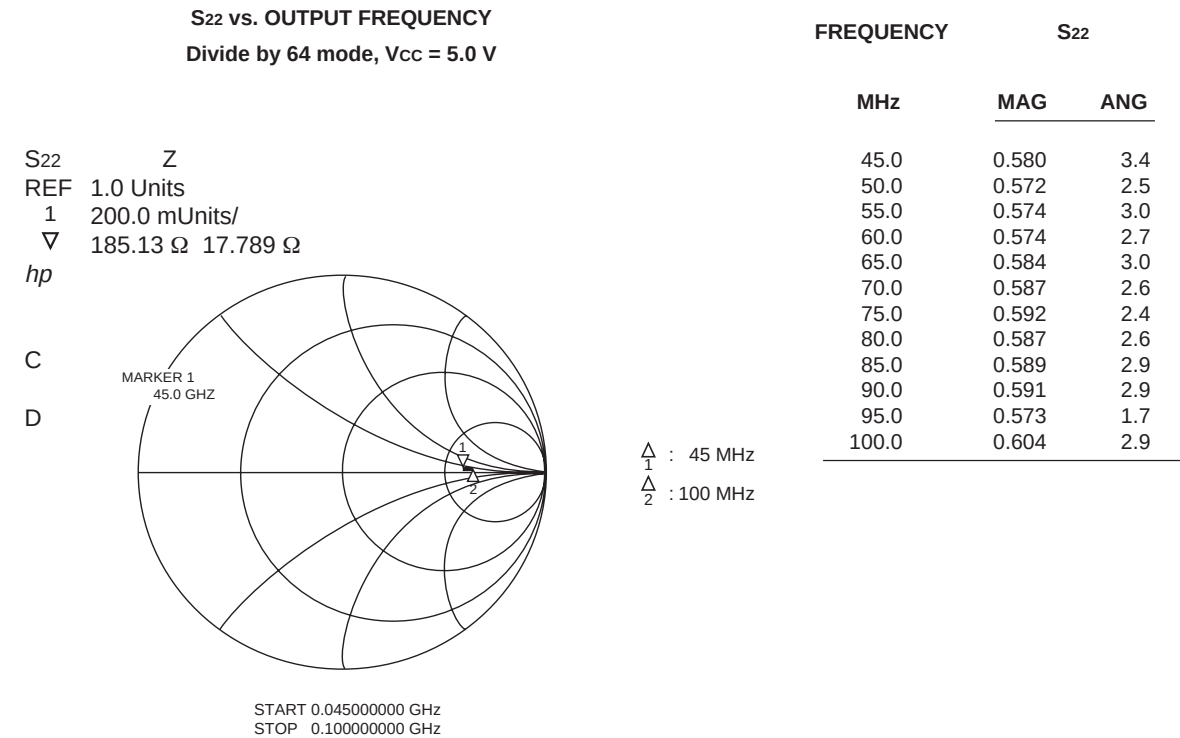


TYPICAL SCATTERING PARAMETERS (TA = 25°C)

UPB1507GV



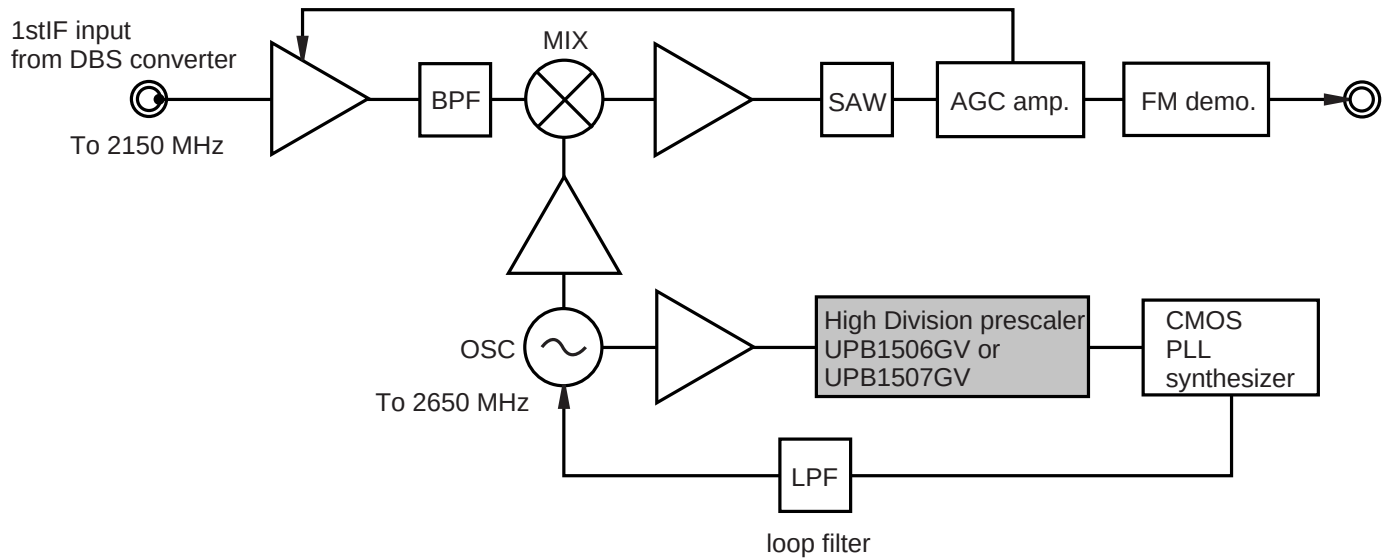
UPB1507GV



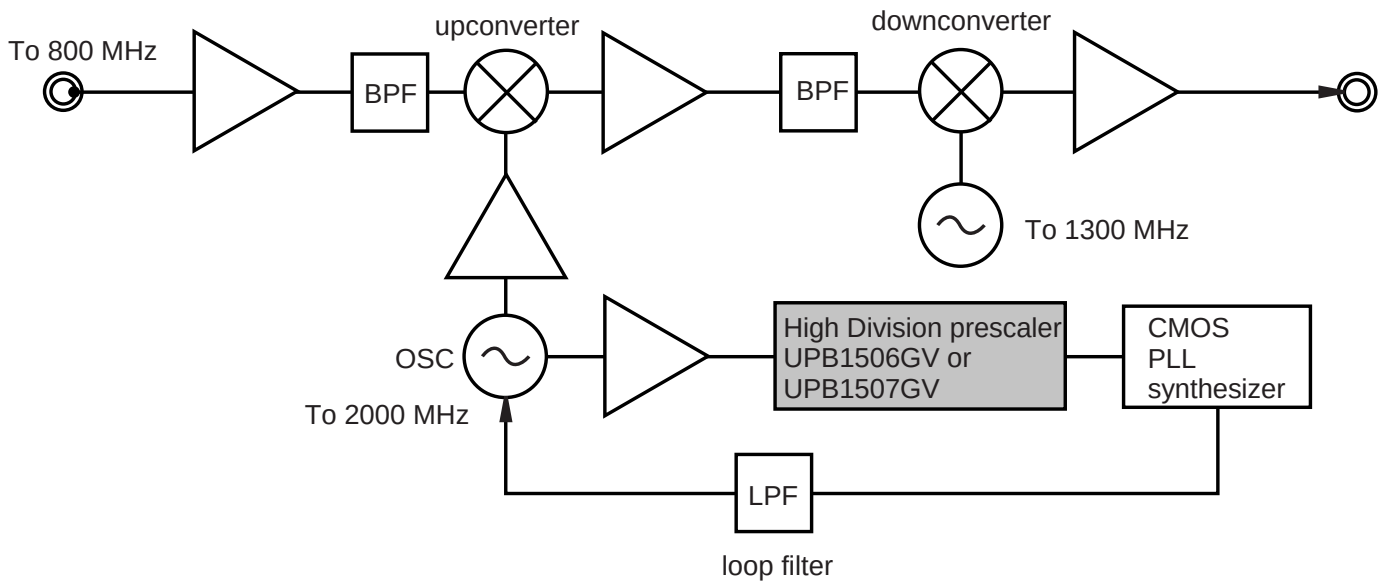
FREQUENCY	S22	
MHz	MAG	ANG
45.0	0.580	3.0
50.0	0.572	2.8
55.0	0.571	2.9
60.0	0.576	2.9
65.0	0.585	3.2
70.0	0.590	2.8
75.0	0.589	2.5
80.0	0.590	2.6
85.0	0.588	2.9
90.0	0.597	2.9
95.0	0.600	3.1
100.0	0.601	3.1

SYSTEM APPLICATION EXAMPLE

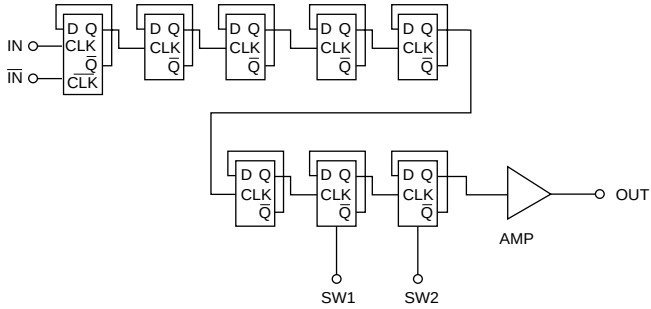
RF unit block of Analog DBS tuners



RF unit block of Analog CATV converter

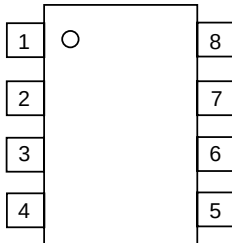
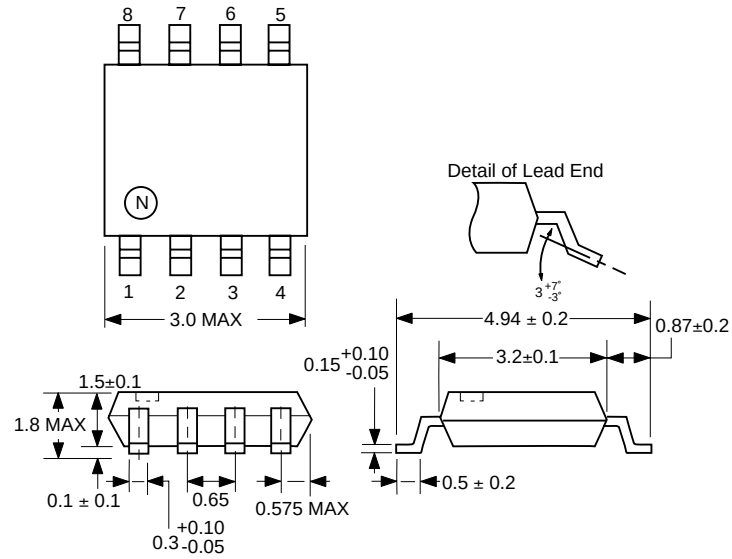


INTERNAL BLOCK DIAGRAM



OUTLINE DIMENSIONS (Units in mm)

PACKAGE OUTLINE S08



PIN CONNECTIONS

Pin No.	UPB1506GV	UPB1507GV
1	SW1	IN
2	IN	Vcc
3	IN-bar	SW1
4	GND	OUT
5	OPEN	GND
6	SW2	SW2
7	OUT	OPEN
8	Vcc	IN-bar

ORDERING INFORMATION

PART NUMBER	QUANTITY	MARKING
UPB1506GV-E1	1000/Reel	1506
UPB1507GV-E1	1000/Reel	1507

Note:

1. Embossed tape 8 mm wide.
Pin 1 is in the tape pull-out direction.

Life Support Applications

These NEC products are not intended for use in life support devices, appliances, or systems where the malfunction of these products can reasonably be expected to result in personal injury. The customers of CEL using or selling these products for use in such applications do so at their own risk and agree to fully indemnify CEL for all damages resulting from such improper use or sale.

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