AN2011S

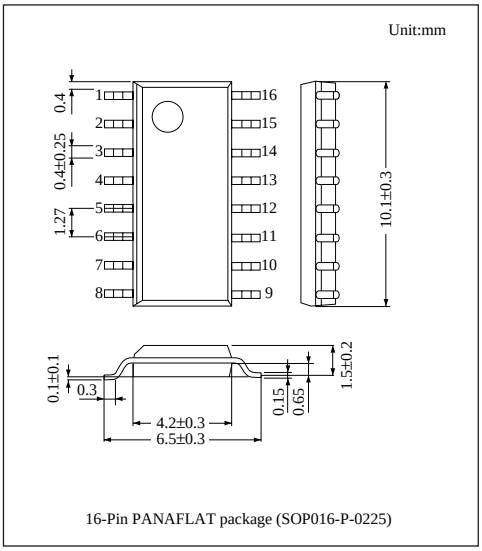
Correlated Double Sampling IC

■ Overview

The AN2011S is an integrated circuit that outputs video-signal made from output-signal of CCD image-element by correlated double sampling.

■ Features

- Available for CCD of up to 768H by using the high-speed sampling circuit
- The sampling pulse corresponds to CMOS level.
- The gain is fixed to 6dB.
- Compatible with the AN2010S



16-Pin PANAFLAT package (SOP016-P-0225)

■ Block Diagram

