



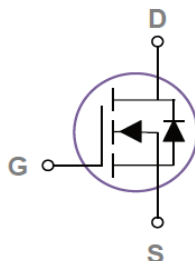
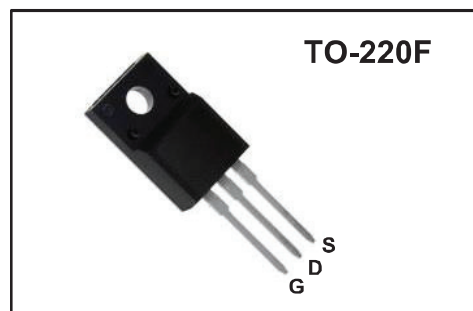
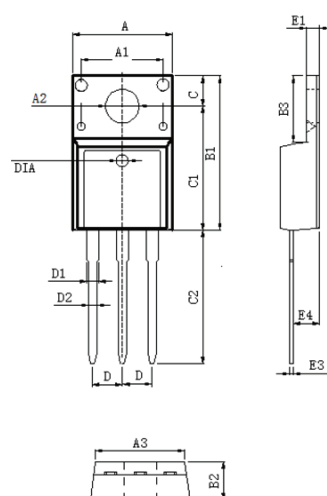
N-Channel Enhancement Mode MOSFET

Features

- ◆ $V_{DS} = 650V$
- ◆ $R_{DS(ON)}$ Typ. 2.2Ω @ $V_{GS} = 10V$
- ◆ Fully Avalanche Rated
- ◆ Pb Free & RoHS Compliant

Applications

- ◆ Backlighting
- ◆ Power Converters
- ◆ Synchronous Rectifiers



Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Ratings	Unit
Drain Source Voltage	V_{DS}	650	V
Gate Source Voltage	V_{GS}	± 30	V
Drain Current Continuous @ $T_c = 25^\circ C$ @ $T_c = 100^\circ C$	I_D	4 2.5	A
Drain Current Pulsed @ $T_c = 25^\circ C$	I_{DM}	16	A
Single Pulse Avalanche Energy	EAS	245	mJ
Single Pulse Avalanche Current	IAS	4	A
Maximum Power Dissipation	P_D	28	W
Storage Temperature Range	T_{STG}	-55 to +150	$^\circ C$
Operating Junction Temperature Range	T_J	-55 to +150	$^\circ C$
Thermal Resistance Junction to Case ^{Note3}	$R_{\theta Jc}$	4.4	$^\circ C/W$

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	10.460	9.860	0.412	0.388
A1	7.100	6.900	0.280	0.272
A2	3.500	3.100	0.138	0.122
A3	9.900	9.500	0.390	0.374
B1	16.170	15.570	0.637	0.613
B2	4.900	4.500	0.193	0.177
B3	6.880	6.480	0.271	0.255
C	3.500	3.100	0.138	0.122
C1	12.870	12.270	0.507	0.483
C2	13.380	12.580	0.527	0.495
D	2.590	2.490	0.102	0.098
D1	1.470	1.070	0.058	0.042
D2	0.900	0.700	0.035	0.028
E1	2.740	2.340	0.108	0.092
E3	0.600	0.400	0.024	0.016
E4	2.960	2.560	0.117	0.101
DIA	$\Phi 1.5$ TYP.	deep 0.1 TYP.	$\Phi 0.059$ TYP.	deep 0.004 TYP.



Electrical Characteristics @ T_J=25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
OFF Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V , I _{DS} =250uA	650	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} =0V , V _{DS} =650V	-	-	1	uA
Gate-Body Leakage	I _{GSS}	V _{GS} =±30V , V _{DS} =0V	-	-	±100	nA
ON Characteristics						
Gate Threshold Voltage	V _{TH}	V _{DS} =V _{GS} , I _{DS} =250uA	2.5	3.5	4.5	V
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V , I _{DS} =2A	-	2.2	2.6	Ω
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V	-	518	900	pF
Output Capacitance	C _{oss}	V _{GS} =0V	-	41.6	80	
Reverse Transfer Capacitance	C _{rss}	Freq.=1MHz	-	4.5	9	
Switching Characteristics						
Turn-On Delay Time	T _{d(on)}	V _{DD} =325V	-	15.4	30	ns
Rise Time	T _r	V _{GS} =10V	-	14.8	29	
Turn-Off Delay Time	T _{d(off)}	R _G =10Ω	-	43.7	65	
Fall Time	T _f	I _{DS} =4A	-	10.5	20	
Total Gate Charge	Q _g	V _{DS} =520V	-	15.4	30	nC
Gate to Source Charge	Q _{gs}	V _{GS} =10V	-	2	4	
Gate to Drain Charge	Q _{gd}	I _{DS} =4A	-	7.3	14	
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V , I _S =1A	-	-	1	V

Notes:

1. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.

2. V_{DD}=50V, V_{GS}=10V, L=30mH, I_{AS}=4A, R_G=25Ω, Starting T_J=25°C

3. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.

R_{θJC} is guaranteed by design while R_{θCA} is determined by the user's board design. R_{θJA} shown below for single device operation on FR-4 in still air.



Typical Characteristics

Fig.1 Continuous Drain Current vs. T_c

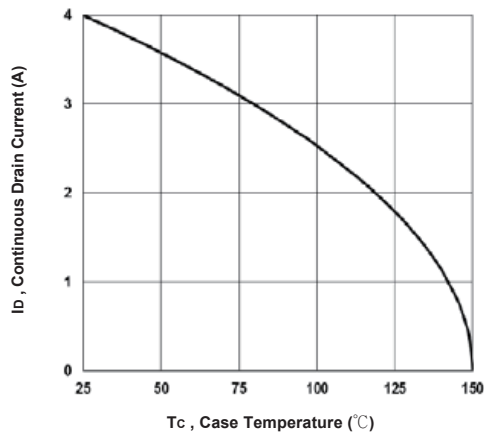


Fig.2 Normalized $R_{DS(on)}$ vs. T_J

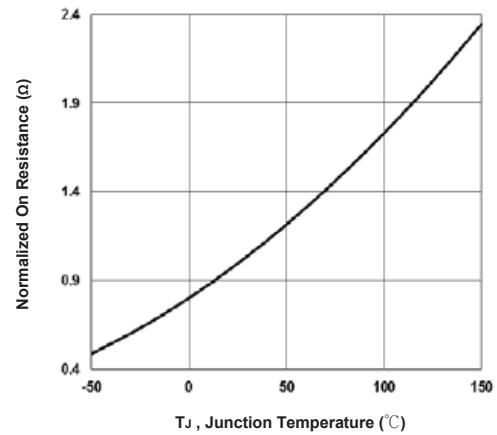


Fig.3 Normalized V_{th} vs. T_J

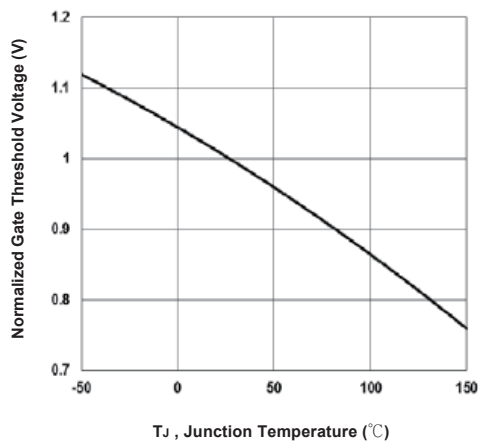


Fig.4 Gate Charge Waveform

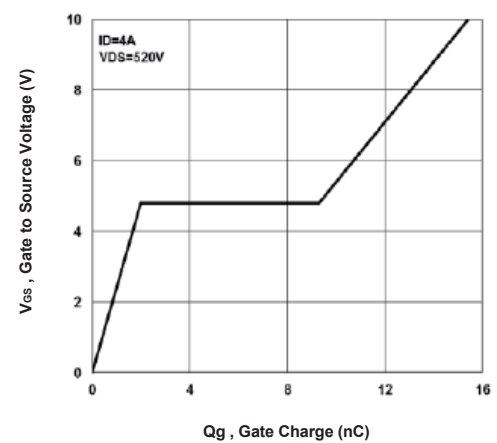


Fig.5 Normalized Transient Impedance

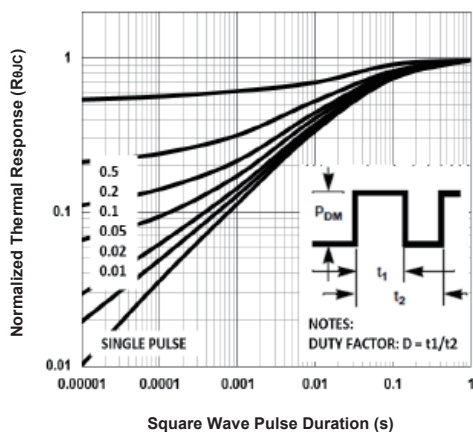
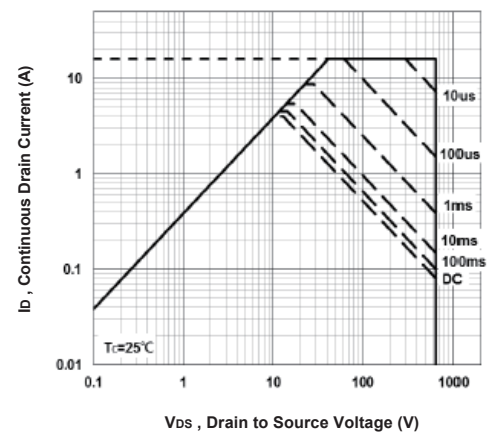


Fig.6 Maximum Safe Operation Area





Typical Characteristics

Fig.7 Switching Time Waveform

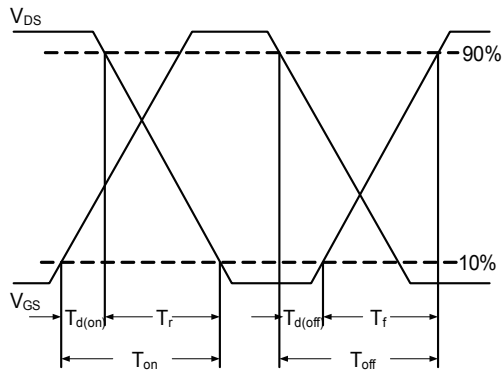


Fig.8 EAS Waveform

