

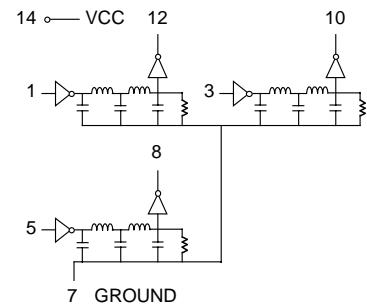
14 Pin DIP High Precision Triple Output TTL Compatible Active Delay Lines

DELAY TIME (nS)	PART NUMBER	DELAY TIME (nS)	PART NUMBER	DELAY TIME (nS)	PART NUMBER
5±1	EPA1825-5	19±1	EPA1825-19	65±2.5	EPA1825-65
6±1	EPA1825-6	20±1	EPA1825-20	70±2.5	EPA1825-70
7±1	EPA1825-7	21±1	EPA1825-21	75±2.5	EPA1825-75
8±1	EPA1825-8	22±1	EPA1825-22	80±2.5	EPA1825-80
9±1	EPA1825-9	23±1	EPA1825-23	85±3	EPA1825-85
10±1	EPA1825-10	24±1	EPA1825-24	90±3	EPA1825-90
11±1	EPA1825-11	25±1	EPA1825-25	95±3	EPA1825-95
12±1	EPA1825-12	30±1.5	EPA1825-30	100±3	EPA1825-100
13±1	EPA1825-13	35±1.5	EPA1825-35	125±4	EPA1825-125
14±1	EPA1825-14	40±1.5	EPA1825-40	150±4.5	EPA1825-150
15±1	EPA1825-15	45±2	EPA1825-45	175±5	EPA1825-175
16±1	EPA1825-16	50±2	EPA1825-50	200±6	EPA1825-200
17±1	EPA1825-17	55±2	EPA1825-55	225±7	EPA1825-225
18±1	EPA1825-18	60±2	EPA1825-60	250±8	EPA1825-250

Delay Times referenced from input to leading edges at 25°C, 5.0V, with no load.

DC Electrical Characteristics		Test Conditions	Min	Max	Unit
Parameter					
V _{OH}	High-Level Output Voltage	V _{CC} = min. V _{IL} = max. I _{OH} = max V _{CC} = min. V _{IH} = min. I _{OL} = max	2.7	0.5	V
V _{OL}	Low-Level Output Voltage				
V _{IK}	Input Clamp Voltage	V _{CC} = min. I _I = I _{IK}		-1.2V	V
I _{IH}	High-Level Input Current	V _{CC} = max. V _{IN} = 2.7V		50	µA
		V _{CC} = max. V _{IN} = 5.25V		1.0	mA
I _{IL}	Low-Level Input Current	V _{CC} = max. V _{IN} = 0.5V		-2	mA
I _{OS}	Short Circuit Output Current	V _{CC} = max. V _{OUT} = 0. (One output at a time)	-40	-100	mA
I _{CCH}	High-Level Supply Current	V _{CC} = max. V _{IN} = OPEN		115	mA
I _{CCL}	Low-Level Supply Current	V _{CC} = max. V _{IN} = 0		115	mA
T _{RO}	Output Rise Time	T _d ≤ 500 nS (0.75 to 2.4 Volts)		4	nS
N _H	Fanout High-Level Output	V _{CC} = max. V _{OH} = 2.7V		20 TTL LOAD	
N _L	Fanout Low-Level Output	V _{CC} = max. V _{OL} = 0.5V		10 TTL LOAD	

Schematic

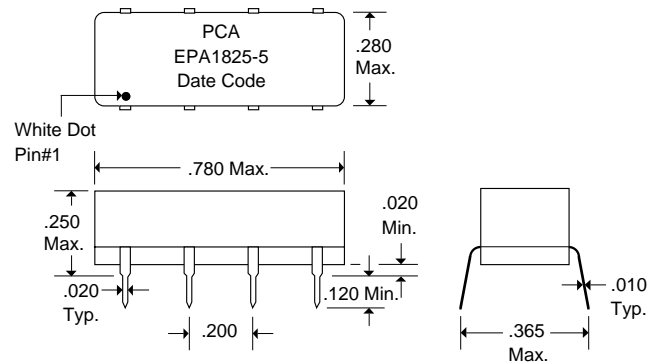


Recommended Operating Conditions		Min	Max	Unit
V _{CC}	Supply Voltage	4.75	5.25	V
V _{IH}	High-Level Input Voltage	2.0		V
V _{IL}	Low-Level Input Voltage		0.8	V
I _{IK}	Input Clamp Current		-18	mA
I _{OH}	High-Level Output Current		-1.0	mA
I _{OL}	Low-Level Output Current		20	mA
PW*	Pulse Width of Total Delay	40		%
d*	Duty Cycle		40	%
T _A	Operating Free-Air Temperature	0	+70	°C

*These two values are inter-dependent.

Input Pulse Test Conditions @ 25° C			Unit
E _{IN}	Pulse Input Voltage	3.2	Volts
PW	Pulse Width % of Total Delay	110	%
TRI	Pulse Rise Time (0.75 - 2.4 Volts)	2.0	nS
PRR	Pulse Repetition Rate @ T _d ≤ 200 nS	1.0	MHz
	Pulse Repetition Rate @ T _d > 200 nS	100	KHz
V _{CC}	Supply Voltage	5.0	Volts

Package Dimensions



DSA1825 8/25/94

QAFC-SO1 Rev. B 8/25/94

Unless Otherwise Noted Dimensions in Inches
Tolerances:
Fractional = ± 1/32
.XX = ± .030 .XXX = ± .010



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