20Amp. Schottky Barrier Rectifiers

MBR20200E3

$I_{F(AV)}$	2 × 10A
V_{RRM}	200V
Tj	175°C
V_F	0.68V

Features

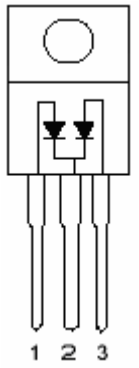
- 175℃ operating junction temperature
- Low V_F and low I_R type
- Metal silicon junction, major carrier conduction
- 20A total (10A per diode leg)
- Guardring for over voltage protection
- Low power loss, high efficiency
- High surge capability
- High temperature soldering guaranteed : 260℃/10s, 0.25”(6.35mm) from case
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection application
- RoHS compliant package

Mechanical Data

- Case: JEDEC TO-220AB molded plastic
- Mounting Position: Any
- Weight: 0.08 ounce, 2.24 grams
- Terminals: Pure tin plated, lead-free, solderable per MIL-STD-750 method 2026
- Epoxy: UL 94V-0 rate flame retardant
- Mounting Torque : 5 in-lbs max

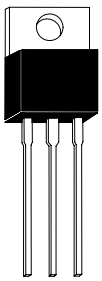
Equivalent Circuit

MBR20200E3



Outline

TO-220AB



A K A

Maximum Ratings and Electrical Characteristics (Per Diode Leg)

(Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.)

Parameter		Symbol	Min.	Typ.	Max.	Units
Maximum Recurrent peak reverse voltage		V_{RRM}			200	V
Maximum RMS voltage		V_{RMS}			140	V
Maximum DC blocking voltage		V_{DC}			200	V
Maximum instantaneous forward voltage at (Note 1)	I_F=10A, T_C=25℃	V_F			0.90	V
	I_F=10A, T_C=125℃			0.68	0.80	
	I_F=20 A, T_C=25℃				1.00	
	I_F=20A, T_C=125℃				0.90	
Maximum Average forward rectified current @ T_C=161℃	Per Diode	$I_{F(AV)}$			10	A
	Per Device				20	
Peak repetitive forward current (square wave, 20kHz, T_C=158℃)		I_{FRM}			20	A
Non-repetitive peak forward surge current @ 8.3ms single half sine wave superimposed on rated load (JEDEC method)		I_{FSM}			150	A
Peak repetitive reverse surge current (Note 1), T_J<175℃		I_{RRM}			2.5	A
Maximum instantaneous reverse current at	V_R=200 V, T_C=25℃	I_R			10	μA
	V_R=200 V, T_C=125℃				5	mA
Voltage rate of change, (rated V_R)		dV/dt			10,000	V/μs
Typical junction capacitance @ f=1MHz and applied 5V reverse voltage		C_J		130		pF
ESD susceptibility (Note 2)					8000	V
Storage temperature range		Tstg	-65		+175	℃
Operating junction temperature range		T_J	-65		+175	℃

Notes : 1. 2.0μs pulse width, f=1.0kHz
2. Human body model, 1.5kΩ in series with 100pF

Thermal Data

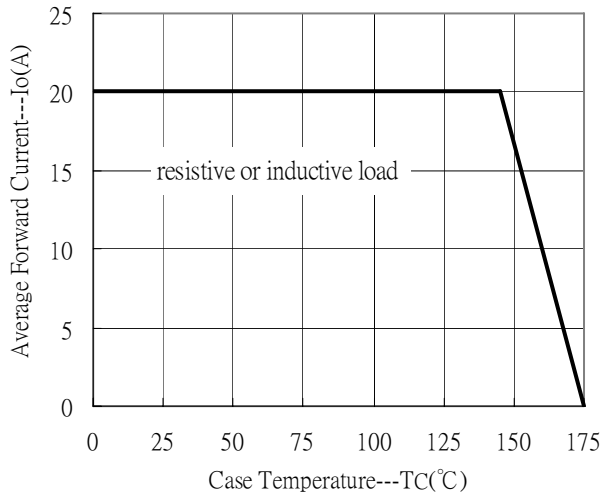
Parameter	Symbol	Value	Unit
Maximum Thermal Resistance, Junction-to-case	$R_{th,j-c}$	2	°C/W
Maximum Thermal Resistance, Junction-to-ambient	$R_{th,j-a}$	60	°C/W

Ordering Information

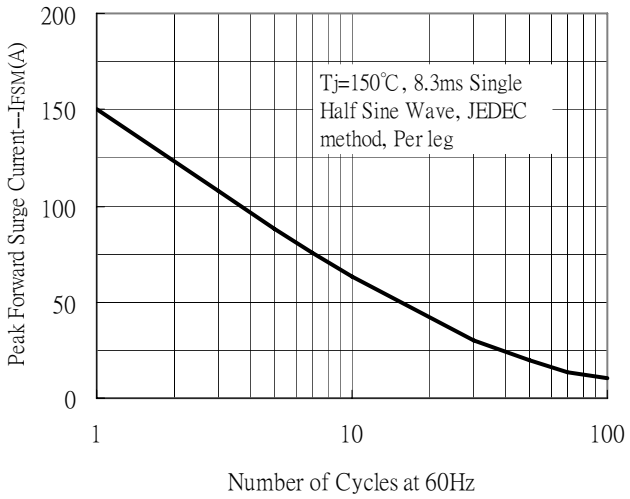
Device	Package	Shipping	Marking
MBR20200E3	TO-220AB (RoHS compliant package)	50 pcs / Tube, 40 Tubes/Box	20200

Characteristic Curves

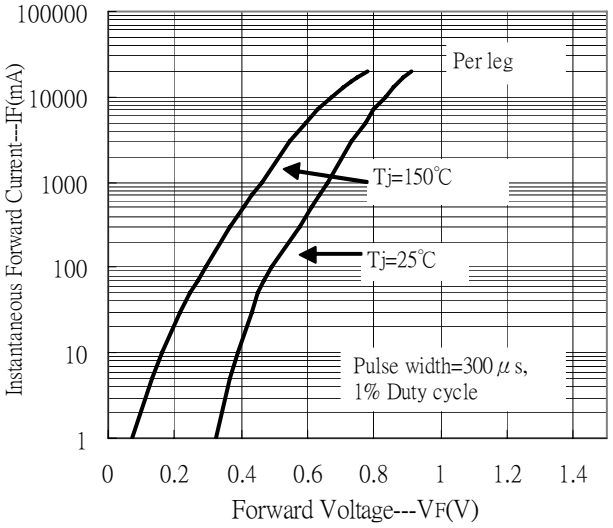
Forward Current Derating Curve



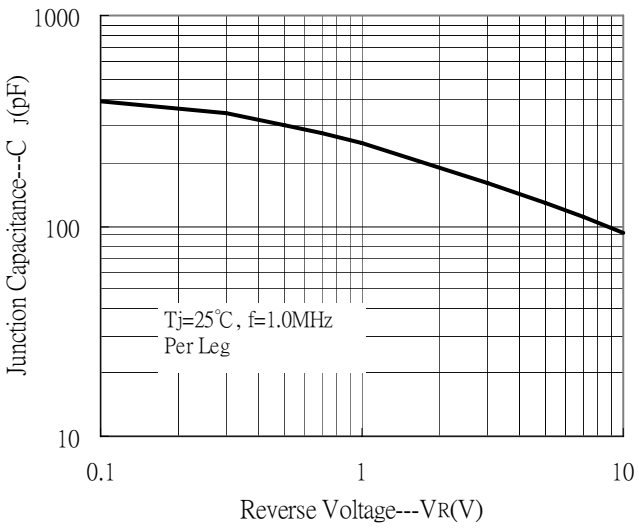
Maximum Non-Repetitive Forward Surge Current



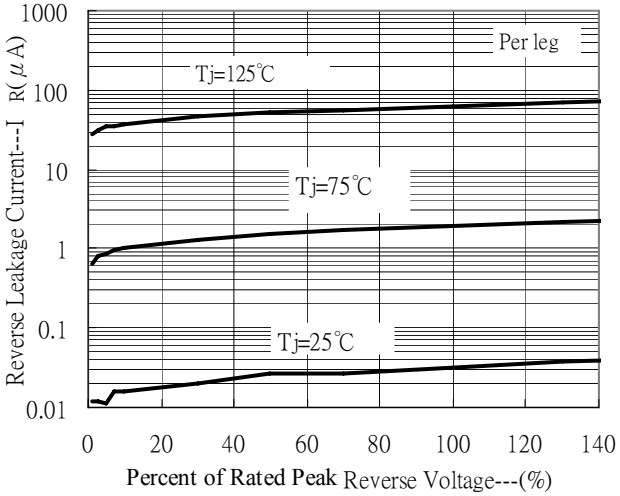
Forward Current vs Forward Voltage



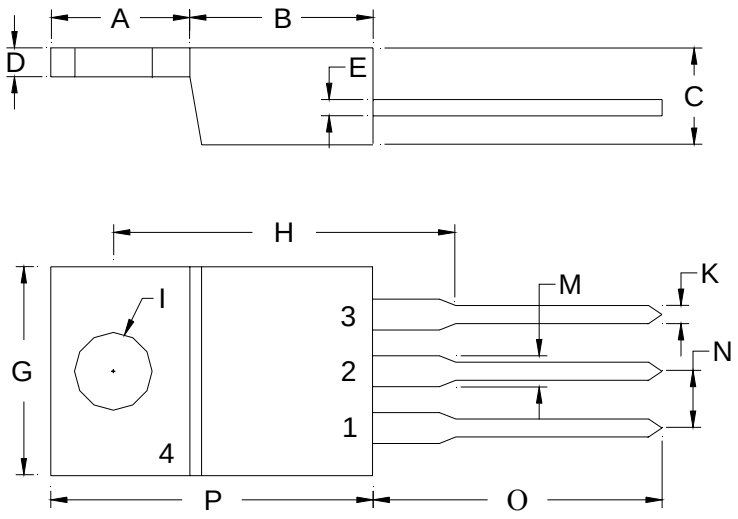
Junction Capacitance vs Reverse Voltage



Reverse Leakage Current vs Reverse Voltage

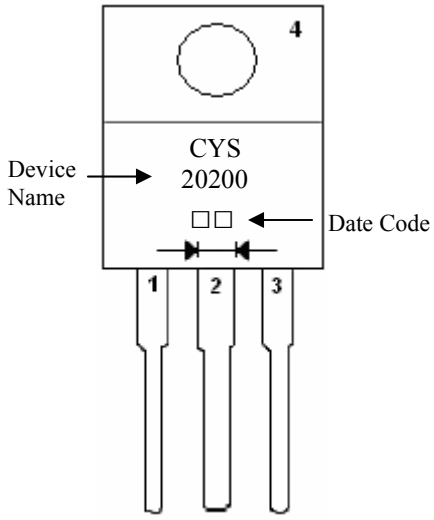


TO-220AB Dimension



3-Lead TO-220AB Plastic Package
CYStek Package Code: E3

Marking:



Style: Pin 1.Anode 2.Cathode 3.Anode

*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.2197	0.2949	5.58	7.49	I	-	*0.1508	-	*3.83
B	0.3299	0.3504	8.38	8.90	K	0.0295	0.0374	0.75	0.95
C	0.1732	0.185	4.40	4.70	M	0.0449	0.0551	1.14	1.40
D	0.0453	0.0547	1.15	1.39	N	-	*0.1000	-	*2.54
E	0.0138	0.0236	0.35	0.60	O	0.5000	0.5618	12.70	14.27
G	0.3803	0.4047	9.66	10.28	P	0.5701	0.6248	14.48	15.87
H	-	*0.6398	-	*16.25					

Notes: 1.Controlling dimension: millimeters.
2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: KFC ; tin plated
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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