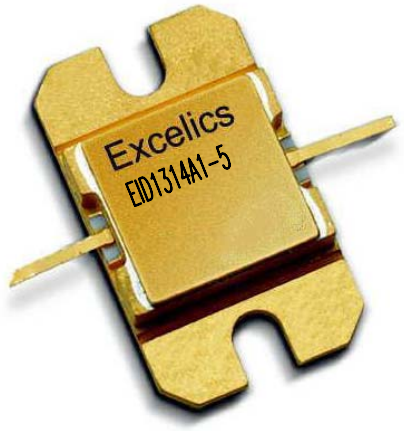
EID1314A1-5

UPDATED 07/12/2007

13.75-14.50 GHz 5-Watt Internally-Matched Power FET

FEATURES

- **13.75-14.50 GHz Bandwidth**
- **Input/Output Impedance Matched to 50 Ohms**
- **+37.5 dBm Output Power at 1dB Compression**
- **7.5 dB Power Gain at 1dB Compression**
- **35% Power Added Efficiency**
- **Hermetic Metal Flange Package**
- **100% Tested for DC, RF, and R_{TH}**



Caution! ESD sensitive device.

DESCRIPTION

The EID1314A1-5 is a high power, highly linear, single stage MFET amplifier in a flange mount package. This amplifier features Excelics' unique PHEMT transistor technology.

ELECTRICAL CHARACTERISTICS (T_a = 25°C)

SYMBOL	PARAMETERS/TEST CONDITIONS[1]	MIN	TYP	MAX	UNITS
P_{1dB}	Output Power at 1dB Compression f = 13.75-14.50GHz V_{DS} = 10 V, I_{DSQ} = 1200mA	37.0	37.5		dBm
G_{1dB}	Gain at 1dB Compression f = 13.75-14.50GHz V_{DS} = 10 V, I_{DSQ} = 1200mA	6.5	7.5		dB
ΔG	Gain Flatness f = 13.75-14.50GHz V_{DS} = 10 V, I_{DSQ} = 1200mA			±0.6	dB
PAE	Power Added Efficiency at 1dB Compression V_{DS} = 10 V, I_{DSQ} = 1200mA f = 13.75-14.50GHz		35		%
Id_{1dB}	Drain Current at 1dB Compression f = 13.75-14.50GHz		1400	1800	mA
I_{DSS}	Saturated Drain Current V_{DS} = 3 V, V_{GS} = 0 V		2080	2880	mA
V_P	Pinch-off Voltage V_{DS} = 3 V, I_{DS} = 20 mA		-2.5	-4.0	V
R_{TH}	Thermal Resistance[2]		5.5	6.0	°C/W

Notes:
1. Tested with 100 Ohm gate resistor.
2. Overall Rth depends on case mounting.

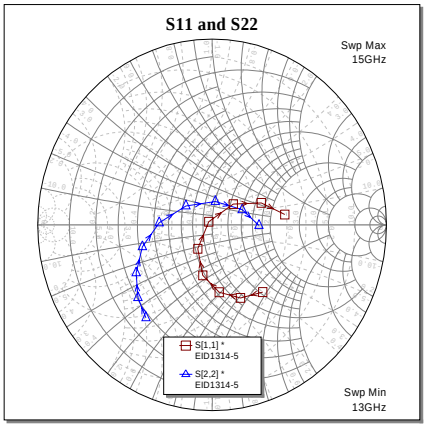
ABSOLUTE MAXIMUM RATINGS FOR CONTINUOUS OPERATION[1,2]

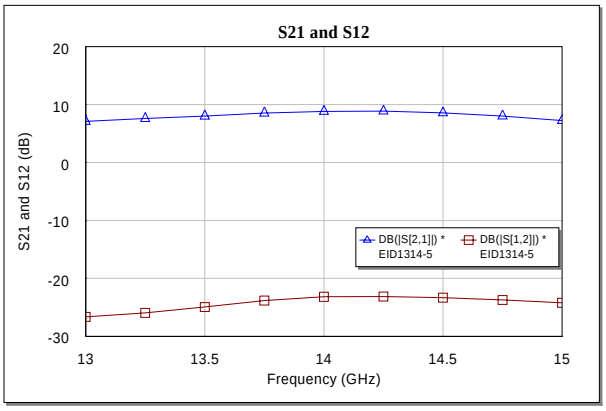
SYMBOL	CHARACTERISTIC	VALUE
V_{DS}	Drain to Source Voltage	10 V
V_{GS}	Gate to Source Voltage	-4.5 V
I_{DS}	Drain Current	IDSS
I_{GSF}	Forward Gate Current	40 mA
P_{IN}	Input Power	@ 3dB compression
P_T	Total Power Dissipation	23 W
T_{CH}	Channel Temperature	150°C
T_{STG}	Storage Temperature	-65/+150°C

Notes:
1. Operating the device beyond any of the above ratings may result in permanent damage or reduction of MTTF.
2. Bias conditions must also satisfy the following equation $P_T < (T_{CH} - T_{PKG})/R_{TH}$; where T_{PKG} = temperature of package, and $P_T = (V_{DS} * I_{DS}) - (P_{OUT} - P_{IN})$.

PERFORMANCE DATA

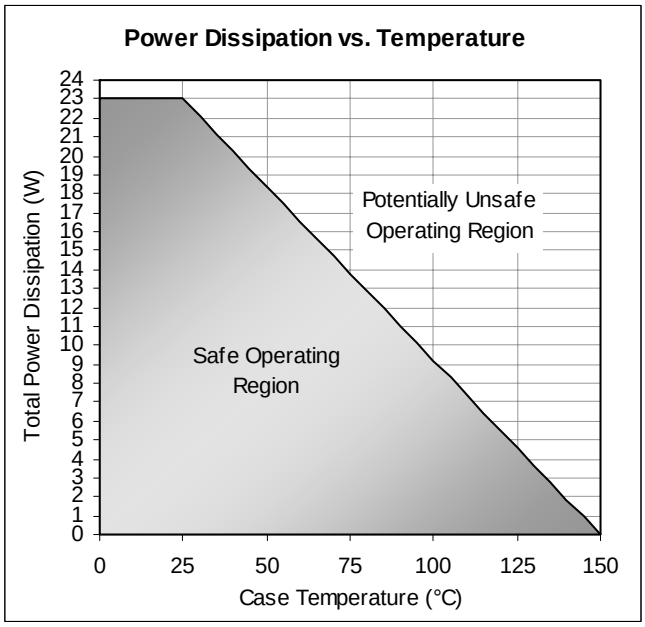
Typical S-Parameters (T= 25°C, 50Ω system, de-embedded to edge of package)
V_{DS} = 10 V, I_{DSQ} = 1200mA



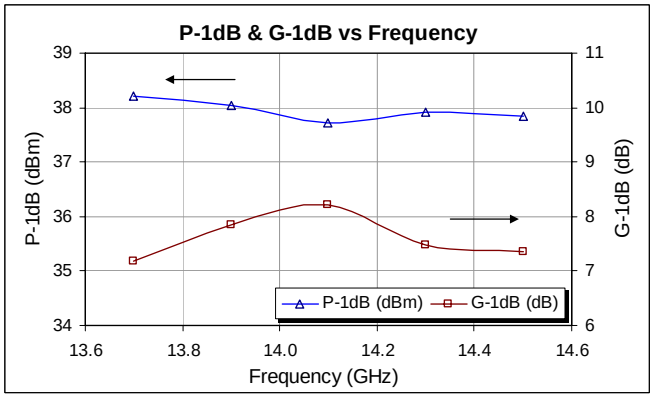


FREQ	--- S11 ---		--- S21 ---		--- S12 ---		--- S22 ---	
(GHz)	MAG	ANG	MAG	ANG	MAG	ANG	MAG	ANG
13.0	0.4637	-51.26	2.2673	-134.00	-0.0465	148.59	0.6281	-127.18
13.2	0.4333	-64.29	2.3733	-147.22	-0.0500	162.65	0.5868	-135.36
13.4	0.3930	-77.23	2.4853	-161.29	-0.0561	176.02	0.5354	-144.04
13.6	0.3380	-90.79	2.5944	-176.44	0.0603	170.24	0.4755	-153.89
13.8	0.2552	-105.20	2.6962	167.96	0.0630	153.33	0.3950	-166.55
14.0	0.1524	-122.48	2.7628	151.12	0.0694	136.76	0.3036	177.97
14.2	0.0411	-169.43	2.7844	133.47	0.0703	119.55	0.2036	152.97
14.4	0.1025	54.46	2.7359	115.60	0.0691	101.22	0.1352	109.08
14.6	0.2214	33.93	2.6150	98.28	0.0693	84.19	0.1428	53.54
14.8	0.3322	20.00	2.4814	81.16	0.0659	66.29	0.2059	19.24
15.0	0.4199	7.64	2.3055	64.97	0.0616	52.11	0.2685	-0.63

Power De-rating Curve



Typical Power Data (V_{DS} = 10 V, I_{DSQ} = 1200 mA)



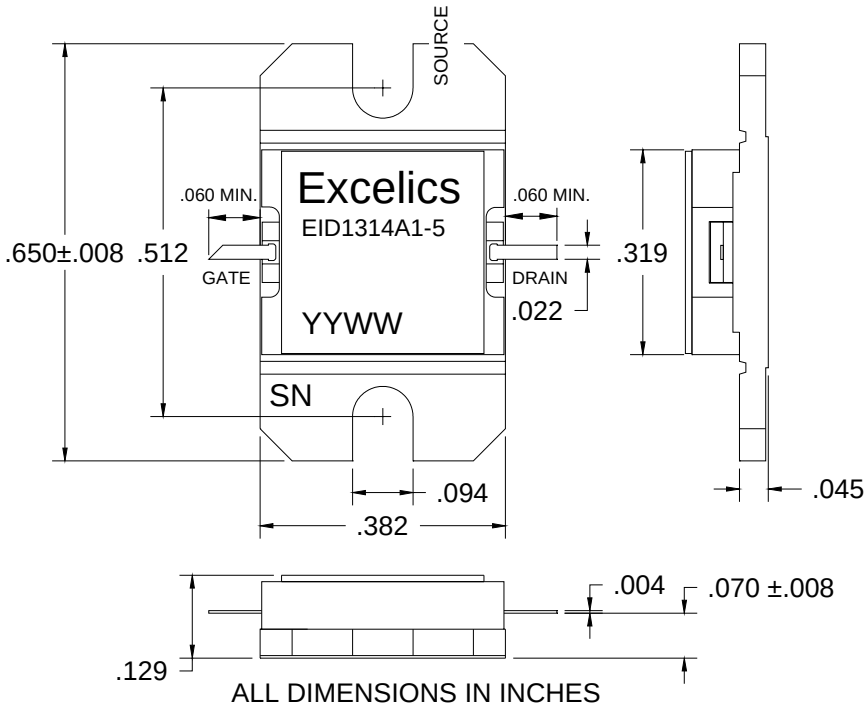
EID1314A1-5

UPDATED 07/12/2007

13.75-14.50 GHz 5-Watt Internally-Matched Power FET

PACKAGE OUTLINE

Dimensions in inches, Tolerance ± .005 unless otherwise specified



ALL DIMENSIONS IN INCHES

ORDERING INFORMATION

Part Number	Grade[1]	f_{Test} (GHz)	P_{1dB} (min)
EID1314A1-5	Industrial	13.75-14.50 GHz	37.0

Notes: 1. Contact factory for military and hi-rel grades.
2. Exact test conditions are specified in "Electrical Characteristics" table.

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness