

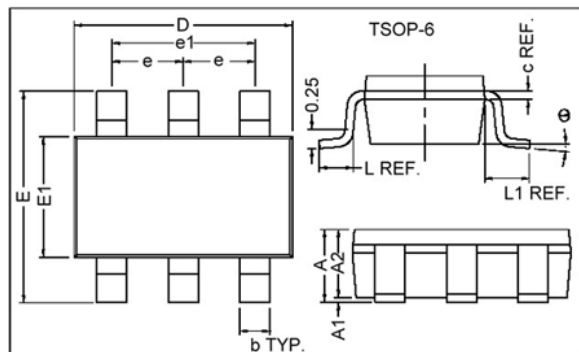
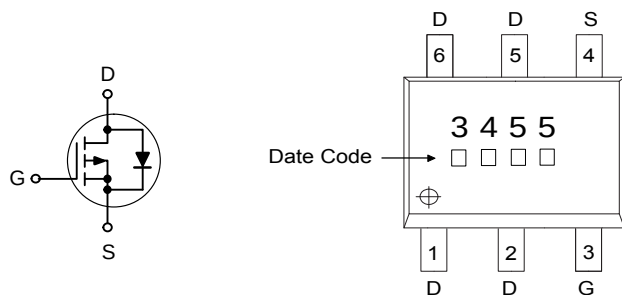
RoHS Compliant Product

Description

The STT3455 utilized advance processing techniques to achieve the lowest possible on-resistance, extremly efficient and cost-effectiveness device. The STT3455 is universally used for all commercial-industrial applications.

Features

- * Fast Switching Characteristic
- * Lower Gate Charge
- * Small Footprint & Low Profile Package



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	1.10	MAX.	L	0.45	REF.
A1	0	0.10	L1	0.60	REF.
A2	0.70	1.00		0°	10°
c	0.12	REF.	b	0.30	0.50
D	2.70	3.10	e	0.95	REF.
E	2.60	3.00	e1	1.90	REF.
E1	1.40	1.80			

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ³	$I_D@T_A=25$	-4.0	A
Continuous Drain Current ³	$I_D@T_A=70$	-3.3	A
Pulsed Drain Current ¹	I_{DM}	-20	A
Total Power Dissipation	$P_D@T_A=25$	2.0	W
Linear Derating Factor		0.016	W/°C
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Ratings	Unit
Thermal Resistance Junction-ambient ³	R_{thj-a}	62.5	°C/W

Electrical Characteristics($T_j=25^{\circ}\text{C}$ Unless otherwise specified)

Parameter	Symbd	Min.	Typ.	Max.	Unit	Test Condition
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	$V_{GS}=0V, I_D=-250\mu A$
Breakdown Voltage Temp. Coefficient	$\Delta BV_{DS}/\Delta T_j$	—	-0.02	—	V/ $^{\circ}\text{C}$	Reference to $25^{\circ}\text{C}, I_D=-1mA$
Gate Threshold Voltage	$V_{GS(th)}$	-1.0	—	-3.0	V	$V_{DS}=V_{GS}, I_D=-250\mu A$
Gate-Source Leakage Current	I_{GSS}	—	—	± 100	nA	$V_{GS}=\pm 20V$
Drain-Source Leakage Current ($T_j=25^{\circ}\text{C}$)	I_{DSS}	—	—	-1	μA	$V_{DS}=-30V, V_{GS}=0$
Drain-Source Leakage Current ($T_j=55^{\circ}\text{C}$)		—	—	-25	μA	$V_{DS}=-24V, V_{GS}=0$
Static Drain-Source On-Resistance ²	$R_{DS(ON)}$	—	—	100	m Ω	$V_{GS}=-10V, I_D=-3.5A$
		—	—	170		$V_{GS}=-4.5V, I_D=-2.7A$
Total Gate Charge ²	Q_g	—	5.5	8.8	nC	$I_D=-4.0A$ $V_{DS}=-24V$ $V_{GS}=-4.5V$
Gate-Source Charge	Q_{gs}	—	1	—		
Gate-Drain ("Miller") Charge	Q_{gd}	—	2.6	—		
Turn-on Delay Time ²	$T_{d(ON)}$	—	7	—	nS	$V_{DD}=-15V$ $I_D=-1A$ $V_{GS}=-10V$ $R_G=3.3\Omega$ $R_D=15\Omega$
Rise Time	T_r	—	6	—		
Turn-off Delay Time	$T_{d(OFF)}$	—	18	—		
Fall Time	T_f	—	4	—		
Input Capacitance	C_{iss}	—	400	640	pF	$V_{GS}=0V$ $V_{DS}=-25V$ $f=1.0MHz$
Output Capacitance	C_{oss}	—	90	—		
Reverse Transfer Capacitance	C_{rss}	—	30	—		
Forward Transconductance	G_{fs}	—	6	—	S	$V_{DS}=-5V, I_D=-4.0A$

Source-Drain Diode

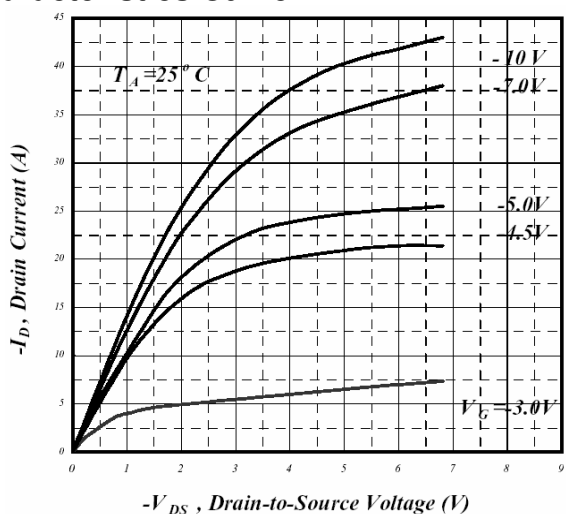
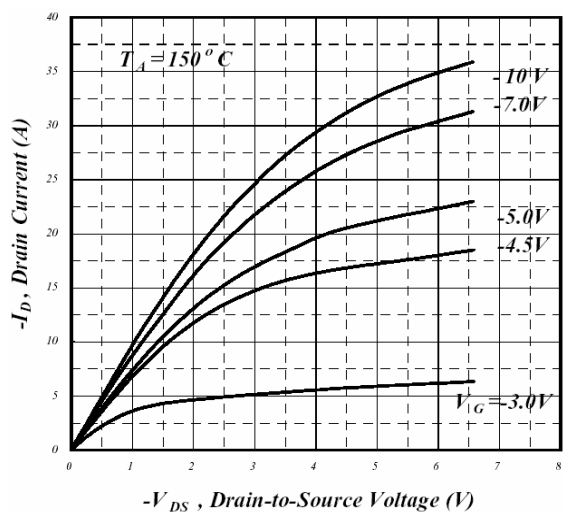
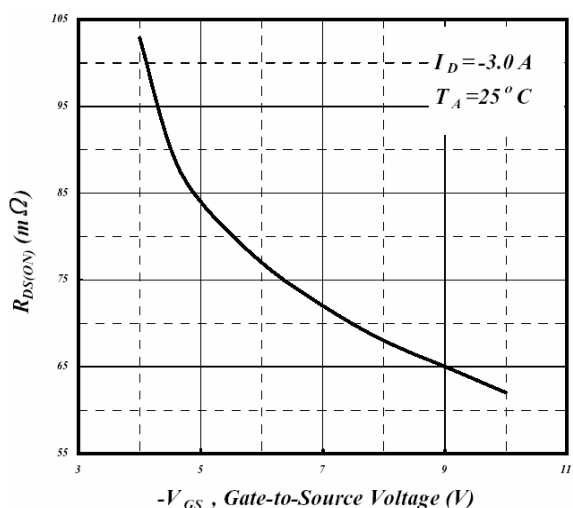
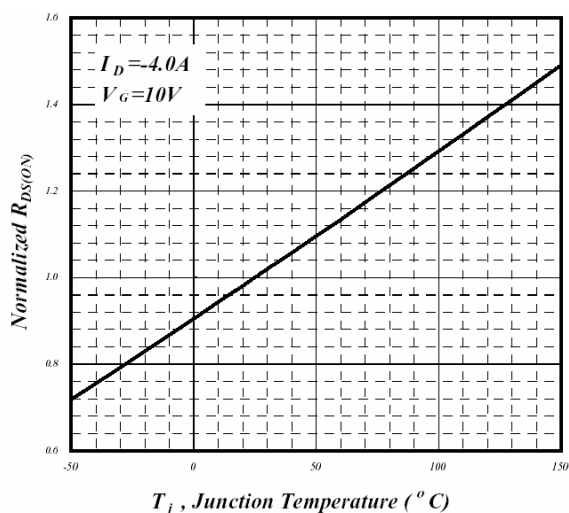
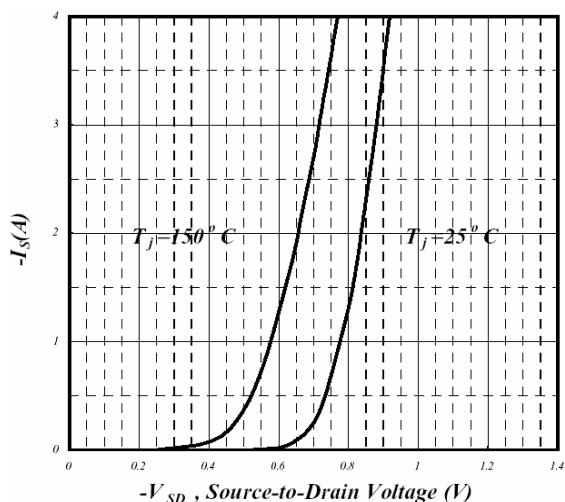
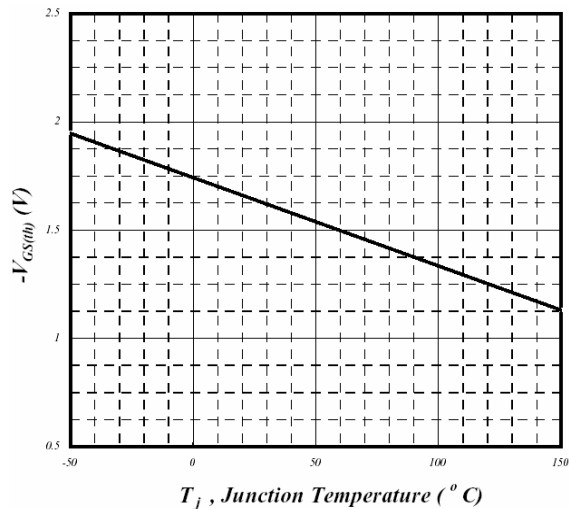
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Forward On Voltage ²	V_{DS}	—	—	-1.2	V	$I_S=-1.6A, V_{GS}=0V.$
Reverse Recovery Time ²	T_{rr}	—	21	—	nS	$I_S=-4.0A, V_{GS}=0V$ $di/dt=100A/\mu s$
Reverse Recovery Charge	Q_{rr}	—	14	—	nC	

Notes: 1.Pulse width limited by safe operating area.

2.Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

3.Surface mounted on 1 in² copper pad of FR4 board; 156 $^{\circ}\text{C}/W$ when mounted on Min. copper pad.

Characteristics Curve



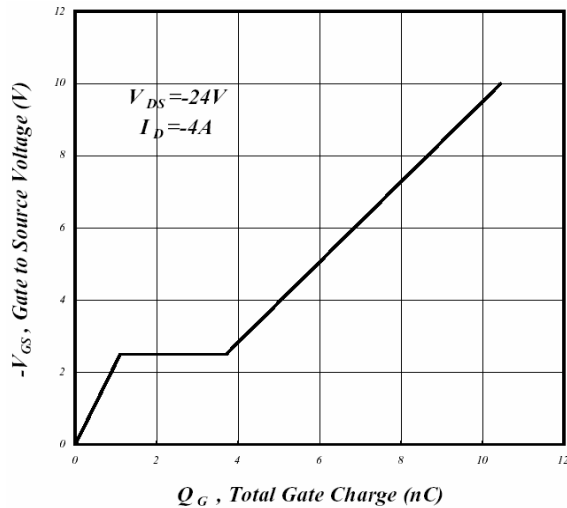


Fig 7. Gate Charge Characteristics

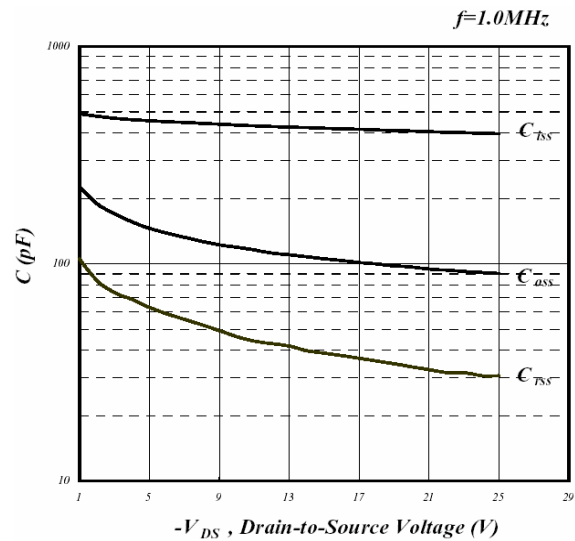


Fig 8. Typical Capacitance Characteristics

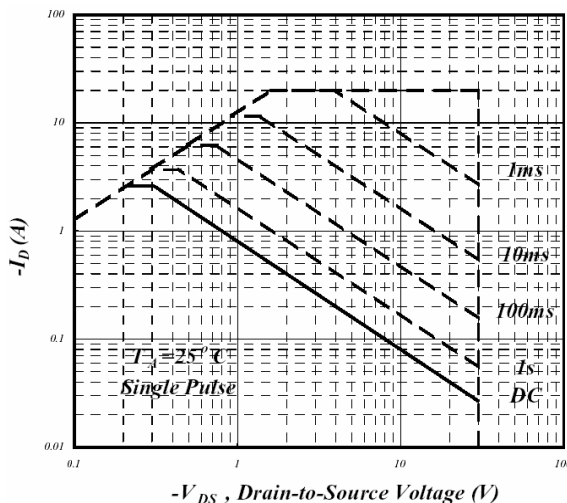


Fig 9. Maximum Safe Operating Area

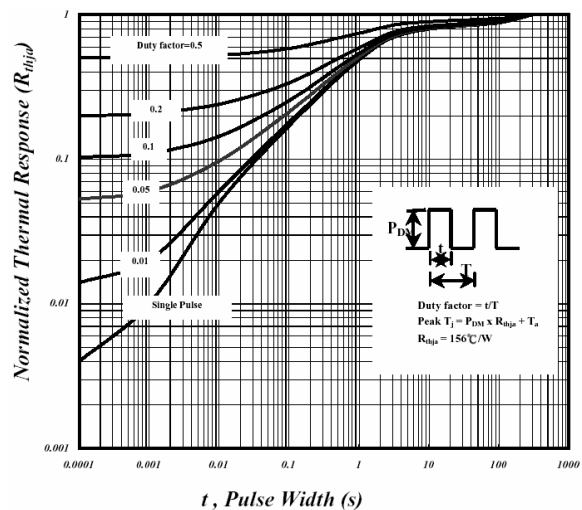


Fig 10. Effective Transient Thermal Impedance

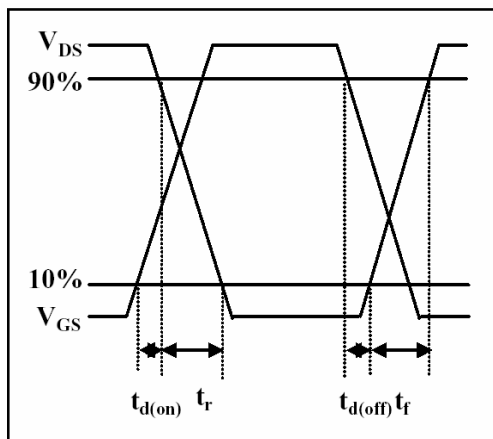


Fig 11. Switching Time Waveform

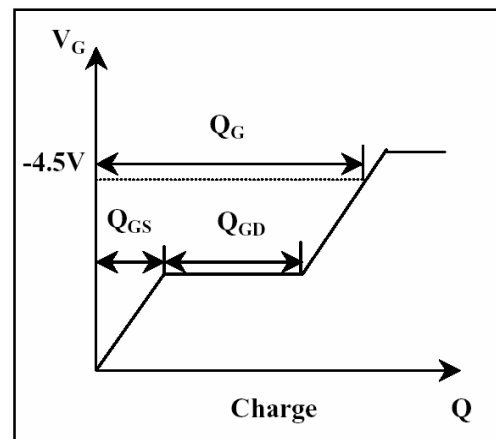


Fig 12. Gate Charge Waveform