XC74UL08AA

ETR1305_002

CMOS Logic

■GENERAL DESCRIPTION

The XC74UL08AA is a 2-input CMOS AND Gate, manufactured using silicon gate CMOS fabrication.
CMOS low power circuit operation makes high speed LS-TTL operation achievable.
With a wave forming buffer connected internally, stabilized output can be achieved as the circuit offers high noise immunity.
As the XC74UL08AA is integrated into mini molded, SSOT-25 and SON-6 package, high density mounting is possible.

■APPLICATIONS

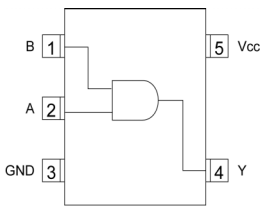
●Palmtops

●Digital equipment

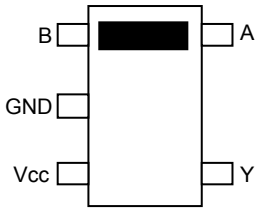
■FEATURES

High Speed Operation : tpd = 2.6ns (TYP.)
Operating Voltage Range : 2V ~ 5.5V
Low Power Consumption: 1μA (MAX.)
CMOS 2-Input AND Gate
Ultra Small Packages : SSOT-25, SON-6*
* Under Development

■PIN CONFIGURATION



SSOT-25
（TOP VIEW）
XC74UL08AAN



SON-6
（TOP VIEW）
XC74UL08AAR

■FUNCTIONS

INPUT		OUTPUT
A	B	Y
L	L	L
L	H	L
H	L	L
H	H	H

H=High level
L=Low level

■ABSOLUTE MAXIMUM RATINGS

Ta=-40℃~85℃

PARAMETER		SYMBOL	RATINGS	UNITS
Supply Voltage		VCC	-0.5~+6.0	V
Input Voltage		VIN	-0.5~+6.0	V
Output Voltage		VOUT	-0.5~VCC+0.5	V
Input Diode Current		IIK	-20	mA
Output Diode Current		IOK	±20	mA
Output Current		IOUT	±25	mA
VCC,GND Current		ICC,IGND	±50	mA
Power Dissipation	SSOT-25*1	Pd	150	mW
	SON-6*2		200	
Storage Temperature Range		Tstg	-65~+150	℃

Voltage is all ground standardized.
*1）Ta=55℃
*2）Ta=25℃

■RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	V_{CC}(V)	CONDITIONS	UNITS
Supply Voltage	V_{CC}	–	2~5.5	V
Input Voltage	V_{IN}	–	0~5.5	V
Output Voltage	V_{OUT}	–	0~V_{CC}	V
Operating Temperature Range	Topr	–	-40~+85	℃
Output Current	I_{OH}	3.0	-4	mA
		4.5	-8	
	I_{OL}	3.0	4	
		4.5	8	
Input Rise and Fall Time	tr,tf	3.3	0~100	ns
		5.0	0~20	

■DC ELECTRICAL CHARACTERISTICS

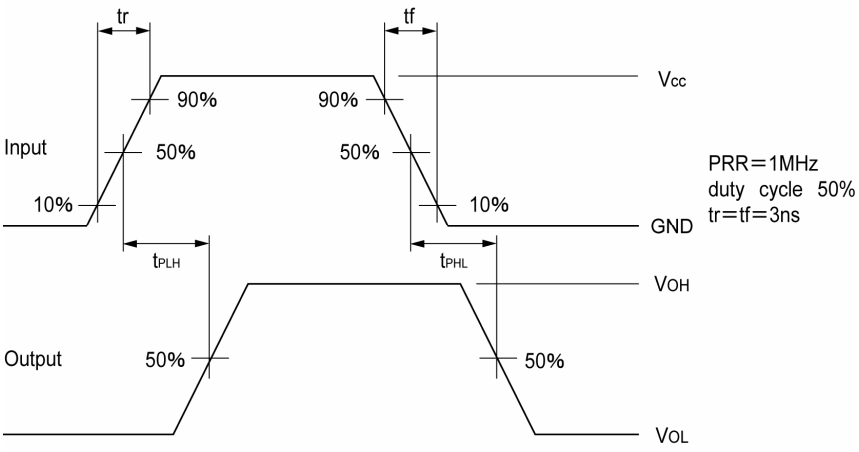
PARAMETER	SYMBOL	V_{CC}(V)	CONDITIONS		Ta=25℃ MIN.	Ta=25℃ TYP.	Ta=25℃ MAX.	Ta=-40℃~85℃ MIN.	Ta=-40℃~85℃ MAX.	UNITS
Input Voltage	V_{IH}	2.0			1.5	–	–	1.5	–	V
		3.0			2.1	–	–	2.1	–	
		5.5			3.85	–	–	3.85	–	
	V_{IL}	2.0			–	–	0.5	–	0.5	V
		3.0			–	–	0.9	–	0.9	
		5.5			–	–	1.65	–	1.65	
Output Voltage	V_{OH}	2.0	V_{IN}=V_{IH}	I_{OH}=-50μA	1.9	2.0	–	1.9	–	V
		3.0			2.9	3.0	–	2.9	–	
		4.5			4.4	4.5	–	4.4	–	
		3.0		I_{OH}=-4mA	2.58	–	–	2.48	–	
		4.5		I_{OH}=-8mA	3.94	–	–	3.80	–	
	V_{OL}	2.0	V_{IN}=V_{IH} or V_{IL}	I_{OL}=50μA	–	–	0.1	–	0.1	V
		3.0			–	–	0.1	–	0.1	
		4.5			–	–	0.1	–	0.1	
		3.0		I_{OL}=4mA	–	–	0.36	–	0.44	
		4.5		I_{OL}=8mA	–	–	0.36	–	0.44	
Input Current	I_{IN}	5.5	V_{IN}=V_{CC} or GND		-0.1	–	0.1	-1.0	1.0	μA
Static Supply Current	I_{CC}	5.5	V_{IN}=V_{CC} or GND,I_{OUT}=0μA		–	–	1.0	–	10.0	

■SWITCHING ELECTRICAL CHARACTERISTICS

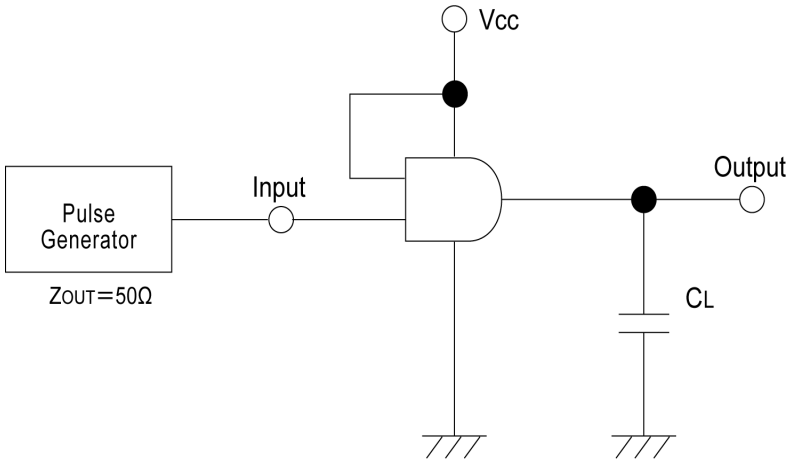
tr=tf=3ns

PARAMETER	SYMBOL	C_L	V_{CC}(V)	CONDITIONS	Ta=25℃ MIN.	Ta=25℃ TYP.	Ta=25℃ MAX.	Ta=-40℃~85℃ MIN.	Ta=-40℃~85℃ MAX.	UNITS
Delay Time	tPLH	15pF	3.3		–	3.7	8.8	1.0	10.5	ns
			5.0		–	2.8	5.9	1.0	7.0	
		50pF	3.3		–	5.2	12.3	1.0	14.0	ns
			5.0		–	3.7	7.9	1.0	9.0	
	tPHL	15pF	3.3		–	3.2	8.8	1.0	10.5	ns
			5.0		–	2.4	5.9	1.0	7.0	
		50pF	3.3		–	4.5	12.3	1.0	14.0	ns
			5.0		–	3.4	7.9	1.0	9.0	
Input Capacitance	C_{IN}	–	5.0	V_{IN}=V_{CC} or GND	–	4	10	–	10	pF
Power Dissipation Capacitance	Cpd	No Load, f=1MHz			–	9.3	–	–	–	pF

■WAVEFORM



■TEST CIRCUIT



Note: Open output when measuring supply current

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