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2N7002DWL

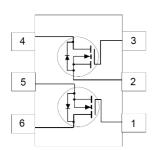
Features

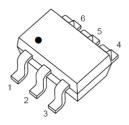
- High density cell design for Low RDS(on)
- Rugged and reliable
- Moisture Sensitivity Level 1
- · Voltage controlled small signal switch
- High saturation current capability
- Epoxy meets UL 94 V-0 flammability rating
- Halogen free available upon request by adding suffix "-HF"

Maximum Ratings @ 25°C Unless Otherwise Specified

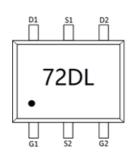
Parameter	Symbol	Value	Unit
Drain-Source Voltage	VDS	60	V
Gate-Source Voltage	VGS	±20	V
Continuous Drain Current	ID	115	mA
Power Dissipation	PD	0.225	W
Thermal Resistance from Junction to Ambient	RθJA	277	°C/W
Junction Temperature	TJ	150	°C
Storage Temperature	TSTG	-55~ +150	°C

Equivalent Circuit:



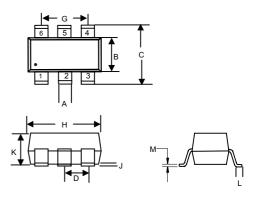


Marking:



Dual N-Channel MOSFET

SOT23-6L



	DIMENSIONS				
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.012	.020	0.30	0.50	
В	.051	.070	1.30	1.80	
С	.087	.126	2.20	3.20	
D	.037		0.95BSC		
G	.074		1.90BSC		
Н	.106	.122	2.70	3.10	
J	.002	.006	0.05	0.15	
K	.030	.035	0.75	0.90	
Ĺ	.012	.024	0.30	0.60	
M	.003	.008	0.08	0.22	



Electrical characteristics (T_A=25 °C, unless otherwise noted)

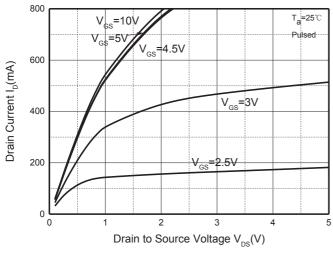
Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit	
Static Characteristics	•		•		•		
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D =250µA	60			V	
Zero gate voltage drain current	I _{DSS}	V _{DS} =60V,V _{GS} = 0V			100	nA	
Gate-body leakage current	Igss	V _{GS} =±20V, V _{DS} = 0V			±1	uA	
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1	1.4	2.5	V	
On-state drain current	I _{D(ON)}	V _{GS} =10V, V _{DS} =7V	500			mA	
Drain-source on-resistance	Ь	V _{GS} =10V, I _D =500mA			4.5		
	R _{DS(on)}	V _{GS} =5V, I _D =50mA			5	Ω	
On-state drain-source voltage		V _{GS} =10V, I _D =500mA			3	V	
	V _{DS(on)}	V _{GS} =5V, I _D =50mA			0.375		
Dynamic characteristics							
Input Capacitance ¹⁾	Ciss			10.1	50	pF	
Output Capacitance ¹⁾	Coss	V _{DS} =25V,V _{GS} =0V,f=1MHz		4.2	25		
Reverse Transfer Capacitance ¹⁾	C _{rss}			1.9	5		
Switching Characteristics							
Turn-on delay time ¹⁾	t _{d(on)}	$V_{DD}=10 \text{ V, } R_{L}=20\Omega$		5.6		ns	
Turn-off delay time ¹⁾	td _(off)	- I _D =500mA,V _{GEN} =10V, R _G =10Ω		25			
Source-Drain Diode characteristi	cs		•	•	•		
Diode Forward voltage	V _{SD}	V _{GS} =0V, I _S =150mA	0.6	0.82	1.0	V	
Source Current Continuous	Is				115	mA	

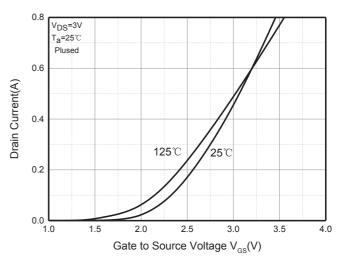
Notes:

1) These parameters have no way to verify.

Typical characteristics

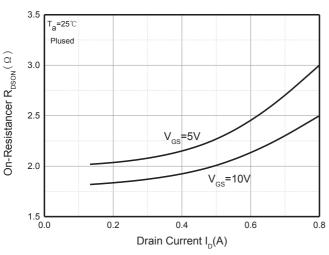


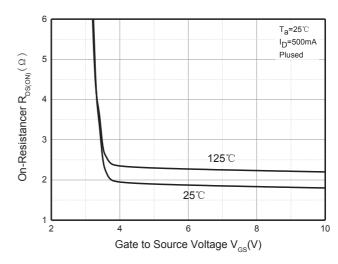




Output Characteristics

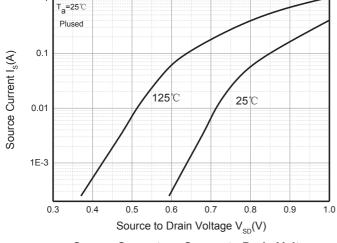
Transfer Characteristics

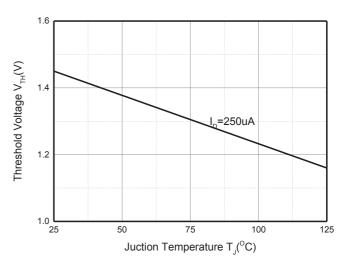




On-Resistance vs. Drain current

On-Resistance vs. Gate to Source Voltage





Source Current vs. Source to Drain Voltage

Threshold voltage vs. Junction temperature



Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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