

Features

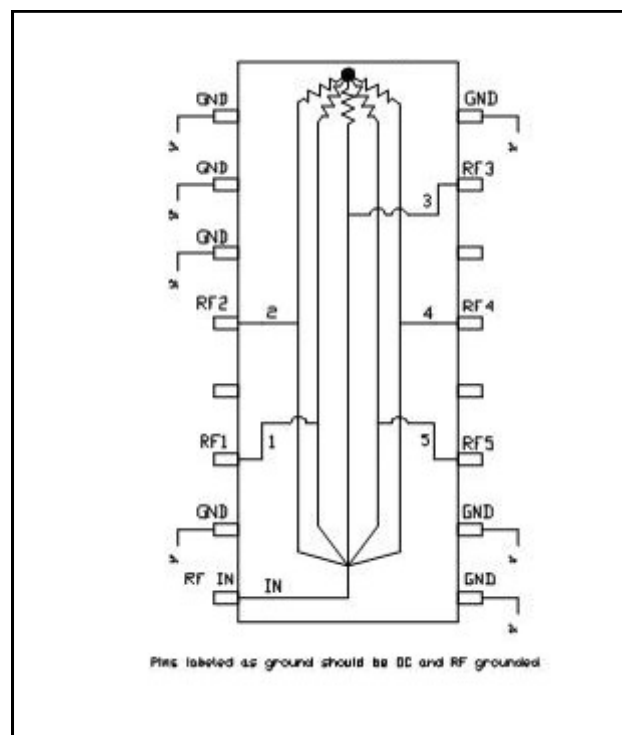
- Small Size, Low Profile
- Superior Repeatability (Lot-to-Lot Variation)
- Typical Isolation: 28 dB
- Typical Insertion Loss: 0.7 dB
- Low Cost
- 1 Watt Power Handling
- Lead-Free SOIC-16 Package
- 100% Matte Tin Plating over Copper
- Halogen-Free “Green” Mold Compound
- 260°C Reflow Compatible
- RoHS* Compliant Version of DS55-0003

Description

M/A-COM's MAPDCC0015 is an IC-based monolithic power divider using M/A-COM's GMIC technology in a low cost SOIC-16 plastic package. This 5-way power divider is ideally suited for applications where PCB real estate is at a premium and part count reduction and cost are critical. Typical applications include base station switching networks and other cellular equipment, including subscriber units. Available in tape and reel.

The MAPDCC0015 is fabricated using a passive-integrated circuit process. The process features full -chip passivation for increased performance and reliability.

Functional Block Diagram



Pin Configuration

Pin No.	Function	Pin No.	Function
1	GND	9	GND
2	GND	10	GND
3	GND	11	RF5
4	RF2	12	GND
5	GND	13	RF4
6	RF1	14	GND
7	GND	15	RF3
8	RF IN	16	GND

Ordering Information

Part Number	Package
MAPDCC0015	Bulk Packaging
MAPDCC0015-TR	1000 piece reel
MAPDCC0015-TB	Sample Test Board

Note: Reference Application Note M513 for reel size information.

* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

Low Cost Five-Way GMIC SMT Power Divider 1850-1990 MHz

Rev. V2

Electrical Specifications: $T_A = 25^\circ\text{C}$, $Z_0 = 50\Omega$

Parameter	Units	Min	Typ	Max
Insertion Loss Above 7.0dB	dB	—	0.7	1.2
Isolation	dB	21	28	—
VSWR Input	—	—	1.5:1	1.6:1
VSWR Output	—	—	1.2:1	1.5:1
Amplitude Balance	dB	—	0.2	0.5
Phase Balance	°	—	6	12

Absolute Maximum Ratings^{1,2}

Parameter	Absolute Maximum
Input Power ⁴	1 W CW
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +150°C

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.
- With Internal load dissipation of 0.125 W maximum.

Handling Procedures

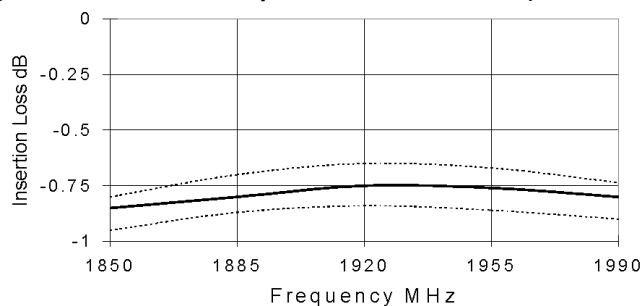
Please observe the following precautions to avoid damage:

Static Sensitivity

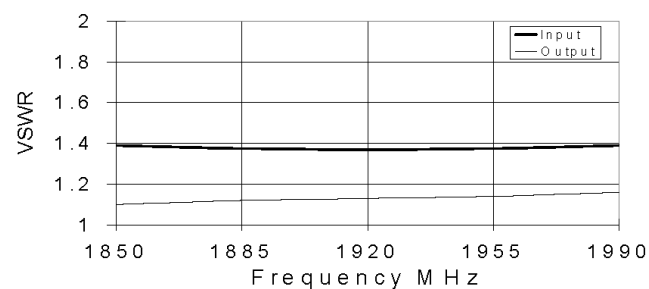
GMIC Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Typical Performance Curves

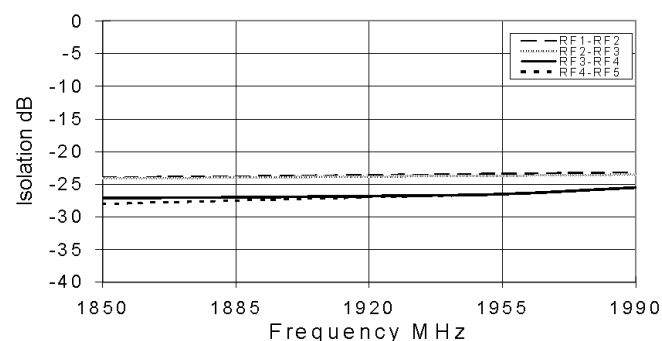
Insertion Loss vs. Frequency
(Dashed lines show amplitude balance window)



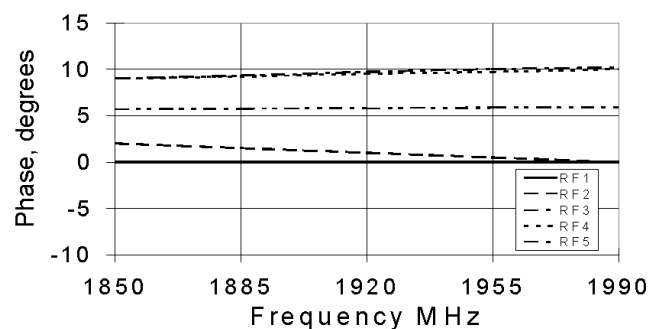
VSWR vs. Frequency



Isolation vs. Frequency



Phase Balance vs. Frequency
(Relative RF1)

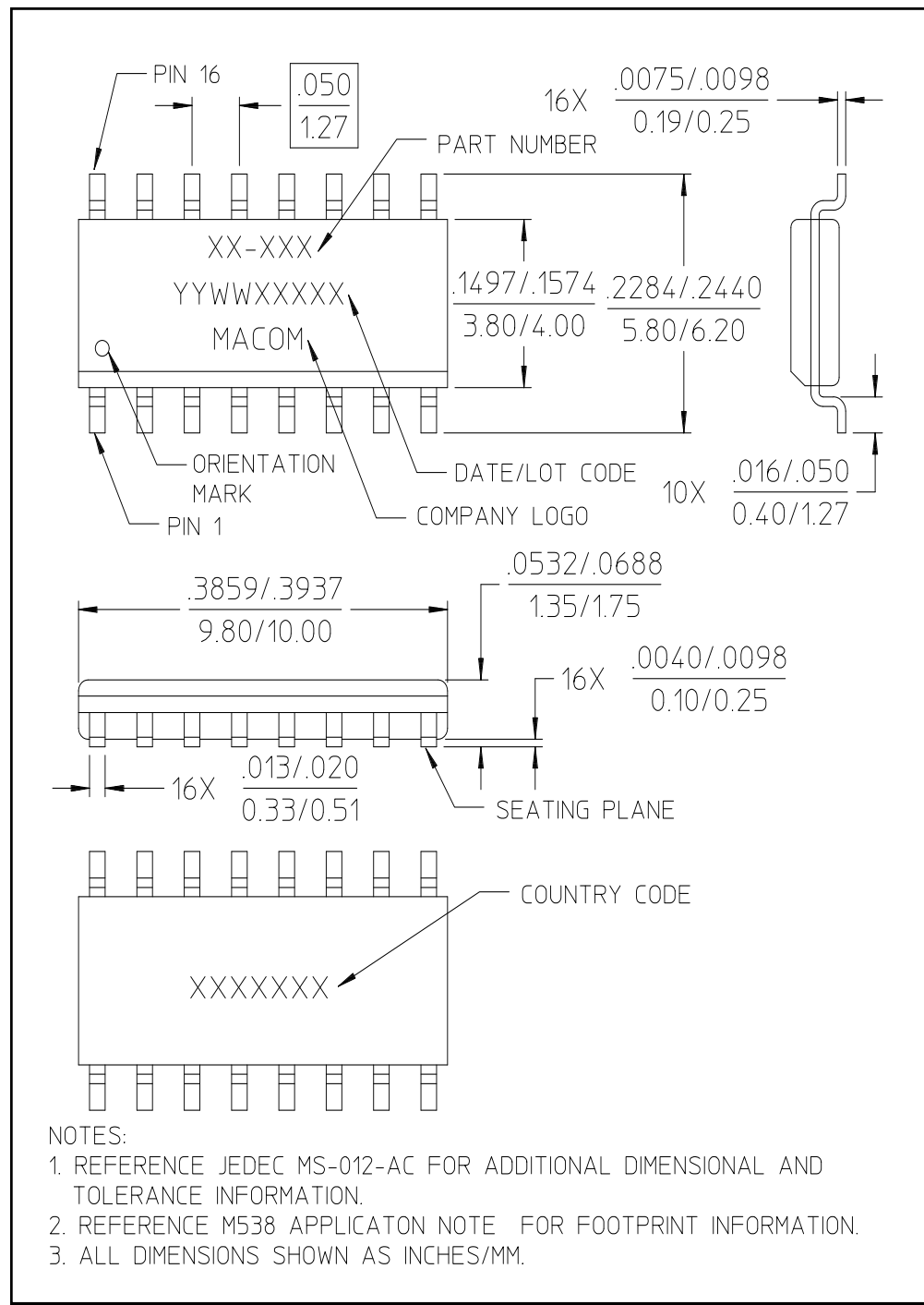


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Lead-Free, SOIC-16[†]



[†] Reference Application Note M538 for lead-free solder reflow recommendations.

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