

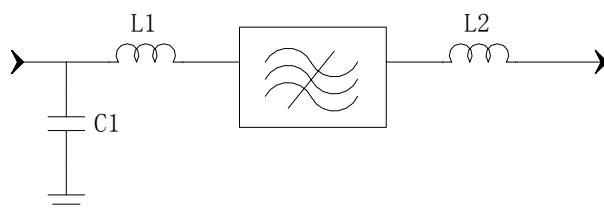
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	94.96	95	95.04
Insertion Loss	dB		23.5	25
2dB Bandwidth	MHz	2.2	2.25	
25dB Bandwidth	MHz		2.83	3
35dB Bandwidth	MHz		2.95	3.4
40dB Bandwidth	MHz		3	4.2
Passband Variation	dB		0.6	1
Absolute Delay	usec		4.3	5
Ultimate Rejection($f0\pm6MHz$)	dB	45	47	
Material temperature coefficient	KHz/°C	1xF ₀ /1000		
Ambient Temperature	°C	25		
Package Size	DIP3512 (35.2x12.7x5.2mm ³)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=L2=180nH
C1=56pF

Source/Load Impedance=50 ohm

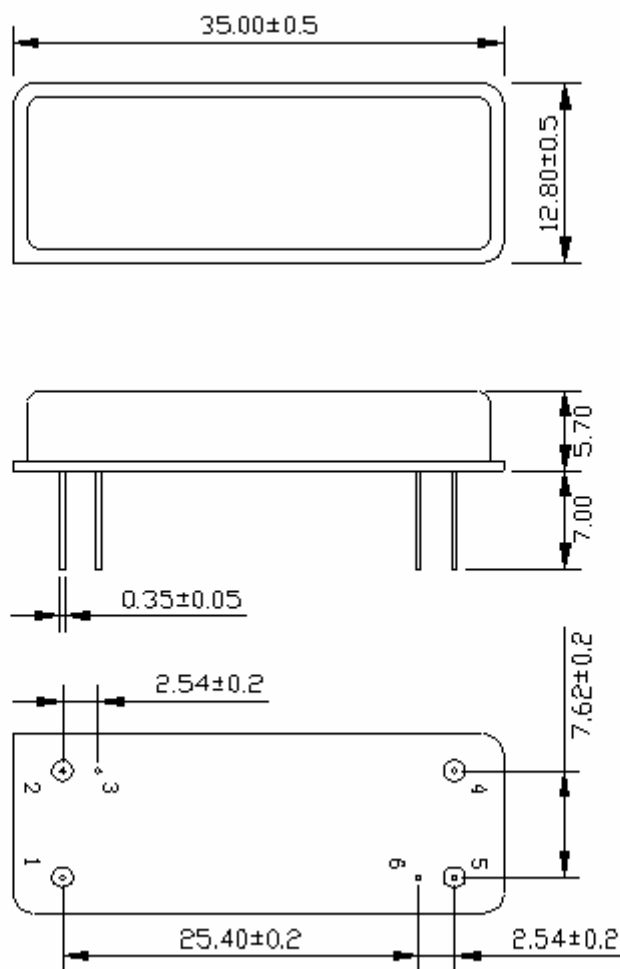
Notes - Component values may change depending
on board layout.



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Package Dimension



Input:1
Output:5

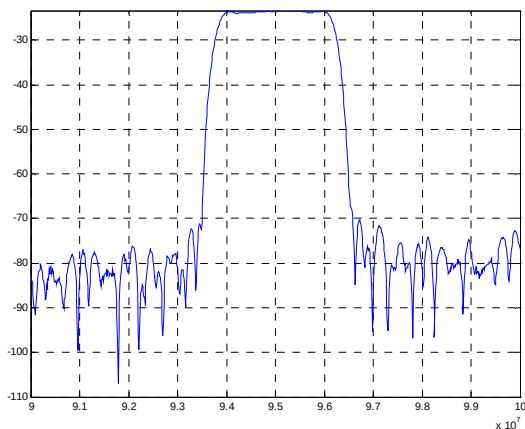


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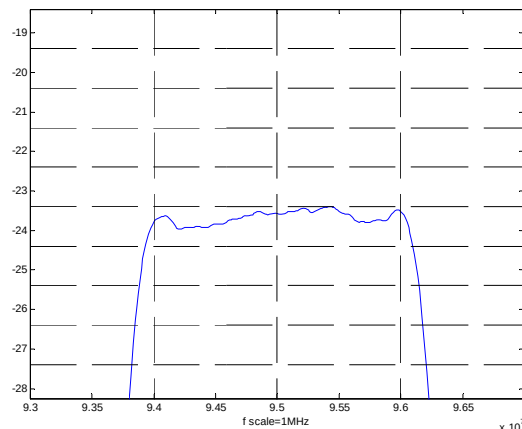
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Typical Performance

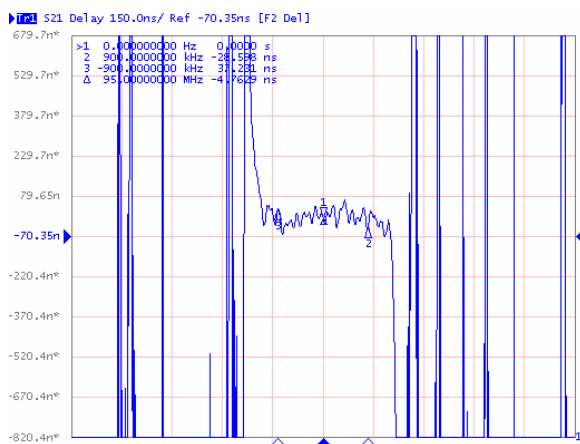
Frequency Respond



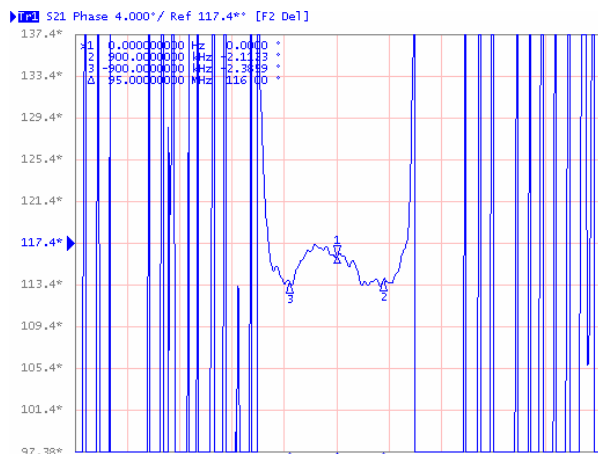
Passband Respond



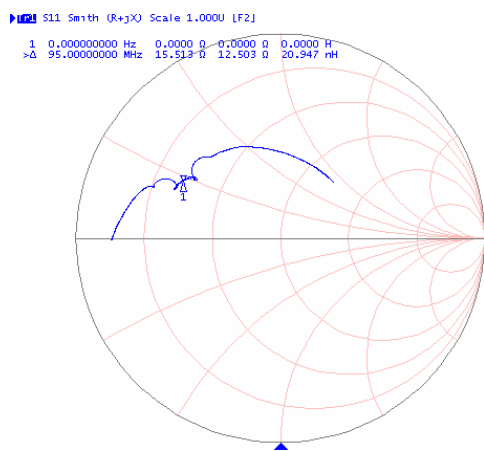
Group Delay Variation($f_0 \pm 0.9\text{MHz}$)



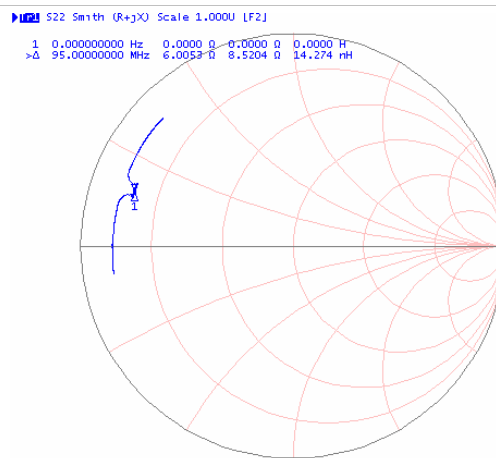
Phase Linearity($f_0 \pm 0.9\text{MHz}$)



Simth Chart S11



Simth Chart S22



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