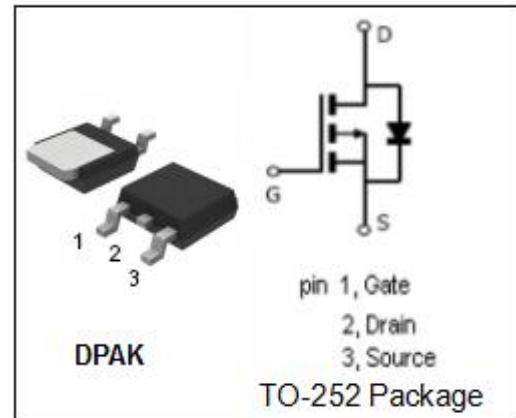


## Isc P-Channel MOSFET Transistor

## SUD09P10-195

### • FEATURES

- With To-252(DPAK) package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

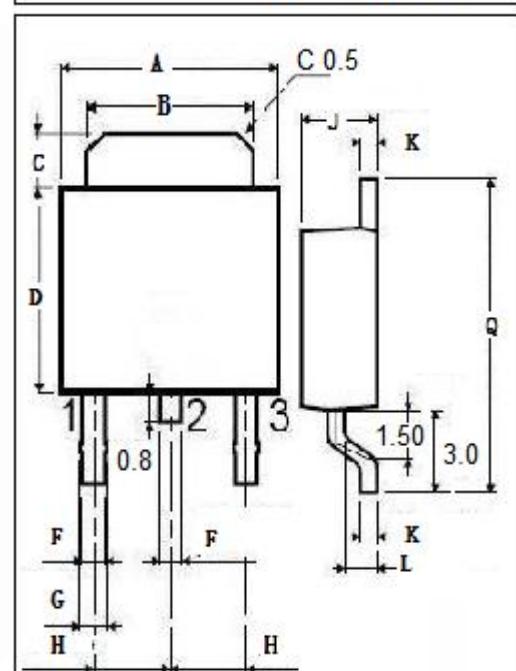


### • APPLICATIONS

- Switching applications
- Motor control
- DC-DC converters

### • ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	-100	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous $T_c=25^\circ\text{C}$ $T_c=100^\circ\text{C}$	-8.8 -7.1	A
$I_{DM}$	Drain Current-Single Pulsed	-15	A
$P_D$	Total Dissipation @ $T_c=25^\circ\text{C}$	32.1	W
$T_j$	Max. Operating Junction Temperature	150	°C
$T_{stg}$	Storage Temperature	-55~150	°C



DIM	mm	
	MIN	MAX
A	6.40	6.60
B	5.20	5.40
C	1.15	1.35
D	5.70	6.10
F	0.65	
G	0.75	
H	2.10	2.50
J	2.10	2.40
K	0.40	0.60
L	0.90	1.10
Q	9.90	10.1

### • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	3.9	°C/W
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	50	°C/W

**Isc P-Channel MOSFET Transistor****SUD09P10-195****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}; I_D=-0.25\text{mA}$	-100			V
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}; I_D=-0.25\text{mA}$	-1.0		-2.5	V
$R_{DS(\text{on})}$	Drain-Source On-Resistance	$V_{GS}=-10\text{V}; I_D=-3.6\text{A}$		0.16	0.195	$\Omega$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}; V_{DS}=0\text{V}$			$\pm 0.25$	$\mu\text{A}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-100\text{V}; V_{GS}=0\text{V}; T_c=25^\circ\text{C}$ $T_c=125^\circ\text{C}$			-1 -50	$\mu\text{A}$
$V_{SDF}$	Diode forward voltage	$I_{SD}=-2.9\text{A}, V_{GS}=0\text{V}$			-1.5	V