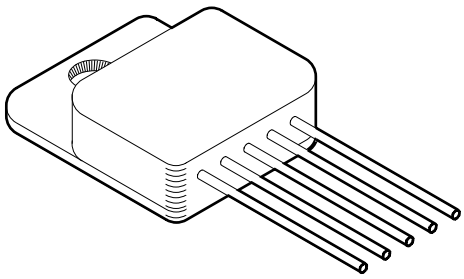
RAD HARD POSITIVE, 1.9A, LDO, SINGLE RESISTOR ADJ VOLTAGE REGULATOR

5985RH

FEATURES:

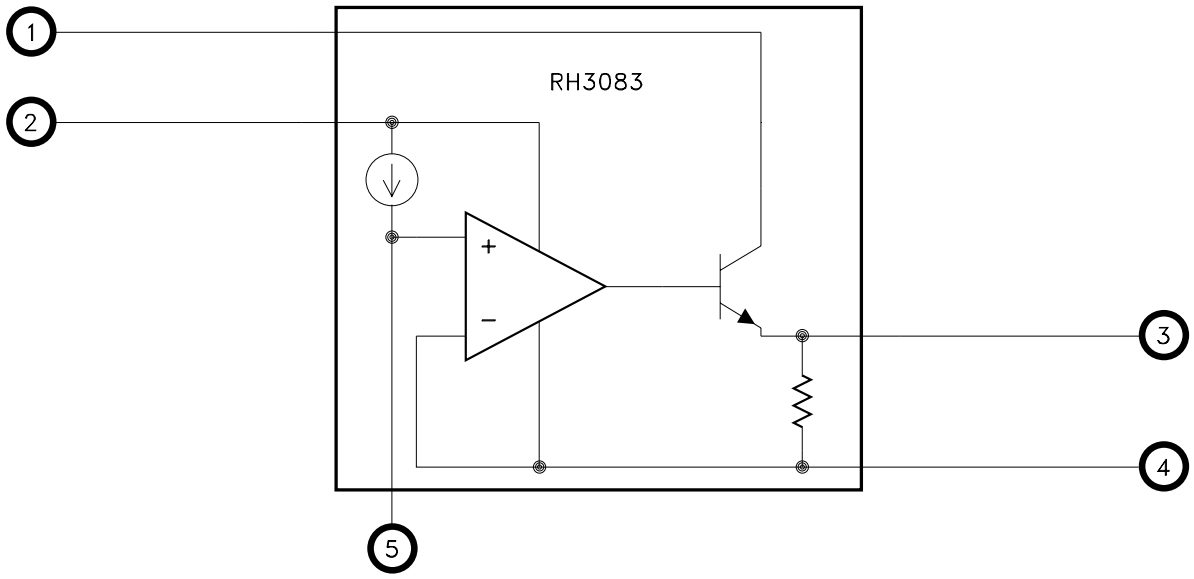
- Manufactured using LINEAR TECHNOLOGY Space Qualified RH3083 Die
- MIL-PRF-38534 Class K Processing & Screening
- Total Dose Hardened to 300 Krads(Si) (Method 1019.7 Condition A)
- Low Dropout to 310mV (VIN - VOUT, with Seperate Control Supply)
- Output Adjustable to Zero Volts
- Internal Short Circuit Current Limit
- Output Voltage is Adjustable with 1 External Resistor
- Output Current Capability to 1.9A
- Internal Thermal Overload Protection
- Outputs may be Paralleled for Higher Current
- Contact MSK for MIL-PRF-38534 Qualification and Radiation Status



DESCRIPTION:

The MSK5985RH offers low dropout down to 310mV and an output voltage range down to zero volts while offering radiation tolerance for space applications. This, combined with the low q_{JC}, allows increased output current while providing exceptional device efficiency. Output voltage is selected by the user through the use of 1 external resistor. Additionally, the regulator offers internal short circuit current and thermal limiting, which allows circuit protection and eliminates the need for external components and excessive derating. The MSK5985RH is hermetically sealed in a space efficient 5 pin SIP, that is electrically isolated from the internal circuitry allowing for direct heat sinking.

EQUIVALENT SCHEMATIC



TYPICAL APPLICATIONS

- High Efficiency Linear Regulators
- Constant Voltage/Current Regulators
- Space System Power Supplies
- Switching Power Supply Post Regulators
- Very low Voltage Power Supplies

PIN-OUT INFORMATION

1	VIN
2	CONTROL
3	VOUT
4	SENSE
5	SET

CASE = ISOLATED

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ABSOLUTE MAXIMUM RATINGS ⑩

V_{IN}	Input Voltage ⑦	+18V, -0.3V
	No Overload or Short ⑦	+23V, -0.3V
$V_{CONTROL}$	Control Pin Voltage ⑦	±28V
I_{OUT}	Output Current	3.0A
I_{SET}	Set Pin Current ⑧	±25mA
V_{SET}	Set Pin Voltage ⑦	±10V

P_D	Power Dissipation	Internally Limited
T_J	Junction Temperature	+150°C
T_{ST}	Storage Temperature Range	-65°C to +150°C
T_{LD}	Lead Temperature Range (10 Seconds)	300°C
T_C	Case Operating Temperature	
	MSK5985RH	-40°C to +85°C
	MSK5985K/H RH	-55°C to +125°C
	ESD Rating	Class 2

ELECTRICAL SPECIFICATIONS

Parameter	Test Conditions ①⑪		Group A Subgroup	MSK 5985K/H RH Min.	MSK 5985K/H RH Typ.	MSK 5985K/H RH Max.	MSK 5985RH Min.	MSK 5985RH Typ.	MSK 5985RH Max.	Units
Set Pin Current (I_{SET})	$V_{CONTROL}$=VIN=3.0V VOUT=1.0V 5mA ≤ I_{LOAD} ≤ 1.9A		1	49.5	50	50.5	49.5	50	50.5	µA
			2, 3	49	-	51.5	-	-	-	µA
		Post Radiation	1	49	-	51	49	-	51	µA
Output Offset Voltage (V_{OS})	$V_{CONTROL}$=VIN=3.0V VOUT=1.0V I_{LOAD}=5mA		1	-4.5	0	4.5	-4.5	0	4.5	mV
			2, 3	-6	-	6	-	-	-	mV
Load Regulation	$V_{CONTROL}$=VIN=3.0V VOUT=0V 5mA ≤ I_{LOAD} ≤ 1.9A	(Δ V_{OS})	1, 2, 3	-4	1	4	-4	-	4	mV
		(Δ I_{SET})	1, 2, 3	-300	-60	+300	-300	-60	+300	nA
		(Δ V_{OS}) SENSE PIN=NC	1, 2, 3	-	8	-	-	8	-	mV
Line Regulation	ΔVIN=2V TO 24V ΔV$_{CONTROL}$=3V TO 24V VOUT=0V I_{LOAD}=5mA	(Δ V_{OS})	1, 2, 3	-0.05	-	0.05	-0.05	-	0.05	mV/V
		(Δ I_{SET})	1, 2, 3	-10	1	+10	-10	1	+10	nA/V
VIN Dropout Voltage	VOUT=1.0V I_{LOAD}=1.9A $V_{CONTROL}$=3.0V		1	-	265	700	-	265	700	mV
			2, 3	-	-	800	-	-	-	mV
Control Pin Dropout Voltage	VOUT=1.0V I_{LOAD}=1.9A VIN=3.0V		1	-	1.25	1.55	-	1.25	1.55	V
			2, 3	-	-	1.65	-	-	-	V
Control Pin Current	VIN=2.0V VOUT=1.0V $V_{CONTROL}$=3.0V	I_{LOAD} = 100mA	1, 2, 3	-	5.5	11	-	5.5	11	mA
		I_{LOAD} = 1.9A	1, 2, 3	-	50	100	-	50	100	mA
Current Limit ⑨	$V_{CONTROL}$=VIN=5.0V VOUT=1.0V		1, 2, 3	1.9	2.3	-	1.9	2.3	-	A
		SENSE PIN=NC	1, 2, 3	-	2.9	-	-	2.9	-	A
Minimum Load Current ⑥	$V_{CONTROL}$=VIN=23V		-	-	-	1	-	-	1	mA
Ripple Rejection ②	F=120Hz ΔVIN=0.5V_{PP} I_{OUT}=0.1A	C_{SET} = 0.1µF	-	-	85	-	-	85	-	dB
Output Noise ②	VIN=$V_{CONTROL}$=3V, I_{OUT}=500mA 10Hz to 100KHz	C_{SET} = 0.1µF	-	-	40	-	-	40	-	μV_{RMS}
Thermal Resistance ②	Junction to Case @ 125°C		-	-	2.7	3.2	-	2.7	3.2	°C/W

NOTES:

① Output is decoupled to ground using a 220µF tantalum low ESR capacitor in parallel with 3 pieces of 1.0µF and one 0.1µF ceramic capacitor unless otherwise specified. Sense pin connected to VOUT unless otherwise specified. (See Figure 1).

② Guaranteed by design but not tested. Typical parameters are representative of actual device performance but are for reference only.

③ Industrial grade devices shall be tested to subgroup 1 unless otherwise specified.

④ Class H and K devices shall be 100% tested to subgroups 1, 2 and 3.

⑤ Subgroup 1 T_A = T_C = +25°C
Subgroup 2 T_A = T_C = +125°C
Subgroup 3 T_A = T_C = -55°C

⑥ Minimum load current verified while testing line regulation.

⑦ Voltage is measured with respect to VOUT.

⑧ Set pin is clamped to VOUT with diodes in series with 1KΩ resistors. Current will flow under transient conditions.

⑨ Reference the current limit typical performance curve for output current capability versus voltage drop.

⑩ Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.

⑪ Pre and Post irradiation limits at 25°C, up to 300 Krad(Si) TID, are identical unless otherwise specified.

APPLICATION NOTES

OUTPUT VOLTAGE

A single resistor (RSET) from the SET pin to ground creates the reference voltage for the internal Error Amplifier. The MSK5985RH SET pin supplies a constant current of 50uA that develops the reference voltage. The output voltage is simply RSET x 50uA. Since the output is internally driven by a unity-gain amplifier, an alternative to using RSET is to connect a high quality reference source to the SET pin. With a minimum load requirement of 1mA on the Output, the Output Voltage can be adjusted to near 0V. To bring the output voltage to 0V, the load must be connected to a slightly negative voltage supply to sink the 1mA minimum load current from a 0V output.

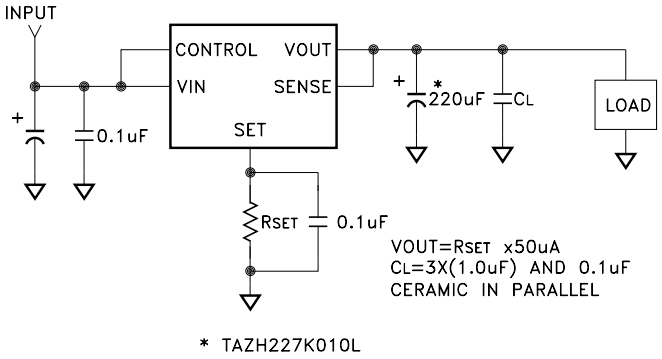


FIGURE 1

INPUT CAPACITANCE

Pins 3-8 are the connection to the collector of the power device of the MSK5985RH. Output load current is supplied through these pins. Minimum input capacitance for these devices is 10uF. Low ESR, ceramic input capacitors are acceptable for applications without long input leads. For applications with long input leads, the self inductance of the wires can cause instability. Care must be taken to minimize the inductance of the input wires. This can be accomplished through the use of series resistance or higher ESR input capacitors. A minimum of 10uF of low ESR tantalum bulk capacitance in parallel with low value ceramic decoupling capacitance is recommended.

CONTROL PIN

The control pin is the bias supply for the control circuitry of the MSK5985RH. Minimum input capacitance on the control pin is 2.2uF. Approximately 1.7% of the output current flows into this pin. For proper regulation, the control pin voltage must be 1.6V greater than the output voltage. (See Control Dropout Voltage Specification).

OUTPUT CAPACITANCE

For stability purposes, the MSK5985RH requires a minimum output capacitor of 10µF with an ESR of 0.5Ω or less. Tantalum or ceramic capacitors are recommended. A larger capacitance value will improve transient response for increased load current changes. Consideration must also be given to temperature characteristics of the capacitors used.

LOW DROPOUT OPERATION

Using separate VIN and CONTROL power supplies allows for lower dropout and improved efficiency. Figure 2 shows the MSK5985RH output transistor collector is connected to the VIN pin. The regulator control circuitry is powered by the CONTROL input. The dropout of the regulator is determined by the saturation voltage of the output transistor, typical 300mV at 1.9A ILOAD. The CONTROL supply must supply the base drive current for the output transistor. The CONTROL current minus the 50µA SET current is supplied to the load. See the Typical Performance Characteristics curves for expected VIN dropout voltage, CONTROL pin dropout voltage and current requirements under various conditions. With separate supplies for VIN and CONTROL, power dissipation is reduced and efficiency improves.

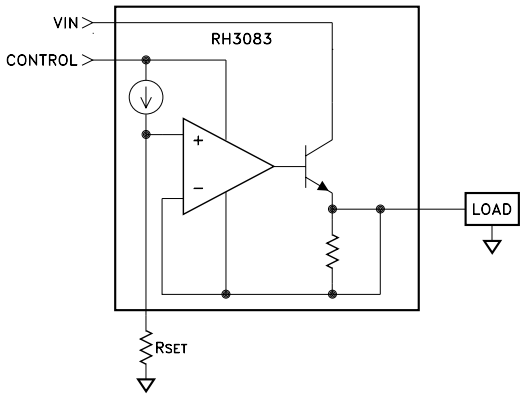


FIGURE 2

ADDITIONAL STABILITY

A capacitor placed in parallel with the SET pin resistor to ground, will improve the output transient response and filter noise in the system. To reduce output noise, typically 500-1000pF is required. Capacitors up to 1µF can be used, however consideration must be given to the effect the time constant created will have on the startup time.

APPLICATION NOTES

LOAD REGULATION

The MSK5985RH specified load regulation is Kelvin Sensed, therefore the parasitic resistance of the system must be considered to design an acceptable load regulation. The overall load regulation includes the specified MSK5985RH load regulation plus the parasitic resistance multiplied by the load current as shown in Figure 3. RSO is the series resistance of all conductors between the MSK5985RH output and the load. It will directly increase output load regulation error by a voltage drop of ΔIO x RSO. RSS is the series resistance between the SET pin and the load. RSS will have little effect on load regulation if the SET pin trace is connected as close to the load as possible keeping the load return current on a separate trace as shown. RSR is the series resistance of all of the conductors between the load and the input power source return. RSR will not effect load regulation if the SET pin is connected with a Kelvin Sense type connection as shown in Figure 3, but it will increase the effective dropout voltage by a factor of IO x RSR. Keeping RSO and RSR as low as possible will ensure minimal voltage drops and wasted power.

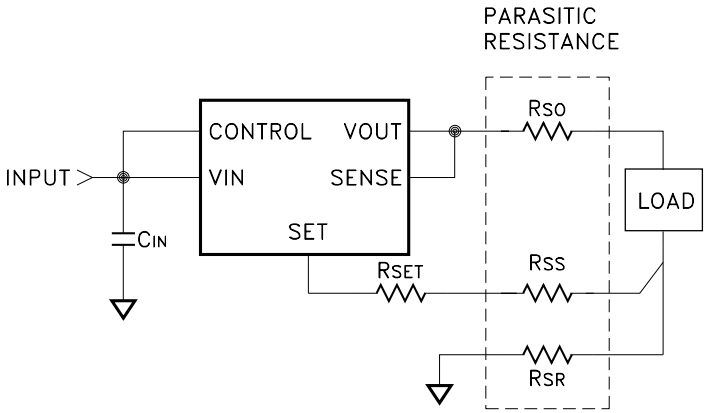


FIGURE 3

PARALLELING DEVICES

When currents greater than 1.9A are needed, the MSK5985RH's may be paralleled to multiply the current capacity. As shown in Figure 4, the VIN and SET pins must be tied together. The VOUT pins are connected to the load with consideration to the conductor resistance. The conductor resistance of each MSK5985RH VOUT connection to the load, must be equal to create equal load sharing. As little as 10mΩ ballast resistance typically ensures better than 80% equal sharing of load current at full load. Additional consideration must be given to the effect the additional VOUT conductor resistance has on load regulation; see paragraph titled "Load Regulation".

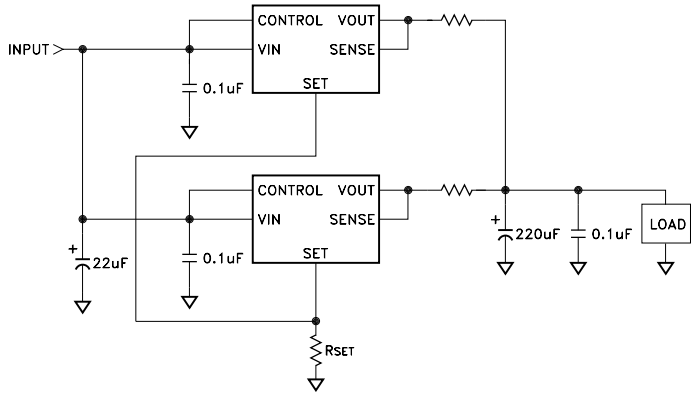


FIGURE 4

IMPROVING INITIAL ACCURACY AND REDUCING TEMPERATURE DRIFT

The initial output accuracy of the MSK5985RH due to SET pin current tolerance and set point resistor accuracy can be reduced to 0.2% using the MSK109RH radiation hardened precision reference. Minimal drift of the MSK109RH from temperature extremes and irradiation ensure very tight regulation. The circuit can be configured to use the 2.5V reference to directly set the output at 2.5V or with a slight variation it can provide any output within the operating range of the MSK5985RH down to 0V output. Select RS to maintain between 1mA and 10mA of current through the reference; see Figure 5 below. RS may be tied to VIN or another power source. The optional trim resistor can be used to further trim out initial output and system error. Reference the MSK109RH data sheet for application circuits that provide stable output voltages across the full operating range of the MSK5985RH including down to 0V output and the operating characteristics of the MSK109RH.

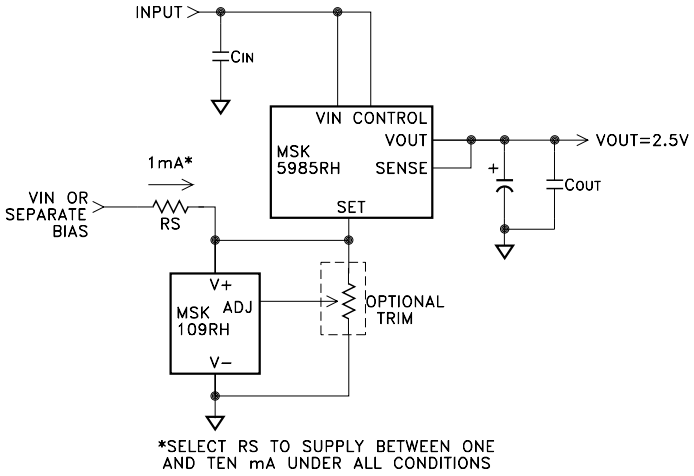


FIGURE 5

ADDING SHUTDOWN

The MSK5985RH can be easily shutdown by either reducing RSET to 0Ω or connecting a transistor from the SET pin to ground. By connecting two transistors, as shown in Figure 6, a low current voltage source is all that is required to take the SET pin to ground as well as pull the output voltage to ground. Q2 pulls the output voltage to ground when no load is present and only needs to sink 10mA.

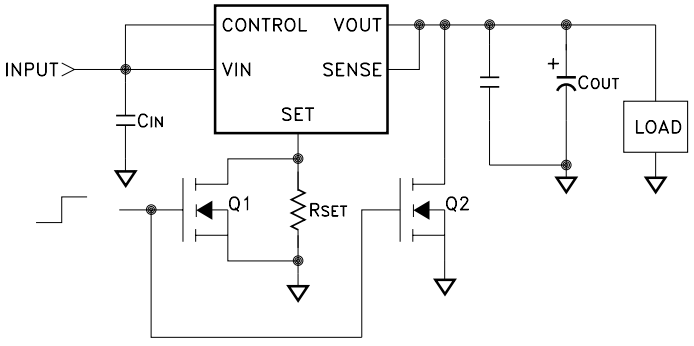


FIGURE 6

HEAT SINKING

To determine if a heat sink is required for your application and if so, what type, refer to the thermal model and governing equation below.

Governing Equation: $T_J = P_D \times (R\theta_{JC} + R\theta_{CS} + R\theta_{SA}) + T_A$

WHERE

T_J = Junction Temperature
P_D = Total Power Dissipation
$R\theta_{JC}$ = Junction to Case Thermal Resistance
$R\theta_{CS}$ = Case to Heat Sink Thermal Resistance
$R\theta_{SA}$ = Heat Sink to Ambient Thermal Resistance
T_C = Case Temperature
T_A = Ambient Temperature
T_S = Heat Sink Temperature

EXAMPLE:

This example demonstrates the thermal calculations for the regulator operating at 1.5A output current.

Conditions for MSK5985RH:

$V_{CONTROL}$=VIN = +3.0V; I_{OUT} = +1.5A VOUT=+1.0V

1.) Assume 45° heat spreading model.
2.) Find regulator power dissipation:

$P_D = (VIN - VOUT)(I_{OUT})$
$P_D = (3-1)(1.50)$
$= 3.0W$

3.) For conservative design, set T_J = +125°C Max.
4.) For this example, worst case T_A = +90°C.
5.) $R\theta_{JC}$ = 3.2°C/W from the Electrical Specification Table.
6.) $R\theta_{CS}$= 0.15°C/W for most thermal greases.
7.) Rearrange governing equation to solve for $R\theta_{SA}$:

$R\theta_{SA} = (T_J - T_A)/P_D - (R\theta_{JC}) - (R\theta_{CS})$
$= (125°C - 90°C)/3.0W - 3.2°C/W - 0.15°C/W$
$= 8.3°C/W$

In this case the result is 8.3°C/W. Therefore, a heat sink with a thermal resistance of no more than 8.3°C/W must be used in this application to maintain regulator circuit junction temperature under 125°C.

TOTAL DOSE RADIATION TEST PERFORMANCE

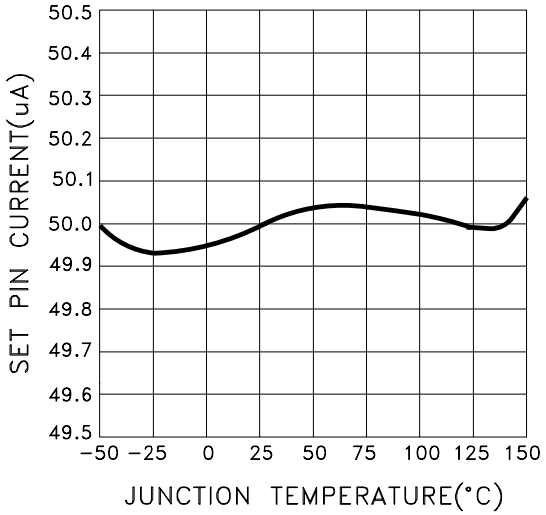
Radiation performance curves for TID testing will be generated for all radiation testing performed by MS Kennedy. These curves show performance trends throughout the TID test process and can be located in the MSK5985RH radiation test report. The complete radiation test report will be available in the RAD HARD PRODUCTS section on the MSK website.

ADDITIONAL APPLICATION INFORMATION

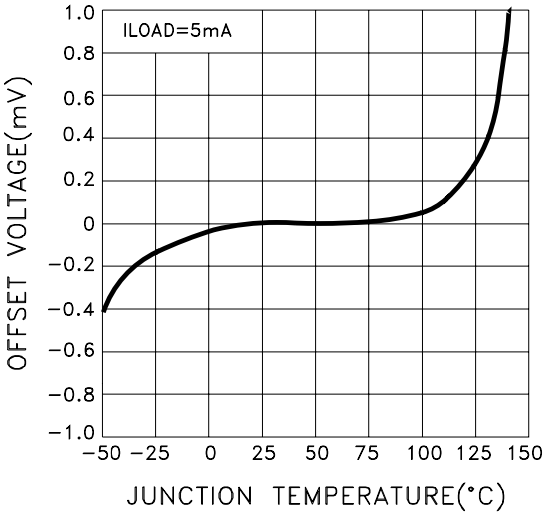
For additional applications information, please reference Linear Technology Corporation's® LT3083 and RH3083 data sheets.

TYPICAL PERFORMANCE CURVES

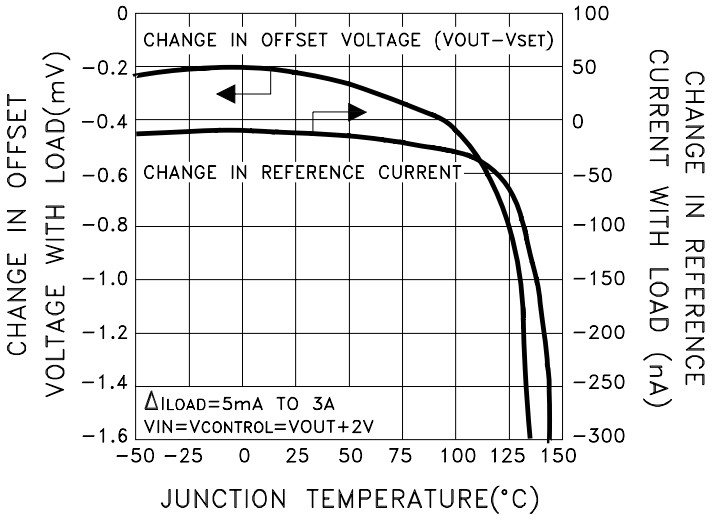
SET PIN CURRENT



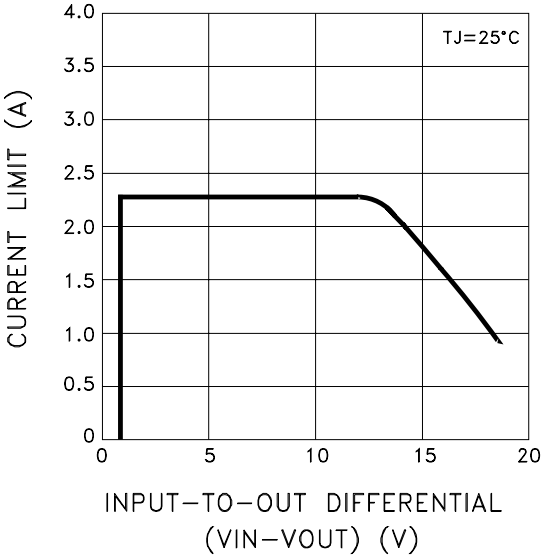
OFFSET VOLTAGE (VOUT−VSET)



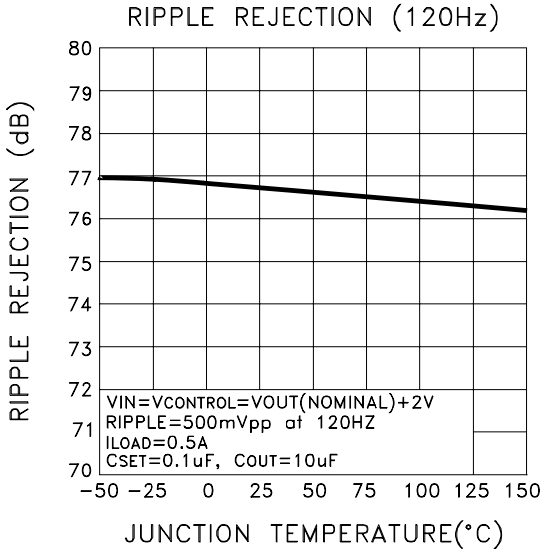
LOAD REGULATION



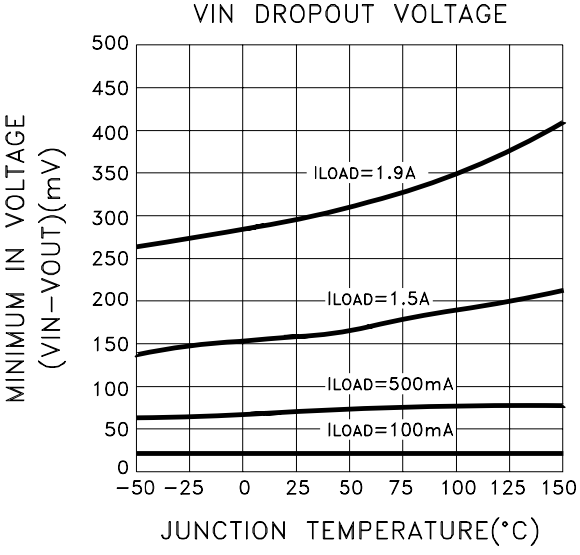
CURRENT LIMIT



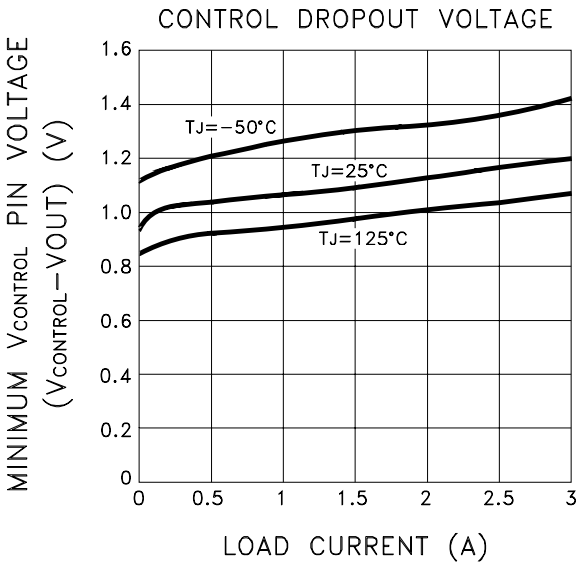
RIPPLE REJECTION (120Hz)

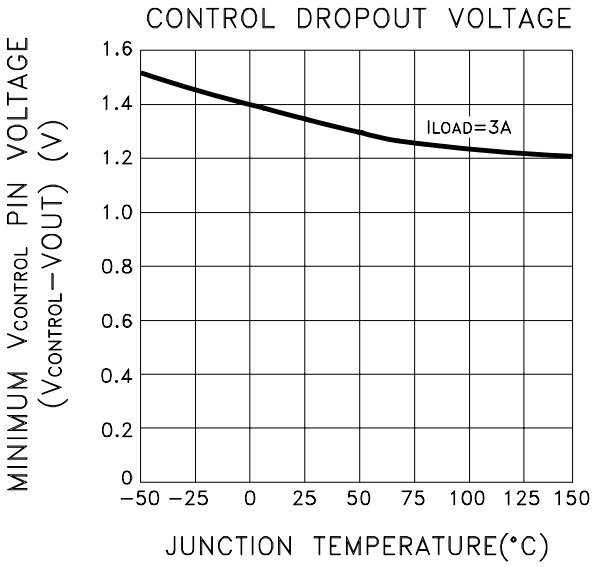


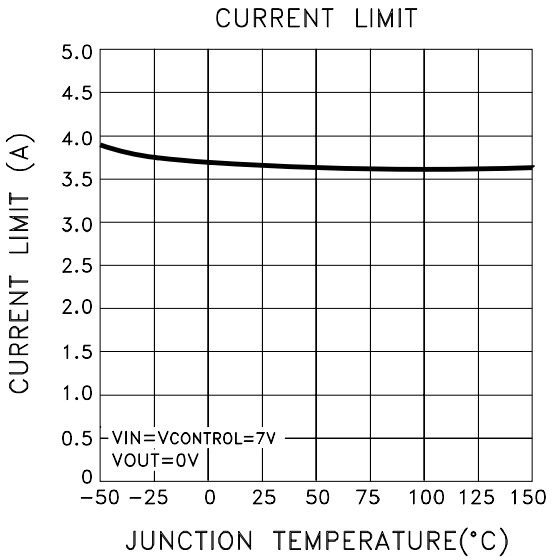
VIN DROPOUT VOLTAGE

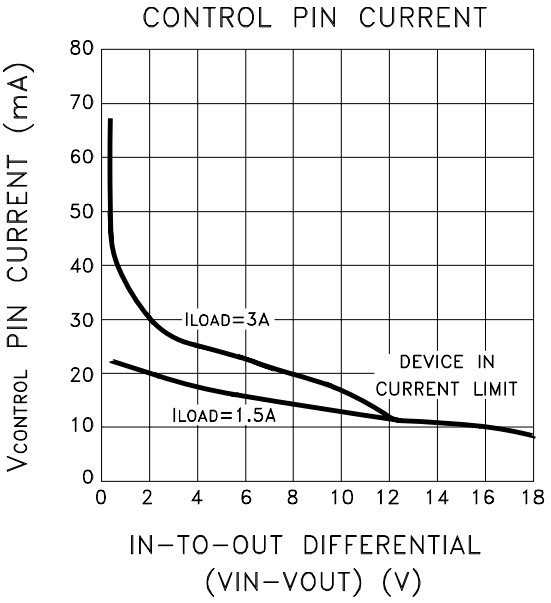


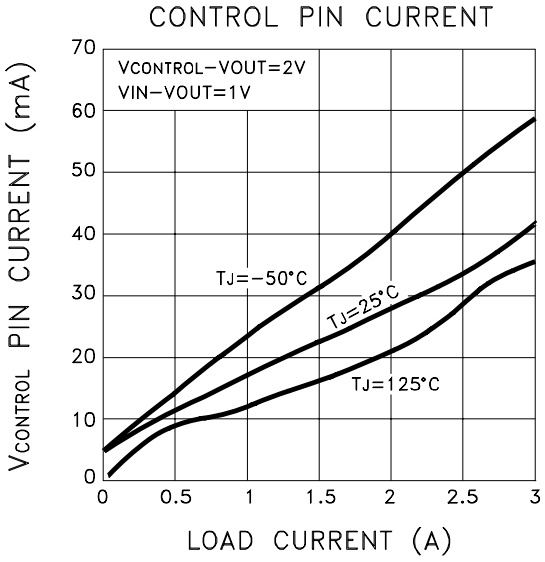
TYPICAL PERFORMANCE CURVES CONT'D



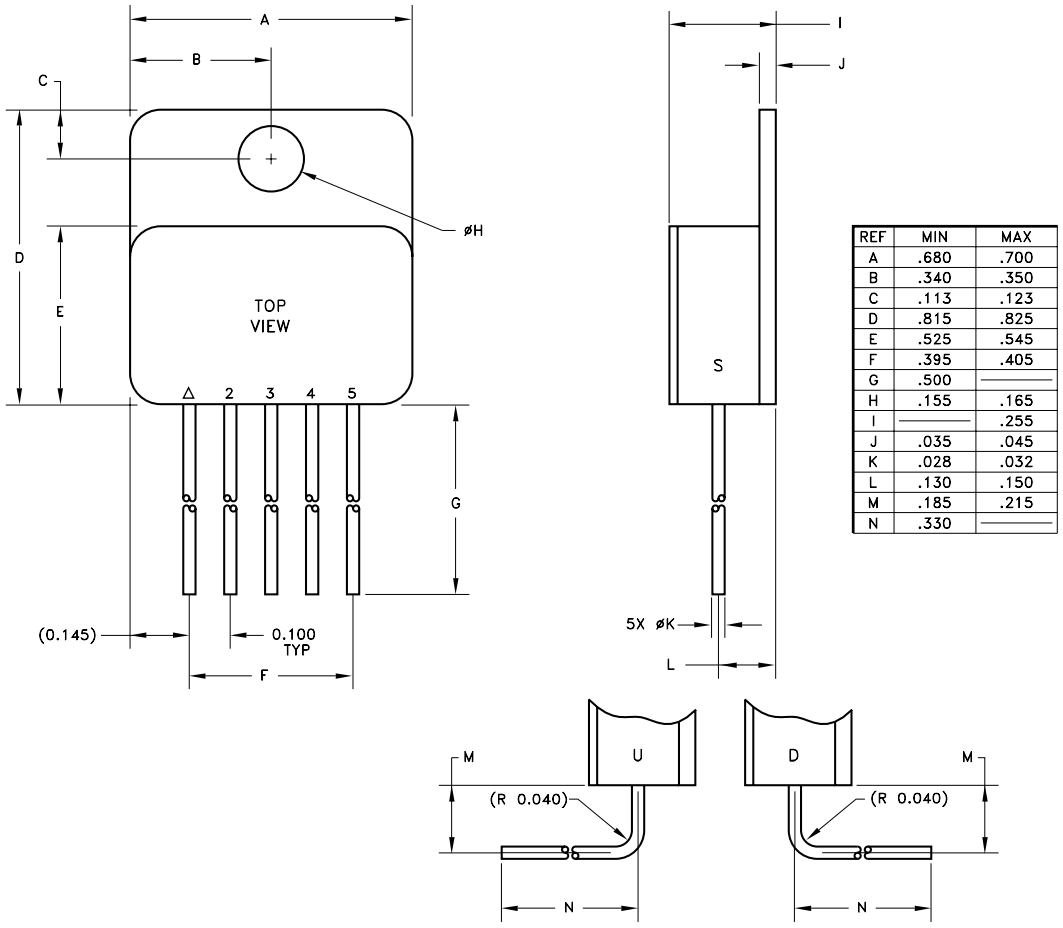








MECHANICAL SPECIFICATIONS



REF	MIN	MAX
A	.680	.700
B	.340	.350
C	.113	.123
D	.815	.825
E	.525	.545
F	.395	.405
G	.500	———
H	.155	.165
I	———	.255
J	.035	.045
K	.028	.032
L	.130	.150
M	.185	.215
N	.330	———

ESD TRIANGLE INDICATES PIN 1
WEIGHT=0.36 GRAMS TYPICAL

ALL DIMENSIONS ARE SPECIFIED IN INCHES

ORDERING INFORMATION

MSK5985 K RH U

LEAD CONFIGURATIONS
S = STRAIGHT; U = BENT UP; D = BENT DOWN

RADIATION HARDENED

SCREENING
BLANK = INDUSTRIAL
H = MIL-PRF-38534 CLASS H; K = MIL-PRF-38534 CLASS K

GENERAL PART NUMBER

The above example is a Class K regulator with leads bent up.

REVISION HISTORY

REV	STATUS	DATE	DESCRIPTION
-	Preliminary	01/14	Initial Release
A	Preliminary	01/14	Add clarifications for block diagram, electrical specificastions, max ratings and applications section.
B	Preliminary	03/14	Add clarifications, electrical specifications.
C	Preliminary	08/14	Update electrical specifications.
D	Released	10/14	Update current limit specifications.
E	Released	12/17	Add ESD Rating.

The information contained herein is believed to be accurate at the time of printing. Anaren, MSK products reserves the right to make changes to its products or specifications without notice, however and assumes no liability for the use of its products.
Please visit our website for the most recent revision of this datasheet.
Contact Anaren, MSK Products for MIL-PRF-38534 Class H, Class K qualification and radiation status.