Elektronische Bauelemente

KS05K5

VOLTAGE: 5.0 V

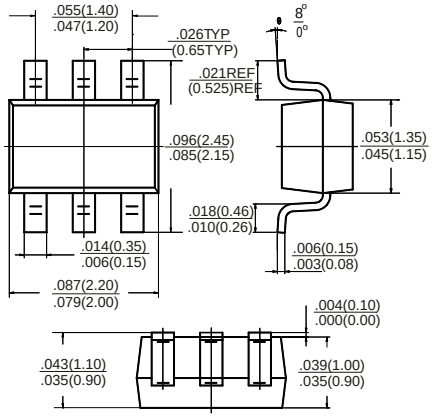
100 W Transient Voltage Suppressors Diode

RoHS Compliant Product

DESCRIPTION

. Designed to protect voltage sensitive components from ESD.
. Excellent clamping capability, low leakage and fast response.
. Cellular phones, MP3 players, digital cameras ... etc.
. Suitable for electronics where board space is a major design consideration.

SOT-363



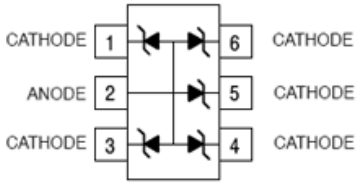
Dimensions in inches and (millimeters)

FEATURES

. Response time is typically < 1 ns
. Low leakage
. Stand-off voltage:5.0V
. ESD rating of class 3 (> 16 kV) per human body model
. IEC61000-4-2 level 4 ESD protection

MARKING CODE

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MAXIMUM RATINGS

Rating 25°C ambient temperature unless otherwise specified.

TYPE NUMBER		SYMBOL	LIMITS	UNITS
IEC61000-4-2 (ESD)	Air Contact		15 15	kV
ESD Voltage	per human body model per machine model	V_{ESD}	16 400	kV V
Lead Solder Temperature - Max. (10 sec duration)		T_L	260	°C
Thermal Resistance Junction-to-ambient		$R_{\theta JA}$	833	°C/W
Junction and Storage Temperature Range		T_J, T_{STG}	-55 ~ +150	°C
Total Power Dissipation on FR-5 board (Note 2)		P_D	150	mW

Stresses exceeding "Maximum Ratings" may damage the device. "Maximum Ratings" are stress ratings only. Functional operation above the recommended. Operating conditions is not implied. Extended exposure to stresses above the recommended operating conditions may affect device reliability.
1. FR-5 = 1.0 x 0.75 x 0.62 in.
2. Only 1 diode under power. For all 4 diodes under power, P_D will be 25%. Mounted on FR-4 board with min pad.

ELECTRICAL CHARACTERISTICS (T= 25 °C unless otherwise noted, V_F = 0.9V Max. @ I_F=10mA for all types)

TYPE NUMBER	SYMBOL	Min.	Typ.	Max.	UNIT	TEST CONDITIONS
Reverse Stand-Off Voltage	V_{RWM}	-	-	5.0	V	
Reverse Leakage Current	I_R	-	-	5.0	µA	V_{RWM} = 5 V
Peak Pulse Current	I_{PP}	-	-	8	A	
Clamping Voltage	V_C	-	-	9.5	V	I_{PP} = 1 A
Clamping Voltage	V_C	-	-	12.5	V	I_{PP} = 5 A
Reverse Breakdown Voltage	V_{BR}	6.0	-	7.2	V	I_T = 1mA, $T_{AMBIENT}$ = 25 °C
Test Current	I_T	-	1.0	-	mA	
Junction Capacitance	C	-	130	-	pF	
Peak Power Dissipation	P_{PK}	-	-	100	W	(@8x20 ms @ TA< & = 25℃; Non-repetitive current per Figure 1. Derate per Figure 2.)

ELECTRICAL CHARACTERISTIC CURVES

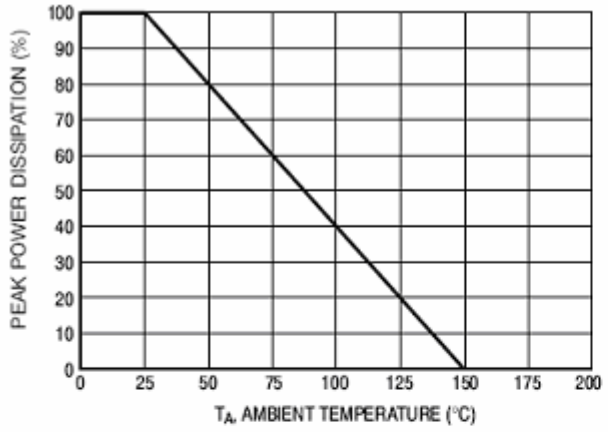


Figure 1. Pulse Derating Curve

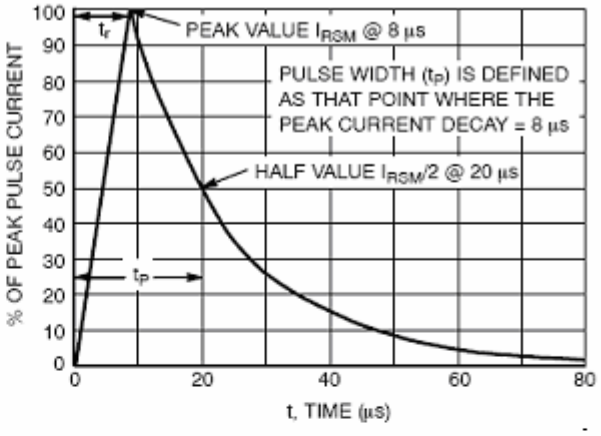


Figure 2. 8 × 20 µs Pulse Waveform

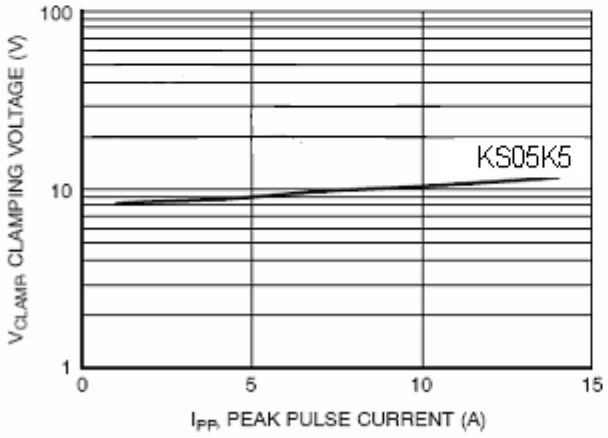


Figure 3. Clamping Voltage vs Peak Pulse Current

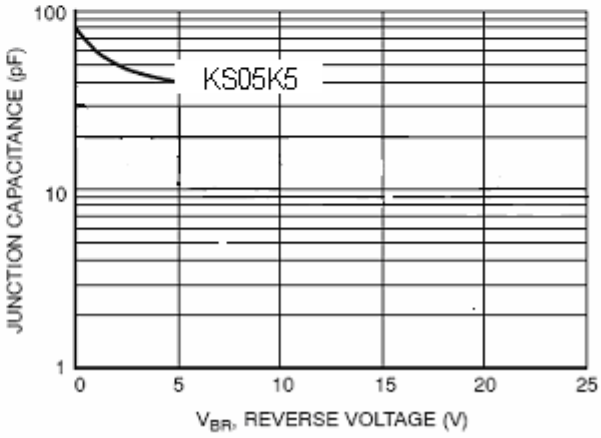


Figure 4. Junction Capacitance vs Reverse Voltage