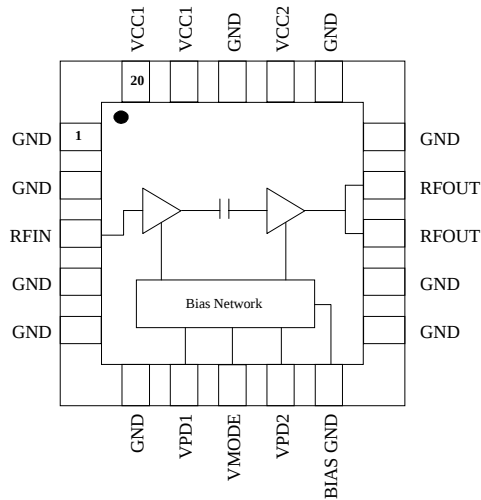
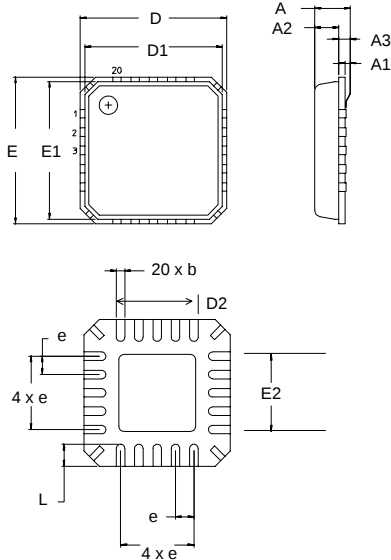




Block Diagram



FQFP-N 4 mm



PIN Configuration

PIN #	PIN Name	Description
1	GND	DC and RF Ground
2	GND	DC and RF Ground
3	RFIN	RF input to the amplifier, requires matching network
4	GND	DC and RF Ground
5	GND	DC and RF Ground
6	GND	DC and RF Ground
7	VPD1	1 st stage bias control
8	VMODE	High / Low gain selector
9	VPD2	2 nd stage bias control
10	BIAS GND	Bias Control Ground, requires RF choke
11	GND	DC and RF Ground
12	GND	DC and RF Ground
13	RF OUT	RF Output of the amplifier, requires matching network
14	RF OUT	RF Output of the amplifier, requires matching network
15	GND	DC and RF Ground
16	GND	DC and RF Ground
17	VCC2	Voltage supply for the bias network
18	GND	DC and RF Ground
19	VCC1	Voltage supply for 1 st stage, requires matching network
20	VCC1	Voltage supply for 1 st stage, requires matching network

Dim.	Measurement (mm)		
	Min.	Nom.	Max.
A	0.80	0.90	1.00
A1	0	0.02	0.05
A2	0	0.65	1.00
A3		0.25 ref.	
b	0.18	0.23	0.30
D		4.00 basic	
D1		3.75 basic	
D2	0.75	1.70	2.25
e		0.50 basic	
E		4.00 basic	
E1		3.75 basic	
E2	0.75	1.70	2.25
L	0.35	0.55	0.75

Specifications subject to change without notice.

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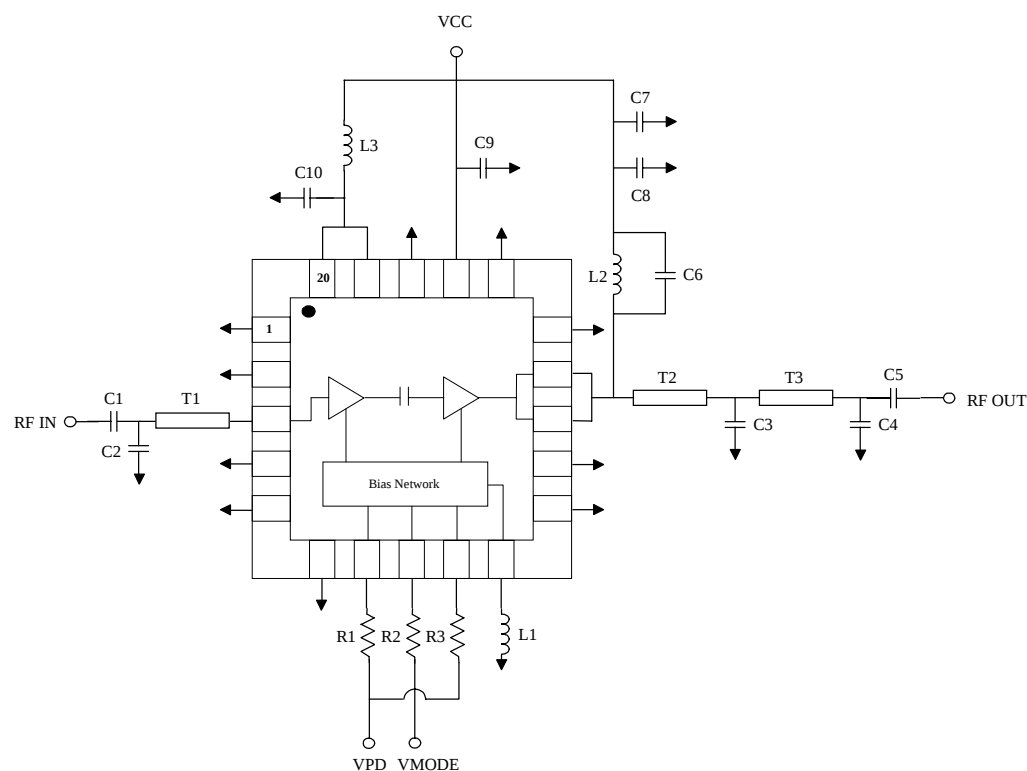
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Sample Board Schematic



External Circuitry Parts List ¹

Ref. Designation	Value	Purpose
C1	3.0 pF	RF Matching / DC Block
C2	2.7 pF	RF Matching
C3, C7	4.7 pF	RF Matching
C4	2.2 pF	RF Matching
C5	15 pF	DC Block
C8, C9	15 pF	RF Matching
C6	0.8 pF	RF Matching
C10	9.0 pF	RF Matching
L1, L2	15.0 nH	RF Choke
L3	8.2 nH	RF Choke
R1	TBD	Bias Adjust
R2	TBD	Bias Adjust
R3	TBD	Bias Adjust
T1	350 Mils	RF Matching
T2	40 Mils	RF Matching
T3	250 Mils	RF Matching

1. Values of external elements not final.

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