

P-Channel Enhancement Mode Power MOSFET

Description

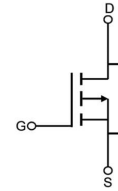
The TF1216 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

General Features

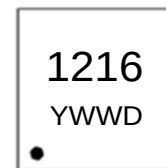
- $V_{DS} = -12V, I_D = -16A$
- $R_{DS(ON\ TYP)} = 12m\Omega @ V_{GS} = -4.5V$
- $R_{DS(ON\ TYP)} = 16m\Omega @ V_{GS} = -2.5V$
- High power and current handling capability
- Lead free product is acquired
- Surface Mount Package

Application

- Motor drive
- Load switch
- Power management

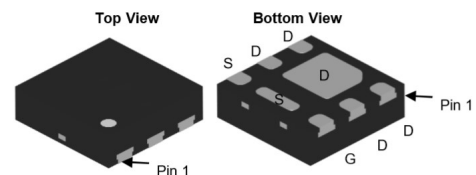


Schematic diagram



Y :year code W :week code

Marking and pin assignment



DFN 2x2 top view

Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-12	V
Gate-Source Voltage	V_{GS}	± 12	V
Drain Current-Continuous	I_D	-16	A
Drain Current-Pulsed ^{note1}	I_{DM}	-50	A
Maximum Power Dissipation	P_D	3.1	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	16	$^\circ C/W$
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Electrical Characteristics ($T_A = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = -250\mu A$	-12	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -12V, V_{GS} = 0V$	-	-	-1	μA
Gate to Body Leakage Current	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 12V$	-	-	± 100	μA

Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D = -250μA	-0.4	-0.7	-1.0	V
R _{DS(on)}	Static Drain-Source on-Resistance note2	V _{GS} = -4.5V, I _D = -6.7A	-	12	15	mΩ
		V _{GS} = -2.5V, I _D = -6.2A	-	16	20	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = -6V, V _{GS} =0V, f=1.0MHz	-	2700	-	pF
C _{oss}	Output Capacitance		-	680	-	pF
C _{rss}	Reverse Transfer Capacitance		-	590	-	pF
Q _g	Total Gate Charge	V _{DS} = -6V, I _D = -6.7A, V _{GS} = -4.5V	-	35	-	nC
Q _{gs}	Gate-Source Charge		-	5	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	10	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} = -6V, I _D = -6.7A, V _{GS} = -4.5V, R _{GEN} =2.5Ω	-	11	-	ns
t _r	Turn-on Rise Time		-	35	-	ns
t _{d(off)}	Turn-off Delay Time		-	30	-	ns
t _f	Turn-off Fall Time		-	10	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-16	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-50	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S = -16A	-	-0.8	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Typical Performance Characteristics

Figure 1: Output Characteristics

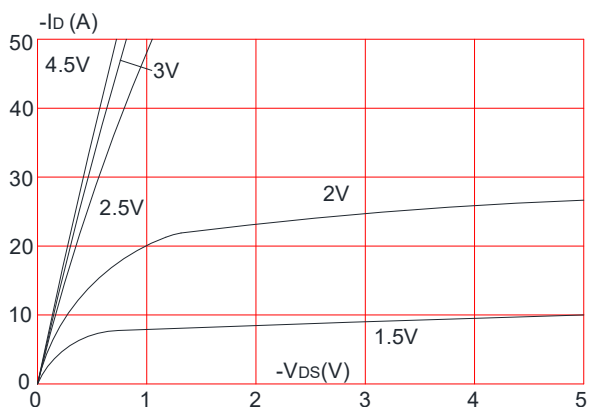


Figure 2: Typical Transfer Characteristics

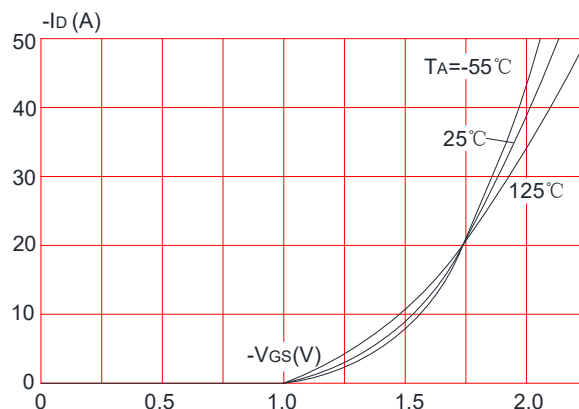


Figure 3: On-resistance vs. Drain Current

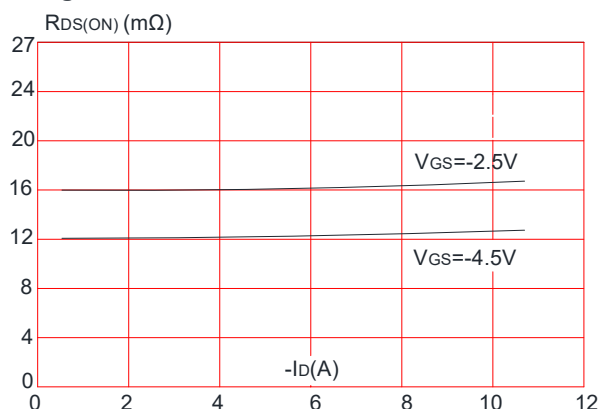


Figure 4: Body Diode Characteristics

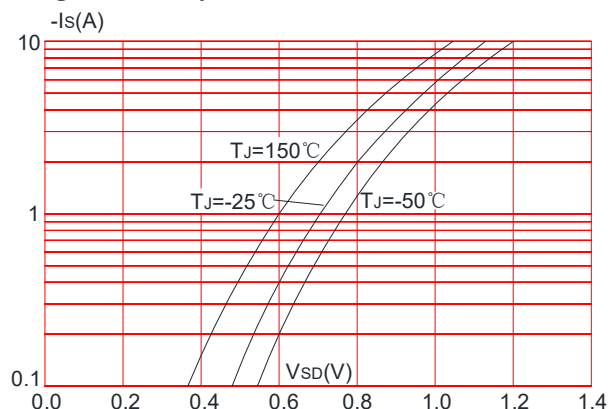


Figure 5: Gate Charge Characteristics

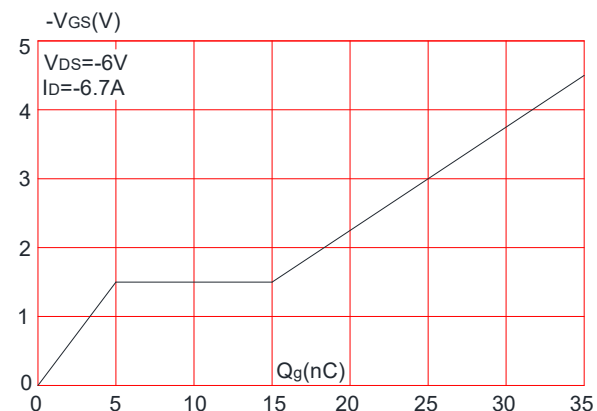


Figure 6: Capacitance Characteristics

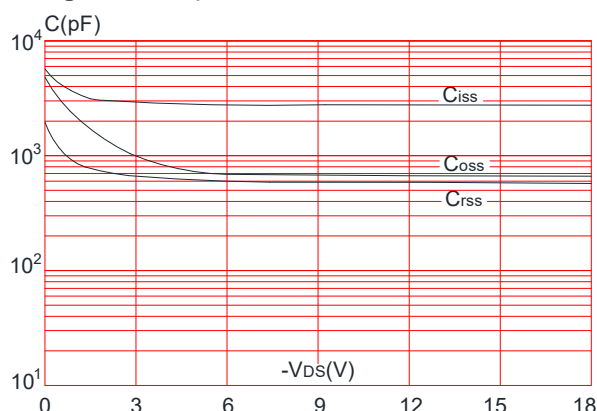


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

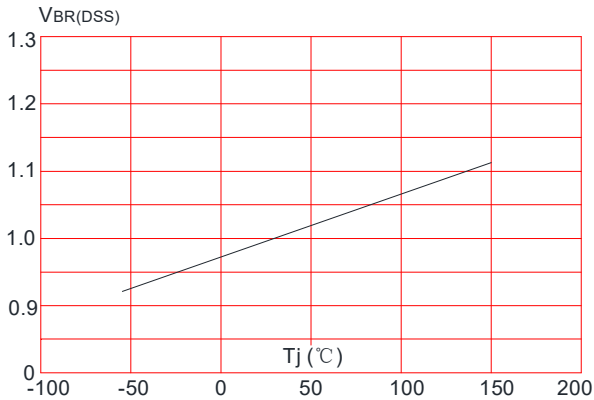


Figure 8: Normalized on Resistance vs. Junction Temperature

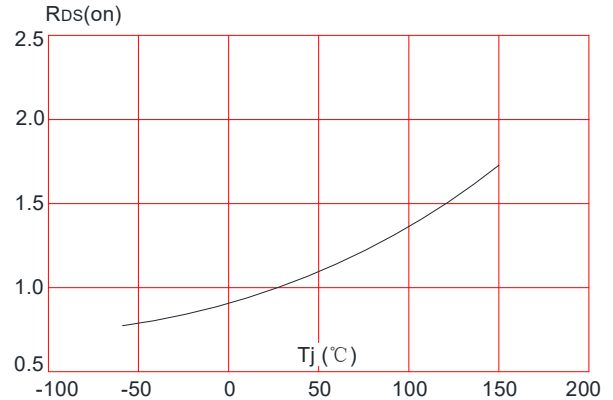


Figure 9: Maximum Safe Operating Area

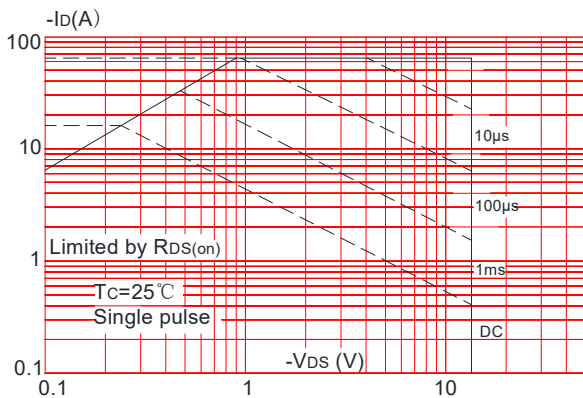


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

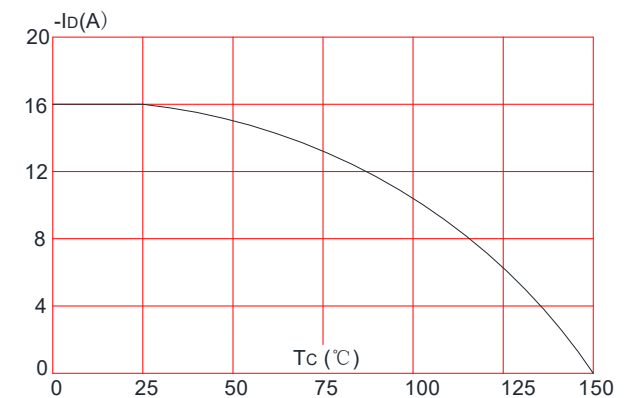
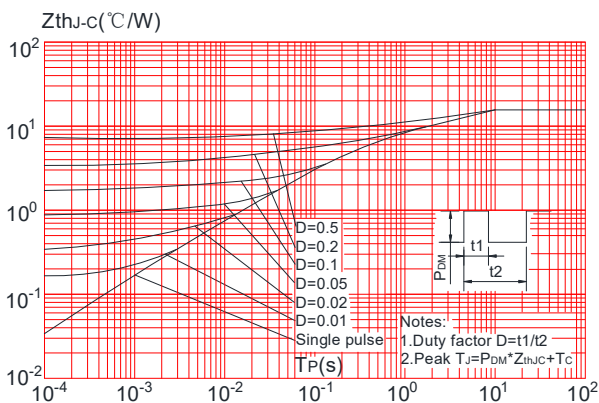
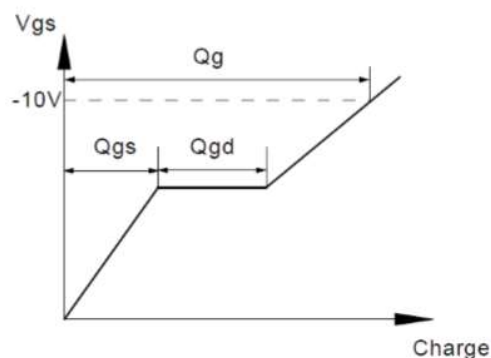
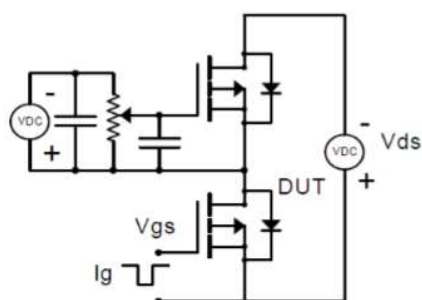


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

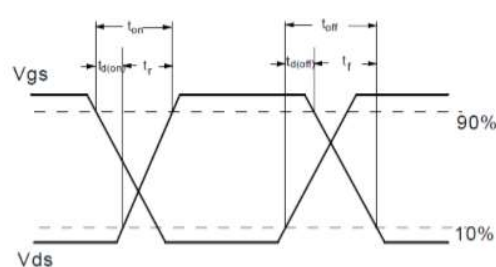
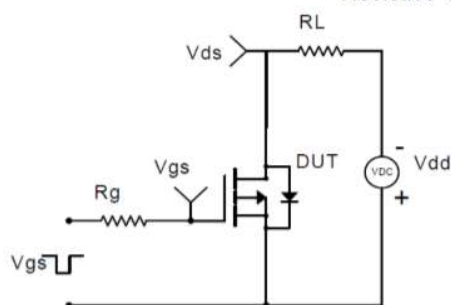


Test Circuit

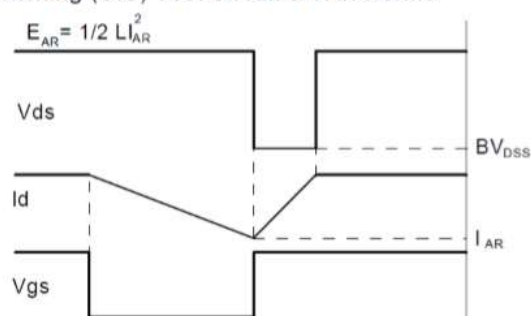
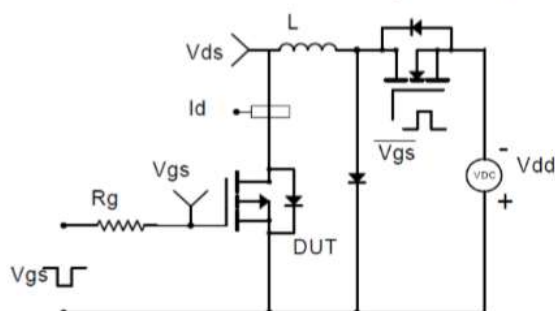
Gate Charge Test Circuit & Waveform



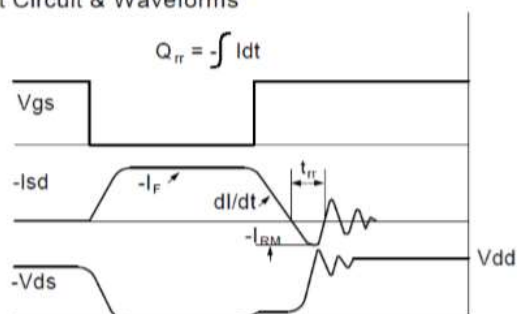
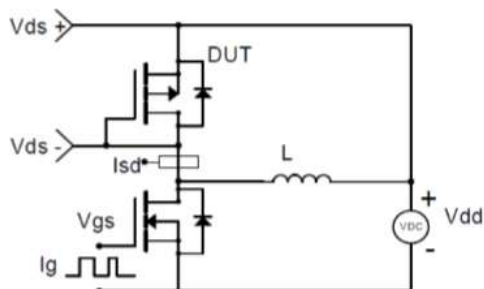
Resistive Switching Test Circuit & Waveforms



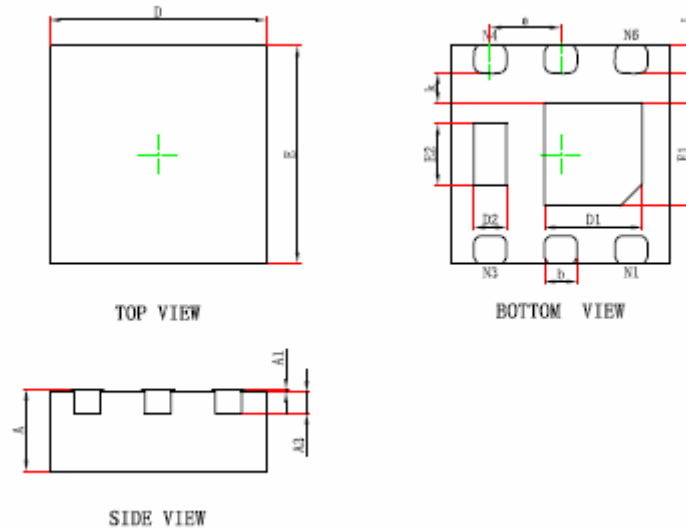
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



DFN2X2-6L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	1.924	2.076	0.076	0.082
E	1.924	2.076	0.076	0.082
D1	0.800	1.000	0.031	0.039
E1	0.850	1.050	0.033	0.041
D2	0.200	0.400	0.008	0.016
E2	0.460	0.660	0.018	0.026
k	0.200MIN.		0.008MIN.	
b	0.250	0.350	0.010	0.014
e	0.650TYP.		0.026TYP.	
L	0.174	0.326	0.007	0.013

Notes

1. All dimensions are in millimeters.
2. Tolerance $\pm 0.10\text{mm}$ (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.