



Surface Mount Transformers/Inductors, Gapped and Ungapped Custom Configurations Available



ELECTRICAL SPECIFICATIONS

Inductance Range: 10 μH to 47 000 $\mu H,$ measured at 0.10 V RMS at 10 kHz without DC current, using an HP 4263A or HP 4284A impedance analyzer



DC Resistance Range: 0.03Ω to 19.1Ω , measured at $+25 ^{\circ}\text{C} \pm 5 ^{\circ}\text{C}$ **RoHS** Rated Current Range: 2.00 amps to 0.09 amps

Dielectric Withstanding Voltage: 500 V RMS, 60 Hz, 5 seconds

9 0
DIMENSIONS in inches [millimeters]
Pad Layout 0.045 [1.14]
Dimensional Outline
0.421 [10.69] Max. 0.492 [12.50] Max. 0.248 [6.30] Max.
0.079
[.71] Foot print Diagram

NOTE: Pad layout guidelines per MIL-STD-275E (printed wiring for electronic equipment). Tolerances: $xx \pm 0.01$ " [± 0.25 mm]; $xxx \pm 0.005$ " [± 0.12 mm]

The underside of these components contains metal and thus should not come in contact with active circuit traces.

STANDARD ELECTRICAL SPECIFICATIONS							
MODEL	IND. (µH)	IND. TOL.	SCHEMATIC LETTER	DCR MAX. (Ohms)	MAX. RATED* DC CURRENT (Amps)	SATURATING CURRENT** (Amps)	
Ungapped Models (A) LPE4841ER101NU LPE4841ER151NU LPE4841ER221NU LPE4841ER331NU LPE4841ER471NU LPE4841ER681NU	100 150 220 330 470 680	±30 % ±30 % ±30 % ±30 % ±30 % ±30 %	A A A A A A	0.17 0.21 0.25 0.30 0.36 0.44	0.88 0.79 0.721 0.65 0.60 0.54	N/A N/A N/A N/A N/A N/A	
LPE4841ER102NU LPE4841ER152NU LPE4841ER222NU LPE4841ER332NU LPE4841ER472NU LPE4841ER682NU	1000 1500 2200 3300 4700 6800	±30 % ±30 % ±30 % ±30 % ±30 % ±30 %	A A A A A	0.53 0.65 0.79 1.55 1.85 4.36	0.49 0.45 0.40 0.29 0.26 0.17	N/A N/A N/A N/A N/A	
LPE4841ER103NU LPE4841ER153NU LPE4841ER223NU LPE4841ER333NU LPE4841ER473NU	10 000 15 000 22 000 33 000 47 000	±30 % ±30 % ±30 % ±30 % ±30 %	A A A A	5.29 6.48 13.1 16.0 19.1	0.16 0.14 0.10 0.09 0.08	N/A N/A N/A N/A N/A	
Gapped Models (B) LPE4841ER100MG LPE4841ER150MG LPE4841ER20MG LPE4841ER330MG LPE4841ER470MG LPE4841ER680MG	10 15 22 33 47 68	±20 % ±20 % ±20 % ±20 % ±20 % ±20 %	ВВССООО	0.03 0.04 0.07 0.09 0.13 0.21	2.03 1.84 1.32 1.20 0.98 0.79	2.320 1.925 1.610 1.330 1.125 0.941	
LPE4841ER101MG LPE4841ER151MG LPE4841ER221MG LPE4841ER331MG LPE4841ER471MG LPE4841ER681MG	100 150 220 330 470 680	±20 % ±20 % ±20 % ±20 % ±20 % ±20 %	E E E E	0.35 0.48 0.73 1.14 1.36 2.07	0.58 0.52 0.42 0.34 0.31 0.25	0.781 0.641 0.532 0.436 0.366 0.305	
LPE4841ER102MG LPE4841ER152MG LPE4841ER222MG LPE4841ER332MG LPE4841ER472MG	1000 1500 2200 3300 4700	±20 % ±20 % ±20 % ±20 % ±20 %	E E E E	3.15 4.76 7.29 11.7 17.7	0.20 0.16 0.13 0.11 0.09	0.252 0.206 0.170 0.139 0.117	

 $^{^{\}star}$ DC current that will create a maximum temperature rise of 30 °C when applied at + 25 °C ambient. ** DC current that will typically reduce the initial inductance by 20 %.

UNGAPPED MODELS: Highest possible inductance with the lowest DCR and highest Q capability. Beneficial in filter, impedance matching and line coupling devices.

GAPPED MODELS: Capable of handling large amounts of DC current, tighter inductance tolerance with better temperature stability than ungapped models. Beneficial in DC to DC converters or other circuits carrying DC currents or requiring inductance stability over a temperature range.

DESCRIPTION													
LPE MODEL	4841 SIZE	1000 μH INDUCTANCE VA	LUE	INDUCTA	± 30 % NCE TC		A CORE	E PAC	ER CKAGE COD	E JEDEC L		e2)-FREE ST	ANDARD
GLOBAL PART NUMBER													
	L	P E ODUCT FAMILY	4	8 SI	4 ZE	1	PACKA COD		INDUC	0 2 CTANCE LUE	N TOL.	CORE	

NOTE Series is also available with SnPb terminations by using package code RY for tape and reel (in place of ER) or SM for bulk (in place of EB).

Vishay Dale

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SCHEMATIC (TOP VIEW)							
Schematic A	Schematic B	Schematic C	Schematic D	Schematic E			
5 © 4 6 ©	5 0 4 6 0 0 3 7 0 0 2 8 0 1	5 © © 4 6 © ~~~~ © 3 7 © ~~~ © 2 8 © ~~~ 0 1	5 0	5 0 0 4 6 0 0 3 7 0 0 2 8 0 0 1			

NOTE: Schematic A is for Ungapped LPE Series

ENVIRONMENTAL PERFORMANCE					
TEST	CONDITIONS				
Thermal Cycling	Withstands - 55 °C to + 125 °C				
Operating Temperature	- 55 °C to + 125 °C*				
High Humidity	85 %				
Soldering Heat	Tested to + 230 °C				
Mechanical Shock	Per MIL-STD-202, Method 213 (100G)				
Vibration	Per MIL-STD-202, Method 204 (20G)				
Solderability	Per industry standards				

^{*} Must be checked in end use application

PART MARKING

- Vishay Dale
- Date code
- Marking code (Suffix of model #)
- Pin 1 indicator

DA	CK.	ΔC	ING
FA		40	114

TAPE SPECIFICATIONS:

Carrier Tape Type: Conductive Cover Tape Type: Anti-static

Cover Tape Adhesion to Carrier: 40 ± 30 grams

REEL SPECIFICATIONS:

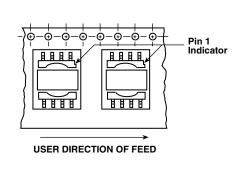
Diameter (flange): 13" [330.2 mm]

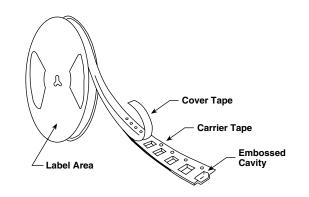
Maximum Width (over flanges): 1.197" [30.4 mm]

STANDARDS: All embossed carrier tape packaging will be accomplished in compliance with latest revision of EIA-481 Taping of Surface Mount Components for Automatic Placement".

	TAPE	COMPONENT	UNITS PER
MODEL	WIDTH	PITCH	13 INCH REEL
I PF-4841	24 mm	16 mm	600







NOTE: Top view shown with cover tape removed

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