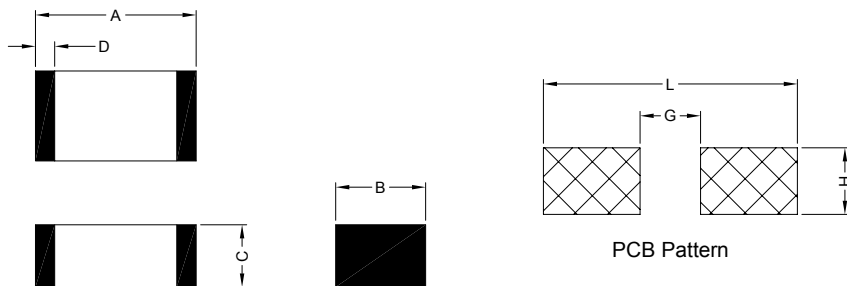


1. PART NO. EXPRESSION :

Z 4 C 1 2 1 - R G - 1 0
 (a)(b)(c) (d) (e)(f) (g)

- (a) Series code
 (b) Dimension code
 (c) Material code
 (d) Impedance code : 121 = 120Ω
 (e) R : Reel
 (f) Current code : G = 700mA
 (g) 10 : Lead Free

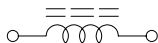
2. CONFIGURATION & DIMENSIONS :



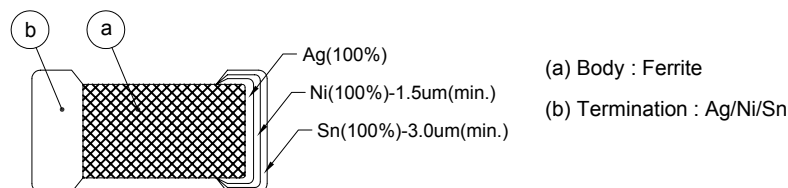
Unit:m/m

A	B	C	D	G	H	L
3.20±0.20	1.60±0.20	1.10±0.20	0.50±0.30	2.20 Ref.	1.40 Ref.	4.40 Ref.

3. SCHEMATIC :



4. MATERIALS :



- (a) Body : Ferrite
 (b) Termination : Ag/Ni/Sn

5. GENERAL SPECIFICATION :

- a) Temp. rise : 30°C Max.
 b) Rated current : Base on temp. rise
 c) Storage temp. : -55°C to +125°C
 d) Operating temp. : -55°C to +125°C
 e) Resistance to solder heat : 260°C.10secs



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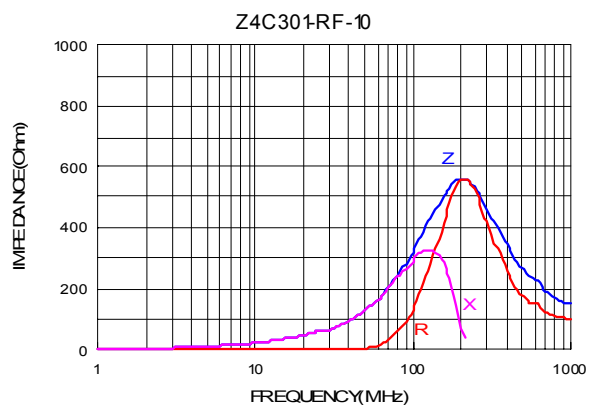
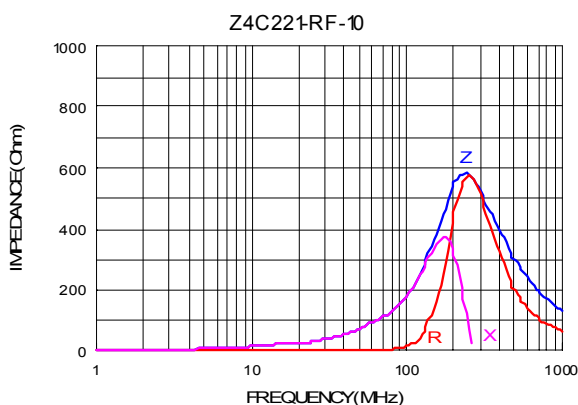
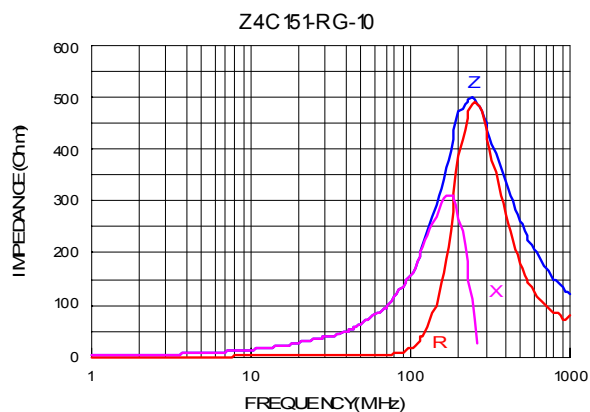
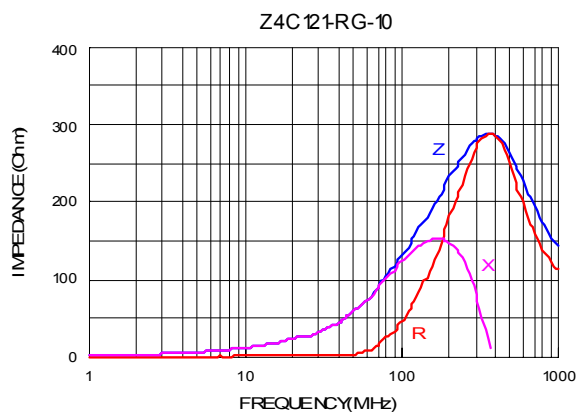


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6. ELECTRICAL CHARACTERISTICS :

Part Number	EIA Size	Impedance (Ω)	Test Frequency (MHz)	DC Resistance (Ω) Max.	Rated Current (mA) Max.
Z4C121-RG-10	1206	120 $\pm$ 25%	100	0.25	700
Z4C151-RG-10	1206	150 $\pm$ 25%	100	0.25	700
Z4C221-RF-10	1206	220 $\pm$ 25%	100	0.30	600
Z4C301-RF-10	1206	300 $\pm$ 25%	100	0.35	600
Z4C471-RF-10	1206	470 $\pm$ 25%	100	0.40	550
Z4C601-RE-10	1206	600 $\pm$ 25%	100	0.50	500
Z4C801-RD-10	1206	800 $\pm$ 25%	100	0.50	400
Z4C102-RC-10	1206	1000 $\pm$ 25%	100	0.55	300

7. IMPEDANCE VS. FREQUENCY CURVES :



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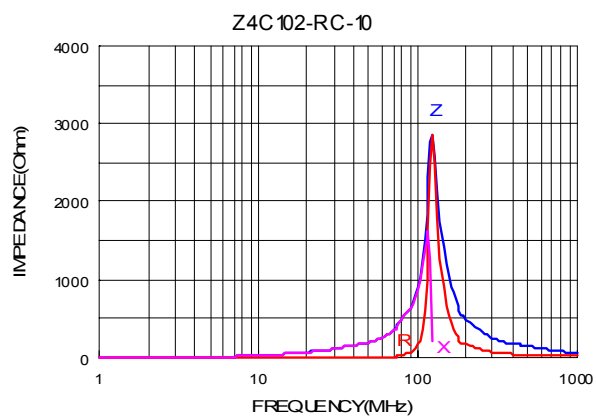
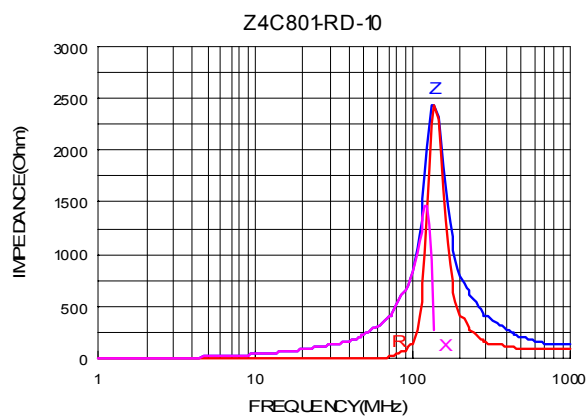
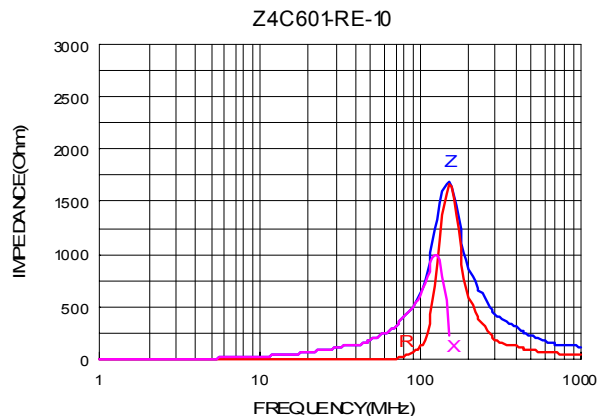
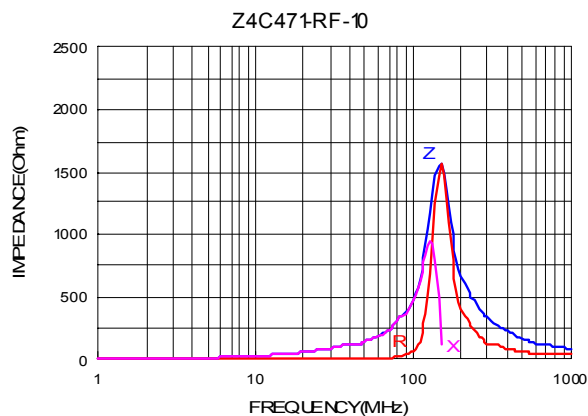
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PG. 2

7. IMPEDANCE VS. FREQUENCY CURVES :



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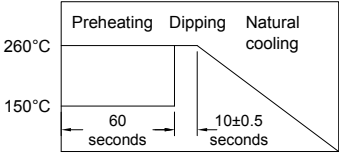
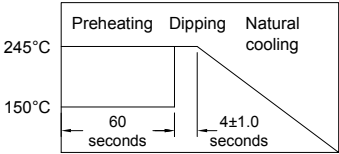
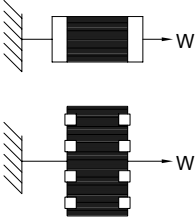
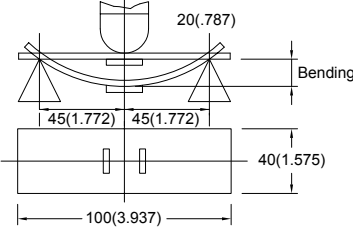
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PG. 3

8. RELIABILITY & TEST CONDITION :

ITEM	PERFORMANCE	TEST CONDITION																											
Electrical Characteristics Test																													
Impedance	Refer to standard electrical characteristics list	HP4291A, HP4287A+16092A																											
DC Resistance		HP4338B																											
Rated Current																													
Temperature Rise Test	30°C max. (Δt)	1. Applied the allowed DC current. 2. Temperature measured by digital surface thermometer.																											
Solder Heat Resistance	Appearance : No significant abnormality Impedance change : Within $\pm 30\%$ No mechanical damage Remaining terminal electrode : 70% min.  The diagram shows a temperature profile for solder heat resistance. It starts with preheating at 150°C for 60 seconds, followed by dipping into a solder bath at 260°C for 10±0.5 seconds, and then natural cooling.	Preheat : 150°C, 60sec. Solder : Sn-Ag3.0-Cu0.5 Solder Temperature : 260±5°C Flux for lead free : rosin Dip Time : 10±0.5sec.																											
Solderability	More than 90% of the terminal electrode should be covered with solder.  The diagram shows a temperature profile for solderability. It starts with preheating at 150°C for 60 seconds, followed by dipping into a solder bath at 245°C for 4±1.0 seconds, and then natural cooling.	Preheat : 150°C, 60sec. Solder : Sn-Ag3.0-Cu0.5 Solder Temperature : 245±5°C Flux for lead free : rosin Dip Time : 4±1sec.																											
Terminal Strength	The terminal electrode & the dielectric must not be damaged by the forces applied on the right conditions.  The diagram shows two cross-sectional views of a ferrite chip bead. In the top view, a force 'W' is applied to the terminal electrode. In the bottom view, a force 'W' is applied to the dielectric body.	For Z Series : <table border="1"> <thead> <tr> <th>Size</th><th>Force (Kgf)</th><th>Time (sec)</th></tr> </thead> <tbody> <tr> <td>1</td><td>0.2</td><td></td></tr> <tr> <td>2</td><td>0.5</td><td></td></tr> <tr> <td>3</td><td>0.6</td><td></td></tr> <tr> <td>4</td><td>1.0</td><td>> 25</td></tr> <tr> <td>5</td><td>1.0</td><td></td></tr> <tr> <td>6</td><td>1.0</td><td></td></tr> <tr> <td>7</td><td>1.5</td><td></td></tr> <tr> <td>8</td><td>2.0</td><td></td></tr> </tbody> </table>	Size	Force (Kgf)	Time (sec)	1	0.2		2	0.5		3	0.6		4	1.0	> 25	5	1.0		6	1.0		7	1.5		8	2.0	
Size	Force (Kgf)	Time (sec)																											
1	0.2																												
2	0.5																												
3	0.6																												
4	1.0	> 25																											
5	1.0																												
6	1.0																												
7	1.5																												
8	2.0																												
Flexture Strength	The terminal electrode & the dielectric must not be damaged by the forces applied on the right conditions.  The diagram shows a cross-sectional view of a ferrite chip bead being bent. Dimensions are given in mm and inches: 20(.787) for the top width, 45(1.772) for the bottom width, 40(1.575) for the height, and 100(3.937) for the total length. The bending is indicated by a curved arrow.	Solder a chip on a test substrate, bend the substrate by 2mm (0.079in) and return.																											



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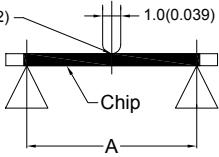
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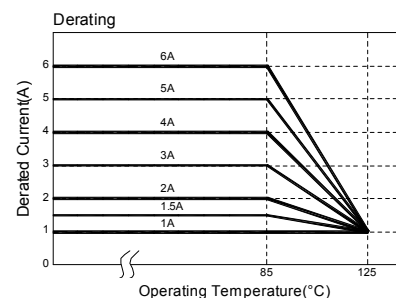
PG. 4

8. RELIABILITY & TEST CONDITION :

ITEM	PERFORMANCE	TEST CONDITION																								
Bending Strength	The ferrite should not be damaged by forces applied on the right condition. 	<table border="1"> <thead> <tr> <th>Series name</th><th>mm (inches)</th><th>P-Kgf</th></tr> </thead> <tbody> <tr> <td>Z2</td><td>0.80 (0.033)</td><td>0.3</td></tr> <tr> <td>Z3</td><td>1.40 (0.055)</td><td>1.0</td></tr> <tr> <td>Z4</td><td>2.00 (0.079)</td><td>2.5</td></tr> <tr> <td>Z5</td><td></td><td></td></tr> <tr> <td>Z6</td><td>2.70 (0.106)</td><td>2.5</td></tr> <tr> <td>Z7</td><td></td><td></td></tr> <tr> <td>Z8</td><td></td><td></td></tr> </tbody> </table>	Series name	mm (inches)	P-Kgf	Z2	0.80 (0.033)	0.3	Z3	1.40 (0.055)	1.0	Z4	2.00 (0.079)	2.5	Z5			Z6	2.70 (0.106)	2.5	Z7			Z8		
Series name	mm (inches)	P-Kgf																								
Z2	0.80 (0.033)	0.3																								
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Z4	2.00 (0.079)	2.5																								
Z5																										
Z6	2.70 (0.106)	2.5																								
Z7																										
Z8																										
Random Vibration Test	Appearance : Cracking, shipping & any other defects harmful to the characteristics should not be allowed. Impedance : Within $\pm 30\%$	Frequency : 10-55-10Hz for 1 min. Amplitude : 1.52mm Directions & times : X, Y, Z directions for 2 hours. A period of 2 hours in each of 3 mutually perpendicular directions (Total 6 hours).																								
Drop	Drop 10 times on a concrete floor from a height of 75cm.	a. No mechanical damage b. Impedance change : $\pm 30\%$																								
Loading at High Temperature	Appearance : No damage. Impedance : Within $\pm 30\%$ of initial value.	Temperature : $125 \pm 5^\circ\text{C}$ Applied Current : rated current Duration : 500 ± 12hrs Measured at room temperature after placing for 2 to 3hrs.																								
Humidity		Humidity : 90~95% RH. Temperature : $40 \pm 2^\circ\text{C}$ Duration : 500 ± 12hrs Measured at room temperature after placing for 2 to 3hrs.																								
Thermal Shock	Appearance : No damage. Impedance : Within $\pm 30\%$ of initial value. <table border="1"> <thead> <tr> <th>Phase</th><th>Temperature ($^\circ\text{C}$)</th><th>Times (min.)</th></tr> </thead> <tbody> <tr> <td>1</td><td>$-55 \pm 2^\circ\text{C}$</td><td>30 ± 3</td></tr> <tr> <td>2</td><td>$+125 \pm 5^\circ\text{C}$</td><td>30 ± 3</td></tr> </tbody> </table> Measured : 5 times	Phase	Temperature ($^\circ\text{C}$)	Times (min.)	1	$-55 \pm 2^\circ\text{C}$	30 ± 3	2	$+125 \pm 5^\circ\text{C}$	30 ± 3	For Z Series : Condition for 1 cycle Step1 : $-55 \pm 2^\circ\text{C}$ 30 ± 3 min. Step2 : $+125 \pm 5^\circ\text{C}$ 30 ± 3 min. Number of cycles : 5 Measured at room temperature after placing for 2 to 3hrs.															
Phase	Temperature ($^\circ\text{C}$)	Times (min.)																								
1	$-55 \pm 2^\circ\text{C}$	30 ± 3																								
2	$+125 \pm 5^\circ\text{C}$	30 ± 3																								
Low temperature storage test		Temperature : $-55 \pm 2^\circ\text{C}$ Duration : 500 ± 12hrs Measured at room temperature after placing for 2 to 3hrs.																								
Drop	Drop 10 times on a concrete floor from a height of 75cm.	a. No mechanical damage b. Impedance change : $\pm 30\%$																								

Derating Curve

For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over 85°C , the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



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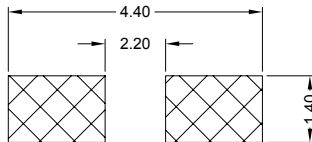
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9. SOLDERING AND MOUNTING :

9-1. Recommended PC Board Pattern



PC board should be designed so that products are not sufficient under mechanical stress as warping the board. Products shall be positioned in the sideways direction against the mechanical stress to prevent failure.

9-2. Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. The terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

9-2.1 Lead Free Solder Re-flow :

Recommended temperature profiles for re-flow soldering in Figure 1.

9-2.2 Solder Wave :

Wave soldering is perhaps the most rigorous of surface mount soldering processes due to the steep rise in temperature seen by the circuit when immersed in the molten solder wave, typical at 230°C. Due to the risk of thermal damage to products, wave soldering of large size products is discouraged. Recommended temperature profile for wave soldering is shown in Fig. 2

9-2.3 Soldering Iron (Figure 3) :

Products attachment with soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

Note :

- a) Preheat circuit and products to 150°C.
- b) 350°C tip temperature (max)
- c) Never contact the ceramic with the iron tip
- d) 1.0mm tip diameter (max)
- e) Use a 20 watt soldering iron with tip diameter of 1.0mm
- f) Limit soldering time to 3 secs.

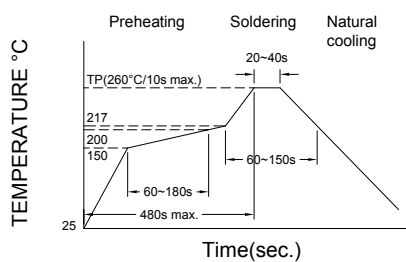


Figure 1. Re-flow Soldering

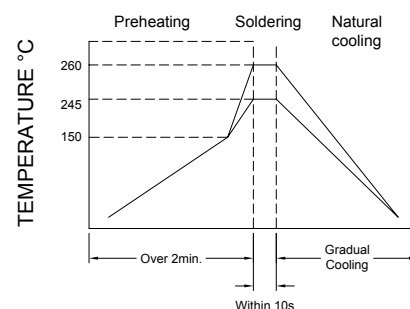


Figure 2. Wave Soldering

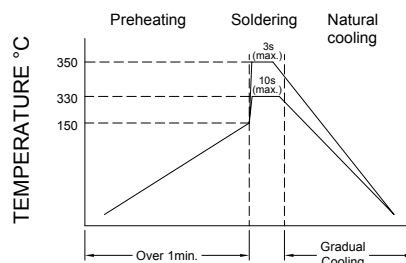


Figure 3. Hand Soldering



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9-3. Solder Volume

Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in Fig. 4.

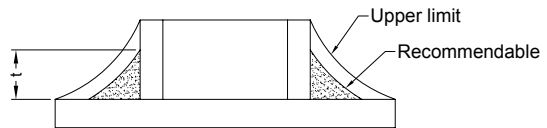


Figure 4



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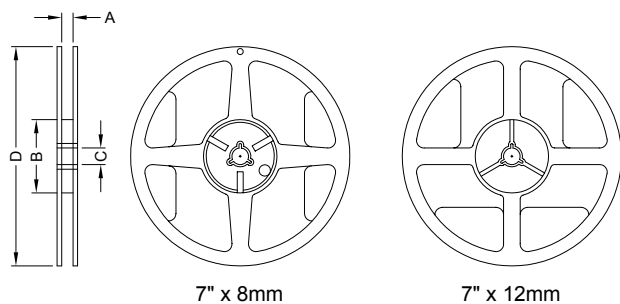
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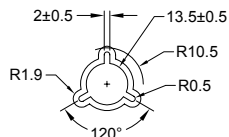
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10. PACKAGING INFORMATION :

10-1. Reel Dimension

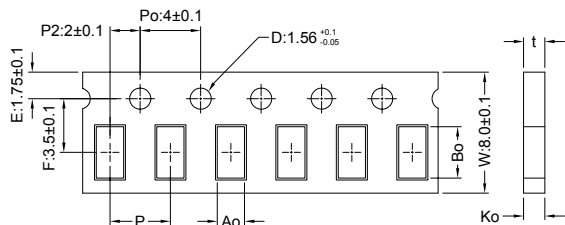


Type	A(mm)	B(mm)	C(mm)	D(mm)
7" x 8mm	9.0±0.5	60.0±2.0	13.5±0.5	178.0±2.0
7" x 12mm	13.5±0.5	60.0±2.0	13.5±0.5	178.0±2.0



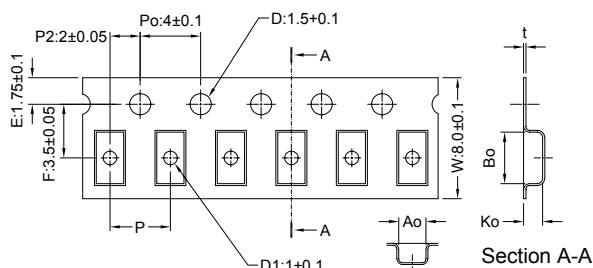
10-2 Tape Dimension / 8mm

Material : Paper



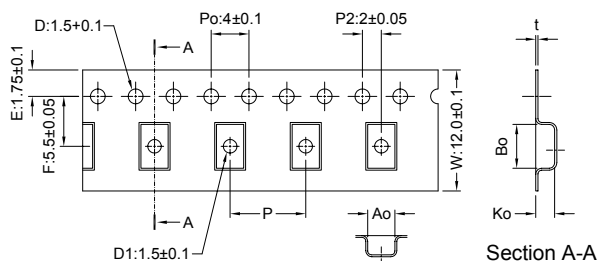
Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
Z / L	1	1.12±0.03	0.62±0.03	0.60±0.03	2.0±0.1	0.60±0.03	none
	2	1.85±0.05	1.05±0.05	0.95±0.05	4.0±0.1	0.95±0.05	none
	3(09)	2.30±0.05	1.50±0.05	0.95±0.05	4.0±0.1	0.95±0.05	none

Material : Plastic



Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
Z / L	2	1.95±0.10	1.05±0.10	1.05±0.10	4.0±0.1	0.23±0.05	none
	3(09)	2.25±0.10	1.42±0.10	1.04±0.10	4.0±0.1	0.22±0.05	1.0±0.1
	3(12)	2.35±0.10	1.50±0.10	1.45±0.10	4.0±0.1	0.22±0.05	1.0±0.1
	4	3.50±0.10	1.88±0.10	1.27±0.10	4.0±0.1	0.22±0.05	1.0±0.1
	5	3.42±0.10	2.77±0.10	1.55±0.10	4.0±0.1	0.22±0.05	1.0±0.1

10-2.1 Tape Dimension / 12mm



Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
Z / L	6	4.95±0.1	1.93±0.1	1.93±0.1	4.0±0.1	0.24±0.05	1.5±0.1
	7	4.95±0.1	3.66±0.1	1.85±0.1	8.0±0.1	0.24±0.05	1.5±0.1
	8	6.10±0.1	5.40±0.1	2.00±0.1	8.0±0.1	0.30±0.05	1.5±0.1



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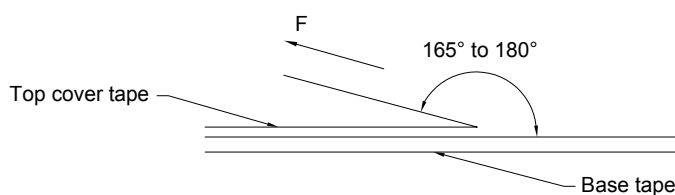
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10-3. Packaging Quantity

Chip Size	8	7	6	5	4	3 (12)	3 (09)	2	1
Chip / Reel	1000	1000	2000	2500	3000	2000	4000	4000	10000
Inner Box	4000	4000	8000	12500	15000	10000	20000	20000	50000
Middle Box	20000	20000	40000	62500	75000	50000	100000	100000	250000
Carton	40000	40000	80000	125000	150000	100000	200000	200000	500000
Bulk (Bags)	7000	12000	20000	30000	50000	100000	150000	200000	300000

10-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed (mm/min)
5~35	45~85	860~1060	300

Application Notice

1. Storage Conditions :

To maintain the solderability of terminal electrodes :

- Temperature and humidity conditions : -10 ~ 40°C and 30 ~ 70% RH.
- Recommended products should be used within 6 months from the time of delivery.
- The packaging material should be kept where no chlorine or sulfur exists in the air.

2. Transportation :

- Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- The use of tweezers or vacuum pick up is strongly recommended for individual components.
- Bulk handling should ensure that abrasion and mechanical shock are minimized.



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