

1.6GHz Low Noise Amplifier

■ GENERAL DESCRIPTION

XC2404 series is an ultra-low-noise amplifier (LNA) with low operating voltage, low noise figure, low power consumption. The device offers easy output matching to 50Ω for input and output with less external components.

The device operates at 1.2V. For higher power supplies such as 1.8V and 2.85V, the device can operate with a self bias of one adding resistor.

■ APPLICATIONS

- GPS band RF signal amplified

■ FEATURES

Noise Figure	: NF=0.94dB(TYP.) @ 1.575GHz
Low Power Consumption	: 12.0mW(TYP.) @ V _{DD} =1.2V, Fixed bias
High Gain	: S ₂₁ =26.5dB(TYP.) @ 1.575GHz
Operation Voltage Range	: 1.14V ~ 1.26V @ Fixed bias
Output	: CMOS Output, 50Ω driver built-in
Operating Temperature Range	: -40 ~ +85
Ultra Small Package	: USP-8A01
Environmentally Friendly	: EU RoHS Compliant, Pb Free

■ TYPICAL APPLICATION CIRCUIT

Figure 1: Fixed Bias

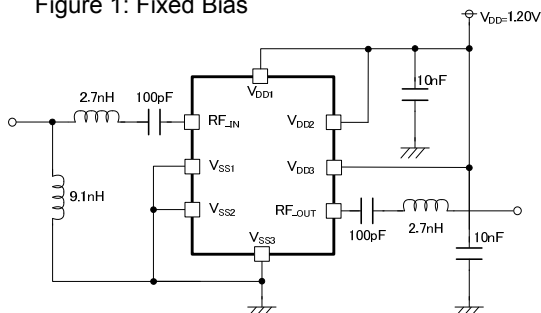
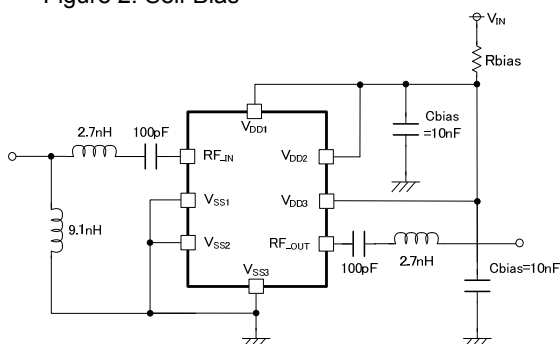


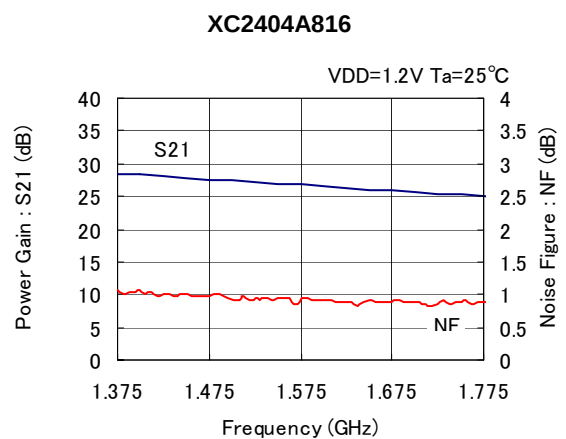
Figure 2: Self Bias



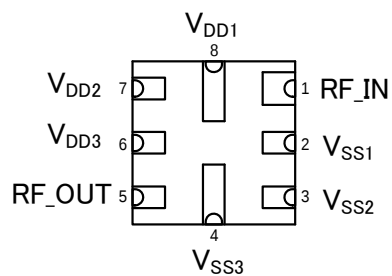
V _{IN} [V]	R _{BIAS} [Ω]
3.00	270
2.85	240
1.80	82

* R_{BIAS} should be in ±1% tolerance and ±200ppm/ temperature stability.
C_{BIAS} is 10nF.

■ TYPICAL PERFORMANCE CHARACTERISTICS



PIN CONFIGURATION



USP-8A01
(BOTTOM VIEW)

PIN ASSIGNMENT

PIN NUMBER USP-8A01	PIN NAME	FUNCTION
1	RF_IN	RF Signal Input
2	V _{SS1}	Ground
3	V _{SS2}	Ground
4	V _{SS3}	Ground
5	RF_OUT	RF Signal Output
6	V _{DD3}	Power Supply
7	V _{DD2}	Power Supply
8	V _{DD1}	Power Supply

PRODUCT CLASSIFICATION

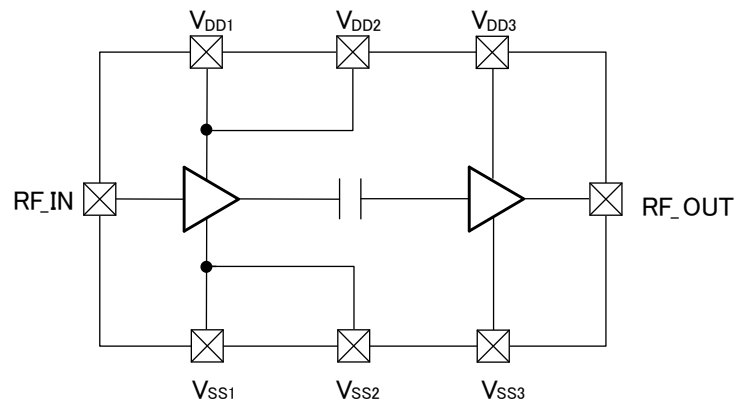
Ordering Information

XC2404A816 - ^(*)

DESIGNATOR	DESCRIPTION	SYMBOL	DESCRIPTON
-	Packages Taping Type	UR-G	USP-8A01

(*) The "-G" suffix indicates that the products are Halogen and Antimony free as well as being fully RoHS compliant.

■ BLOCK DIAGRAMS



■ ABSOLUTE MAXIMUM RATINGS

Ta=25

PARAMETER	SYMBOL	RATINGS	UNITS
Supply Voltage	V_{DD}	1.60	V
Current Circuit	I_{DD}	42	mA
RF Input Power	P_{IN}	10	dBm
Power Dissipation	P_d	120	mW
Operating Temperature Range	Ta	-40 ~ +85	°C
Storage Temperature Range	Tstg	-55 ~ +125	°C

■ ELECTRICAL CHARACTERISTICS

● DC Characteristics

Fixed Bias (refer to TYPICAL APPLICATION CIRCUIT, Figure 1)

Ta=25

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUITS
Power Supply Pin Voltage	V _{DD}	-	1.14	1.20	1.26	V	
Current Circuit	I _{DD}	V _{DD} =1.2V	4.00	10.0	18	mA	

Self Bias (refer to TYPICAL APPLICATION CIRCUIT, Figure 2)

Ta=25

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUITS
Input Power Supply Voltage 1	V _{IN1}	R _{BIAS} =270Ω ± 1%, ± 200ppm/	2.850	3.000	3.150	V	
Input Power Supply Voltage 2	V _{IN2}	R _{BIAS} =240Ω ± 1%, ± 200ppm/	2.708	2.850	2.992	V	
Input Power Supply Voltage 3	V _{IN3}	R _{BIAS} =82Ω ± 1%, ± 200ppm/	1.710	1.800	1.890	V	
Power Supply Pin Voltage	V _{DD}	V _{IN} = V _{IN1} , V _{IN2} , V _{IN3}	0.90	1.12	1.32	V	
Current Circuit	I _{DD}	V _{IN} = V _{IN1} , V _{IN2} , V _{IN3}	5.25	7.25	11.0	mA	

* When the device is used in self bias, please use the specified R_{BIAS} and C_{BIAS}.

■ ELECTRICAL CHARACTERISTICS (Continued)

● AC Characteristics

 $V_{DD}=1.2V$, $T_a=25$

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUITS
Power Gain	S21	$f=1.575GHz$	24	26.5	-	dB	
Input Return Loss	S11	$f=1.575GHz$	-	10	-	dB	
Output Return Loss	S22	$f=1.575GHz$	-	17	-	dB	
Isolation	S12	$f=1.575GHz$	-	-36	-	dB	
Noise Figure ^(*)	NF	$f=1.575GHz$	-	0.94	-	dB	
Input Power IP3	I_{IP3}	$f=1.575GHz, 1.576GHz$	-	-14.5	-	dBm	
Input Power @ 1dB Gain Compression	P1dB	$f=1.575GHz$	-	-25.5	-	dBm	

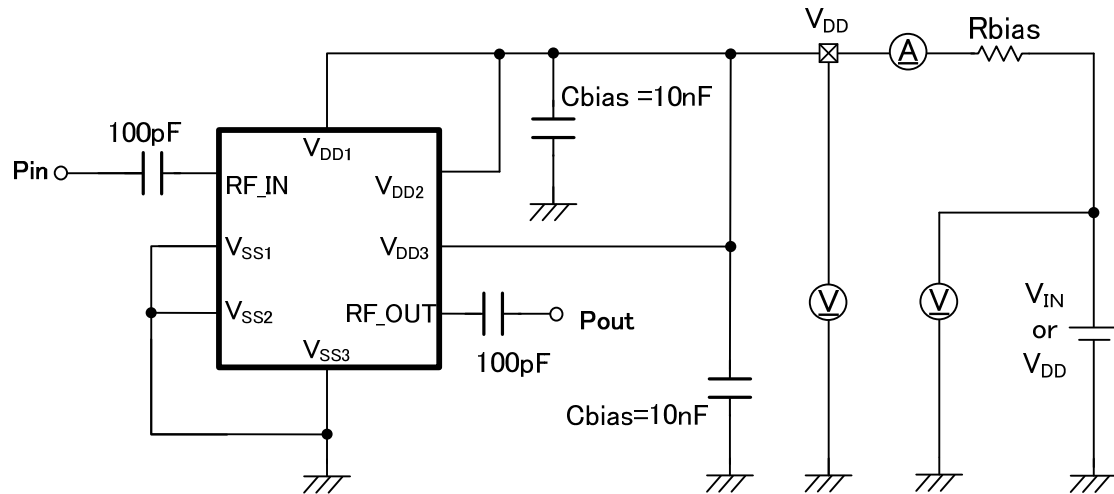
*1: NF is the value excluding the substrate loss.

Note

1. This series is structurally weak in electrostatic discharge.
Please eliminate static electricity from the operational table, people, and soldering iron.
2. Please use noiseless power supply for stable operation.
3. Please use $\pm 1\%$ Rbias with $\pm 200ppm/^{\circ}C$ temperature stability and 10nF Cbias.
4. Please connect Cbias to V_{DD} pin as close as possible.
5. Please refer to the pattern layout. V_{DD} pins (V_{DD1} , V_{DD2} , V_{DD3}) should be connected each other outside. Also, V_{SS} pins (V_{SS1} , V_{SS2} , V_{SS3}) should be connected as well.
6. Please ensure to use an external component which does not depend on bias or temperature too much.

■ TEST CIRCUITS

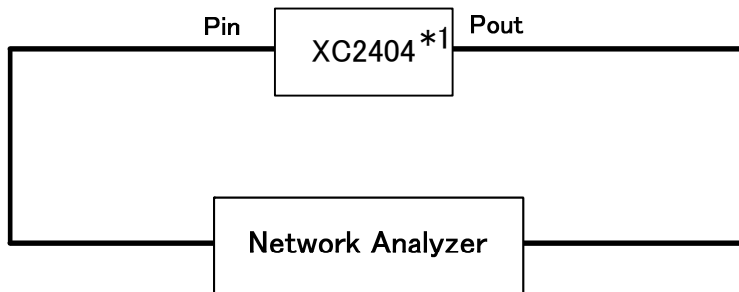
- Circuit (DC Characteristics: Power Supply Pin Voltage, Circuit Current, Input Power Supply Voltage)



* Fixed Bias: $R_{bias}=0\Omega$, $V_{IN}=V_{DD}$

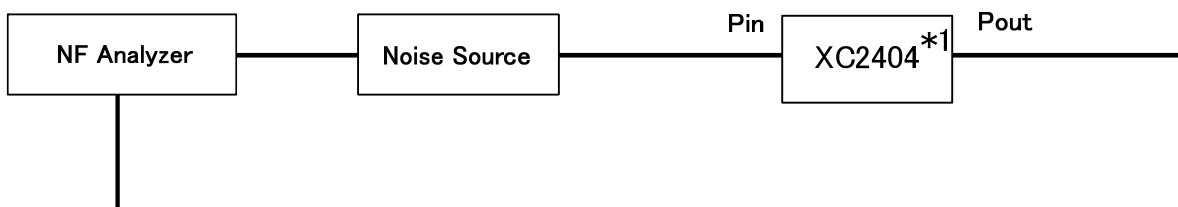
** Pin / Pout is 50Ω

- Circuit (Power Gain, Input Return Loss, Output Return Loss, Isolation, Input Power @ 1dB Gain Compression)



*1: Refer to the circuit for the block detail.

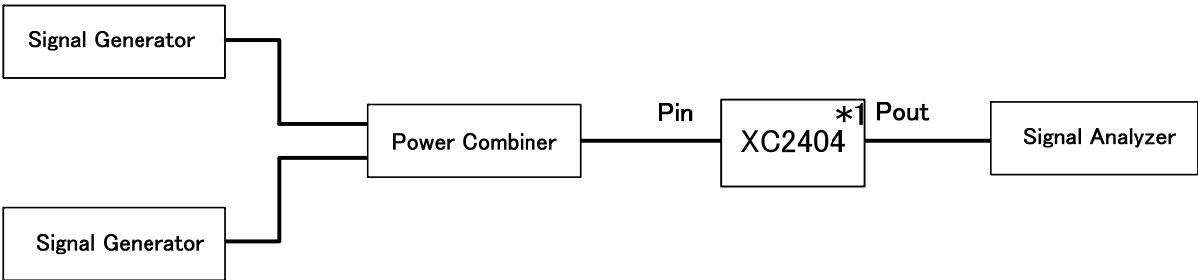
- Circuit (Noise Figure)



*1: Refer to the circuit for the block detail.

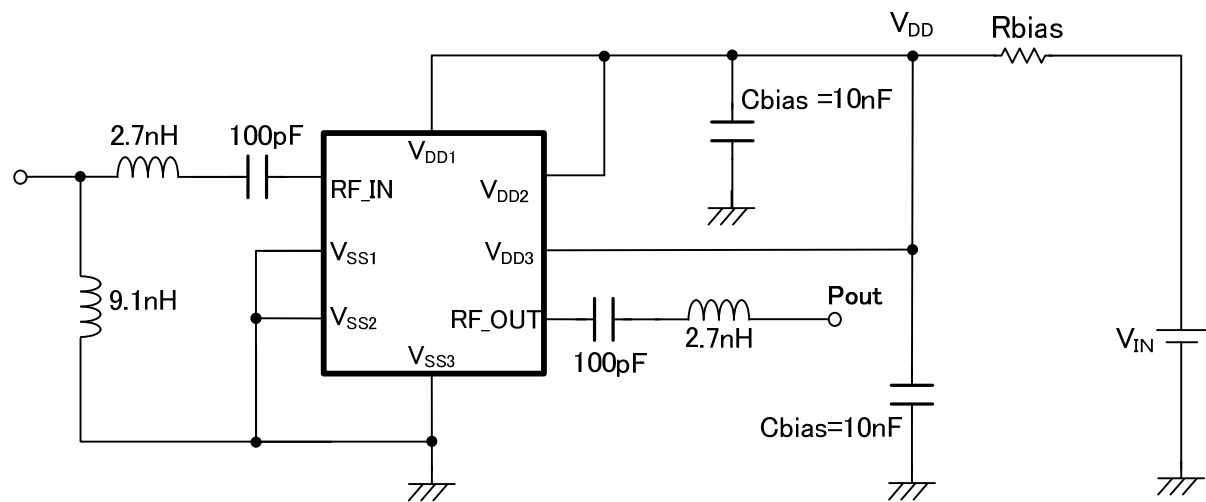
■ TEST CIRCUITS (Continued)

● Circuit (Input Power IIP3)



*1: Refer to the circuit for the block detail.

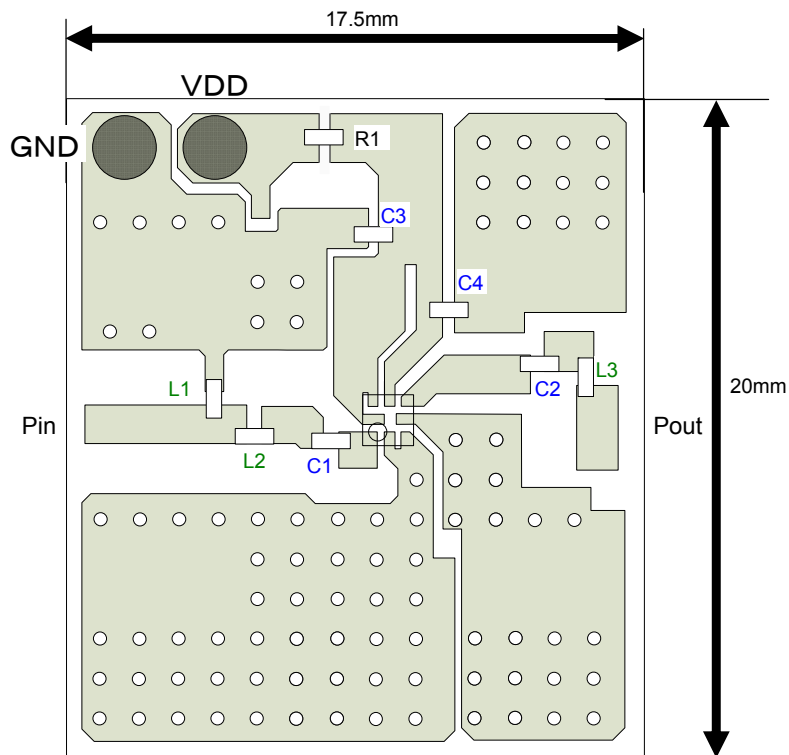
● Circuit (XC2404 series, the circuit of the block)



$V_{IN}(V)$	$R_{bias}(\Omega)$
3.00	270
2.85	240
1.80	82

- * Fixed Bias: $R_{bias}=0\Omega$, $V_{IN}=V_{DD}$
- ** R_{bias} : Should be in $\pm 1\%$ tolerance and $\pm 200\text{ppm/}$ temperature stability. C_{bias} is 10nF.
- *** Please refer to the EVALUATION BOARD layout. V_{DD} pin (V_{DD1} , V_{DD2} , V_{DD3}) and V_{SS} pin (V_{SS1} , V_{SS2} , V_{SS3}) are connected respectively on the board.

■ EVALUATION BOARD



SYMBOL	SPEC	COMMENT
C1, C2	100pF	MURATA (GRM1552C1H)
C3,C4	10nF	-
L1	9.1nH	TAIYO YUDEN AQseries (AQ1059N1J)
L2,L3	2.7nH	TAIYO YUDEN AQseries (AQ1052N7J)
R1 (Rbias) ^(*)	- ^(*)	Less than $\pm 1\%$ tolerance, Less than $\pm 200\text{ppm/}$ temperature stability

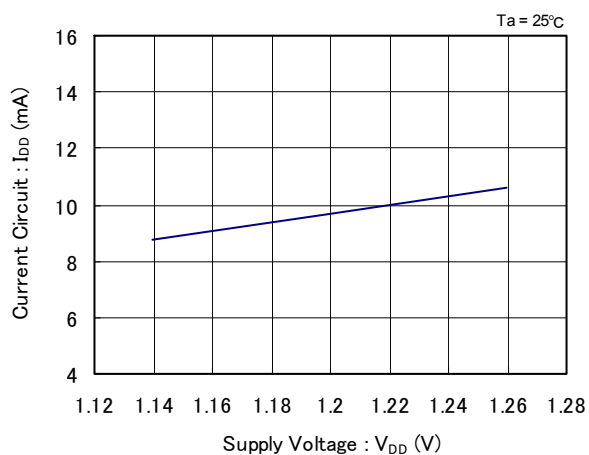
PCB (FR-4)
 MICROSTRIPLINE WIDTH =1.25mm
 t=0.8mm
 PCB size = 20mm × 17.5mm

*1: Fixed Bias: Rbias=0Ω

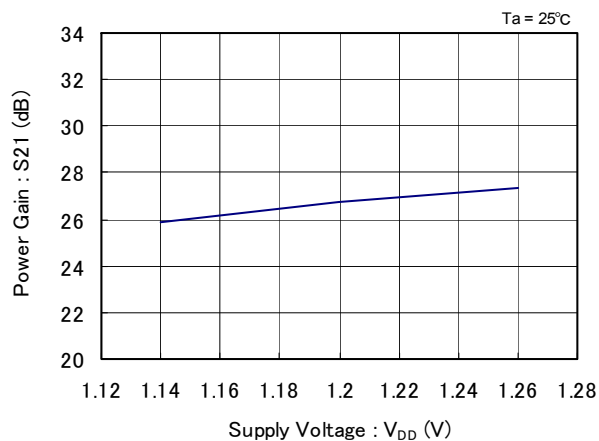
* Please use an external component which does not depend on bias or temperature too much.

■ TYPICAL PERFORMANCE CHARACTERISTICS

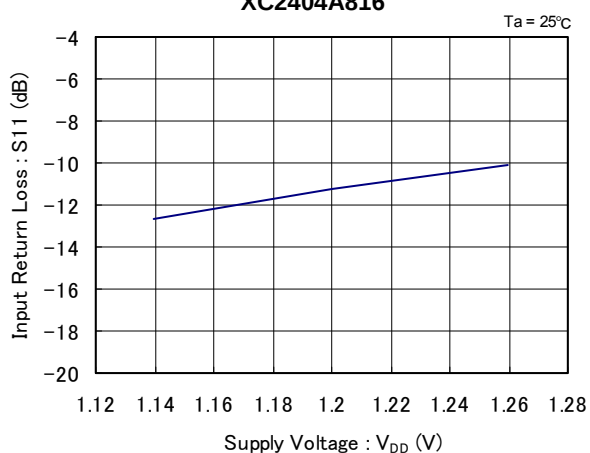
(1) Current Circuit vs. Supply Voltage

XC2404A816

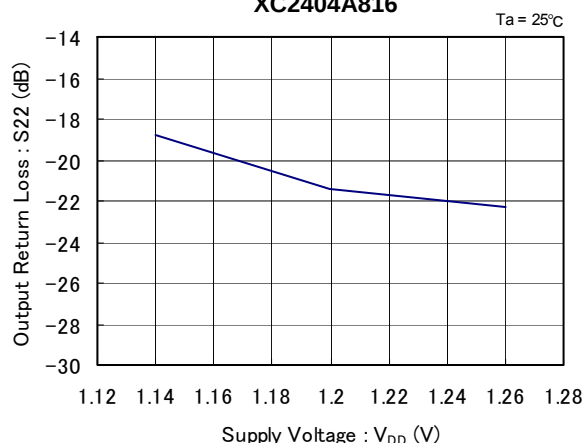
(2) Power Gain vs. Supply Voltage

XC2404A816

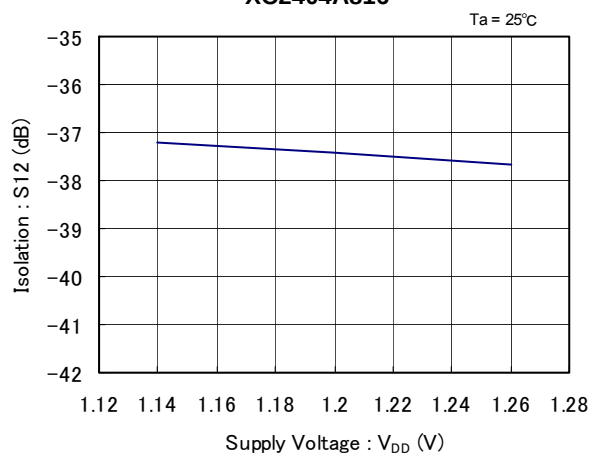
(3) Input Return Loss vs. Supply Voltage

XC2404A816

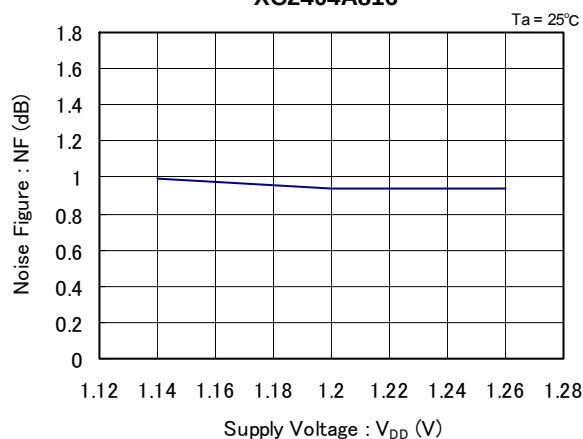
(4) Output Return Loss vs. Supply Voltage

XC2404A816

(5) Isolation vs. Supply Voltage

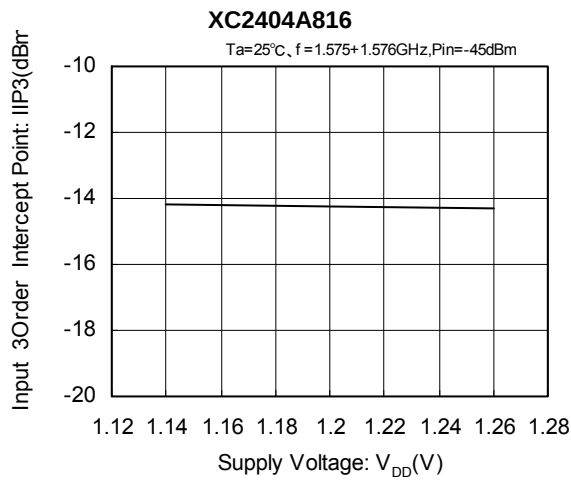
XC2404A816

(6) Noise Figure vs. Supply Voltage

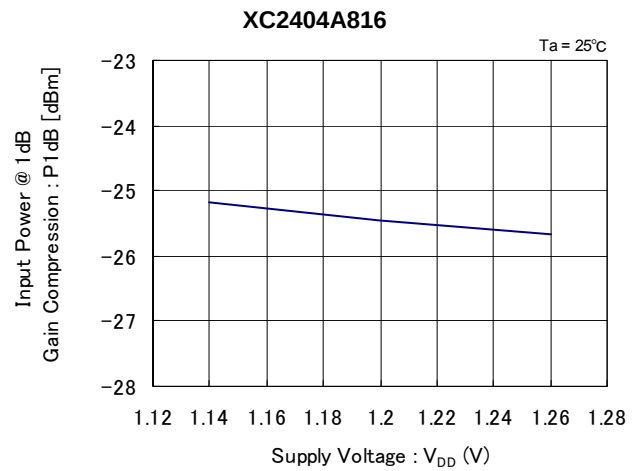
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■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

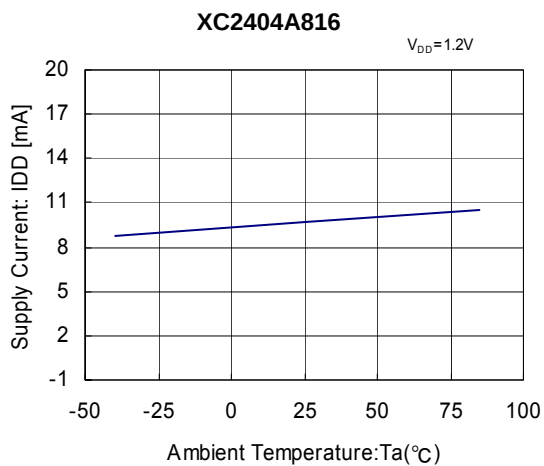
(7) Input 3 Order Intercept Point vs. Supply Voltage



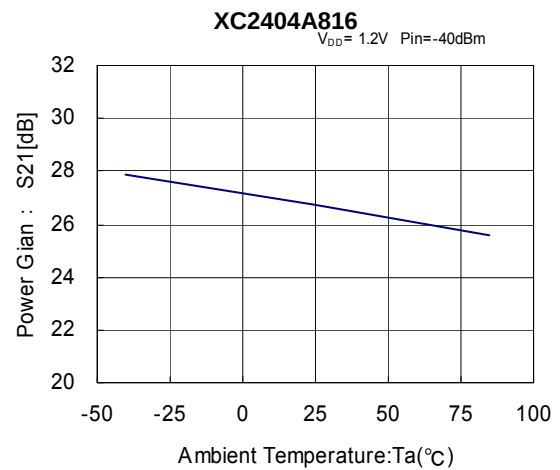
(8) Input Power @ 1dB Gain compression vs. Power Supply Voltage



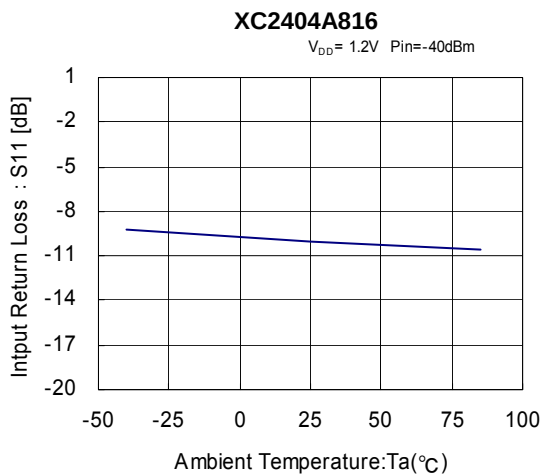
(9) Supply Current vs. Ambient Temperature



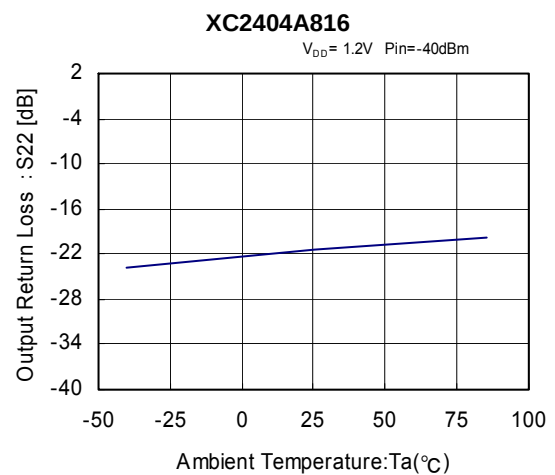
(10) Power Gain vs. Ambient Temperature



(11) Input Return Loss vs. Ambient Temperature

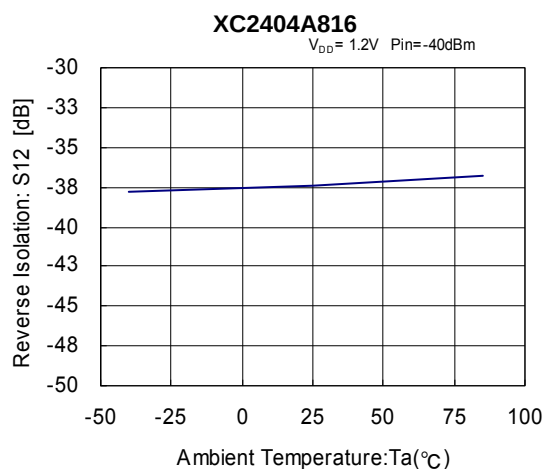


(12) Output Return Loss vs. Ambient Temperature

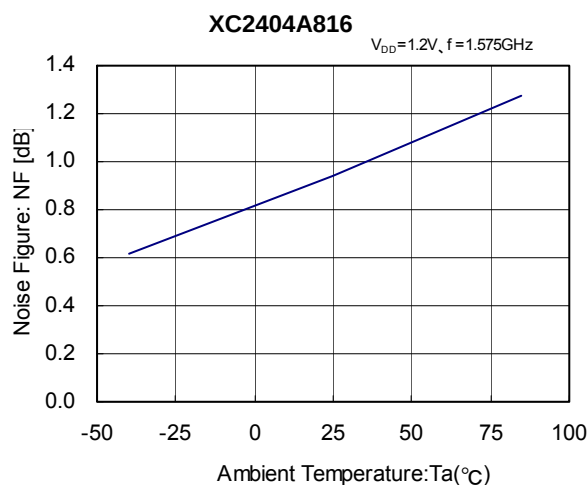


■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

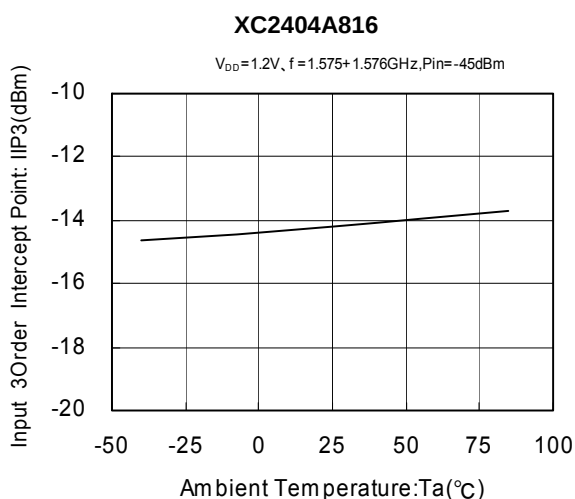
(13) Reverse Isolation vs. Ambient Temperature



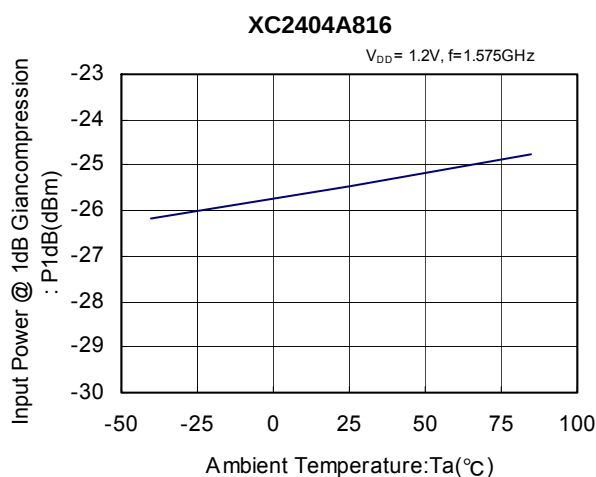
(14) Noise Figure vs. Ambient Temperature



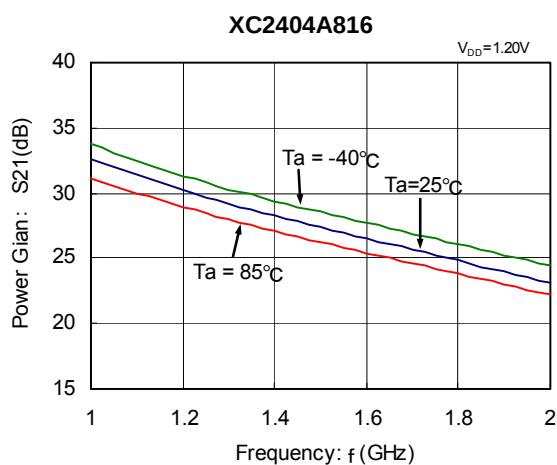
(15) Input 3 Order intercept point vs. Ambient Temperature



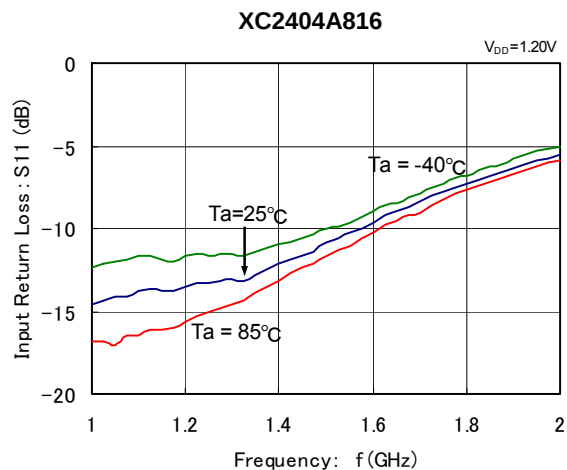
(16) Input Power @ 1dB Gain Compression vs. Ambient Temperature



(17) Power Gain vs. Frequency

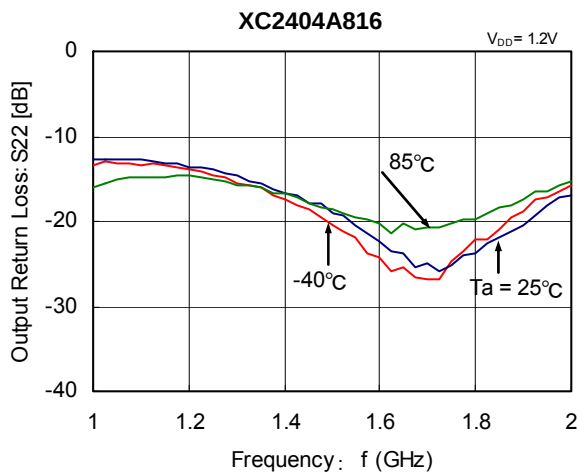


(18) Input Return Loss vs. Frequency

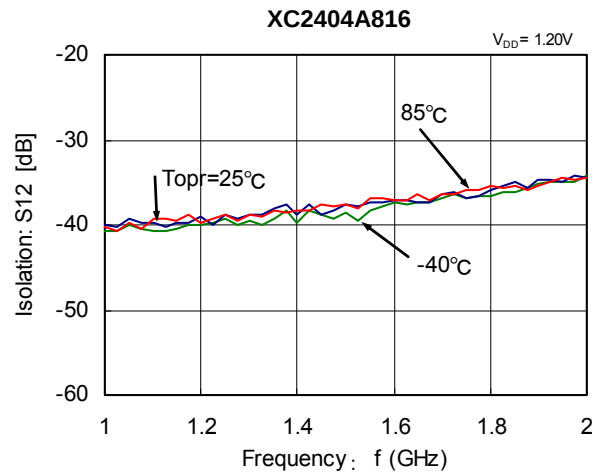


■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

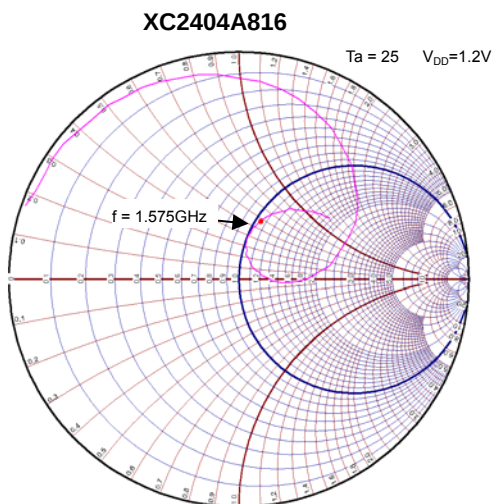
(19) Output Return Loss vs. Frequency



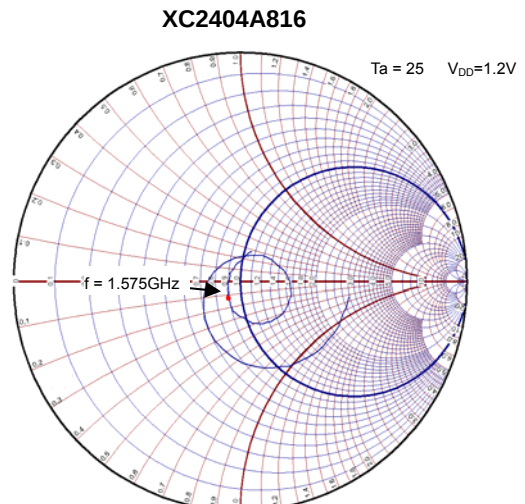
(20) Isolation vs. Frequency



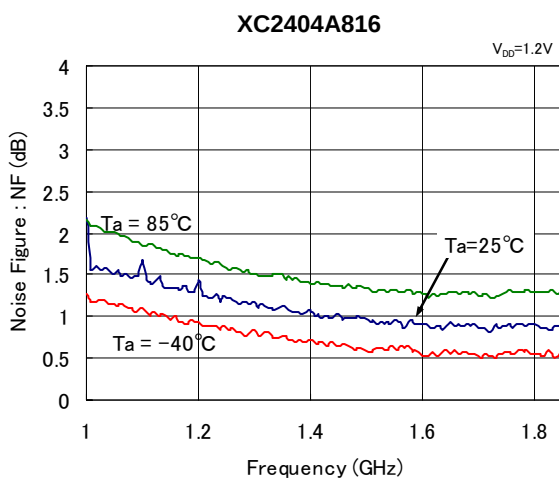
(21) Input Return Loss vs. Frequency (Smith Chart)



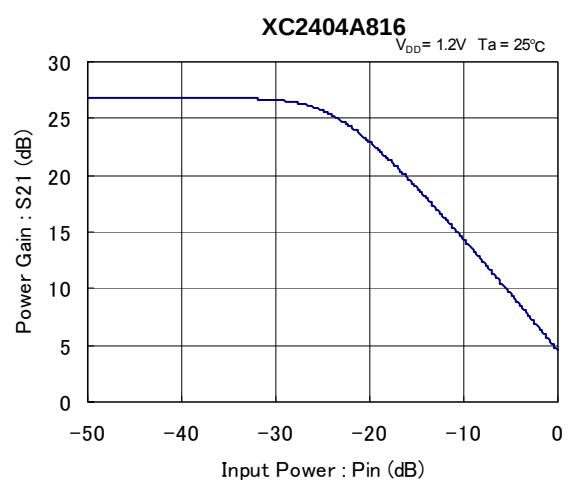
(22) Output Return Loss vs. Frequency (Smith Chart)



(23) Noise Figure vs. Frequency

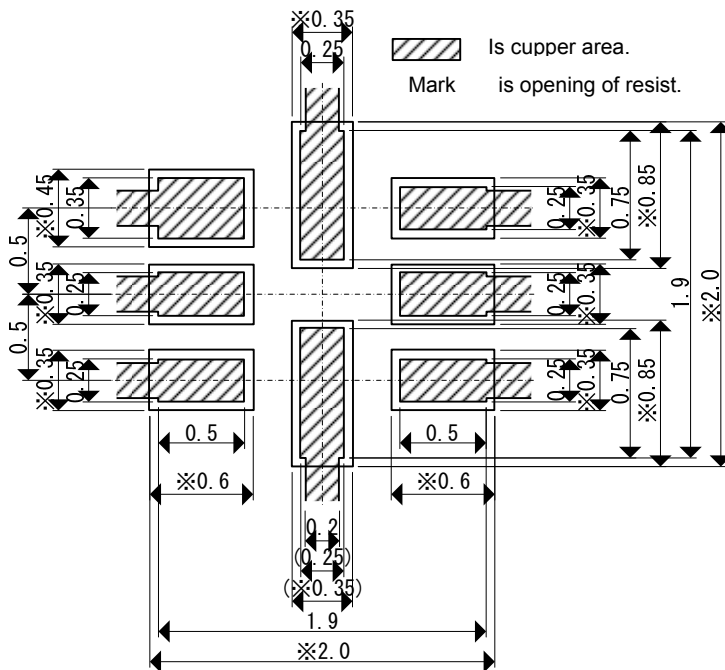


(24) Power Gain vs. Input Power

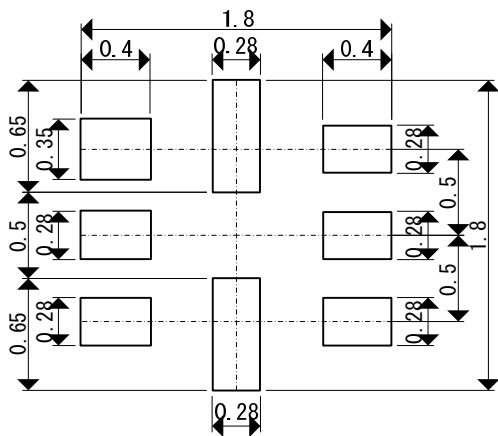


■ PACKAGING INFORMATION (Continued)

- USP-8A01 Reference Pattern Layout



- USP-8A01 Reference Metal Mask Design



■ MARKING RULE

represents product series.

MARK	PRODUCT SERIES
4	XC2404*****-G

represents product.

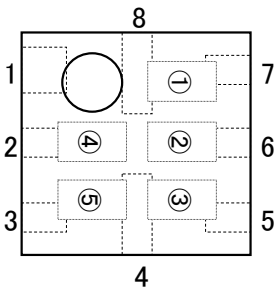
MARK		PRODUCT SERIES
A	0	XC2404A816**-G

represents production lot number.

01 to 09, 0A to 0Z, 11 to 9Z, A1 to A9, AA to Z9, ZA to ZZ in order.
(G, I, J, O, Q, W excepted)

*No character inversion used.

USP-8A01



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