

OVERVIEW

The 5075 series are miniature VCXO ICs that provide a wide frequency pulling range, even when using miniature crystal units for which a wide pulling range is difficult to provide. They employ a recently developed varicap diode fabrication process that provides a wide frequency pulling range and good linearity without any external components. Also, they employ a regulated voltage drive oscillator circuit that significantly reduces current consumption, crystal current, and oscillation characteristics supply voltage dependency. The 5075 series are ideal for miniature, wide pulling range, low power consumption, VCXO modules.

FEATURES

- VCXO with recently developed varicap diode built-in
- New fabrication process that significantly reduces parasitic capacitance and provides wide pulling range even when using miniature crystal units
- Regulated voltage drive oscillator circuit for reduced power consumption, crystal drive current, and oscillation characteristics voltage dependency
- Wide frequency pulling range
 - $\pm 190\text{ppm}$ (B1 version, $f = 27\text{MHz}$)
(Crystal: $\gamma = 300$, $C_0 = 1.5\text{pF}$)
- Operating supply voltage range: 2.25V to 3.63V
- Oscillation frequency range (for fundamental oscillation): 20MHz to 55MHz (varies with version)
- Low current consumption: 1.0mA
(B1 version, $f = 27\text{MHz}$, no load, $V_{DD} = 3.3\text{V}$)
- Frequency divider built-in
 - Selectable by version: f_O , $f_O/2$, $f_O/4$, $f_O/8$, $f_O/16$
 - Frequency divider output for 1.3MHz (min) low frequency output
- VC pin input resistance: $10\text{M}\Omega$ (min)
- CMOS output
- Two types of pad layout selectable by mounting method
 - A× version: for Flip Chip Bonding
 - B× version: for Wire Bonding
- Package: Wafer form (WF5075××)
Chip form (CF5075××)

APPLICATIONS

- $2.5 \times 2.0\text{mm}$, $3.2 \times 2.5\text{mm}$ size miniature VCXO modules for digital mobile TV tuner, digital TV (PDP, LCD), PND (Personal Navigation Device), etc.

ORDERING INFORMATION

Device	Package
WF5075××-4	Wafer form
CF5075××-4	Chip form

SERIES CONFIGURATION

Operating supply voltage range [V]	PAD layout	Recommended operating frequency range ^{*1} [MHz]	Output frequency and version name ^{*2}				
			f _O output	f _O /2 output	f _O /4 output	f _O /8 output	f _O /16 output
2.25 to 3.63	Flip Chip Bonding	20 to 40	(5075A1)	(5075A2)	(5075A3)	(5075A4)	(5075A5)
		40 to 55	(5075AJ)	(5075AK)	(5075AL)	(5075AM)	(5075AN)
	Wire Bonding	20 to 40	5075B1	(5075B2)	(5075B3)	(5075B4)	(5075B5)
		40 to 55	5075BJ	(5075BK)	(5075BL)	(5075BM)	(5075BN)

*1. The recommended operating frequency is a yardstick value derived from the crystal used for NPC characteristics authentication. However, the oscillation frequency range is not guaranteed. Specifically, the characteristics can vary greatly due to crystal characteristics and mounting conditions, so the oscillation characteristics of components must be carefully evaluated.

*2. Versions in parentheses () are under development.

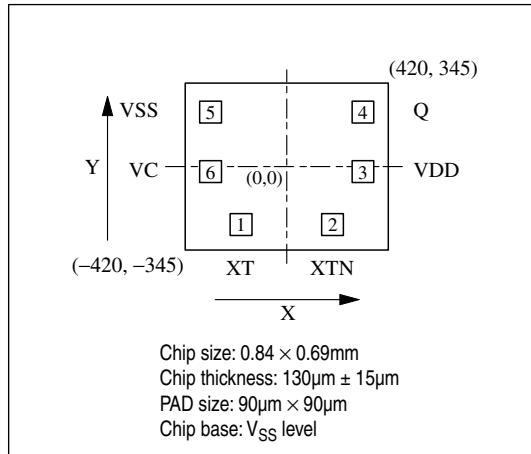
VERSION NAME

Device	Package	Version name
WF5075xx-4	Wafer form	WF5075□□-4 Form WF: Wafer form CF: Chip (Die) form Oscillation frequency range, frequency divider function Pad layout type A: for Flip Chip Bonding B: for Wire Bonding
CF5075xx-4	Chip form	

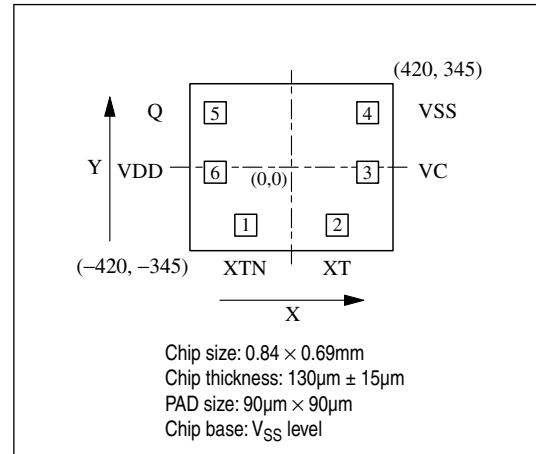
PAD LAYOUT

(Unit: μm)

■ 5075A× (for Flip Chip Bonding)



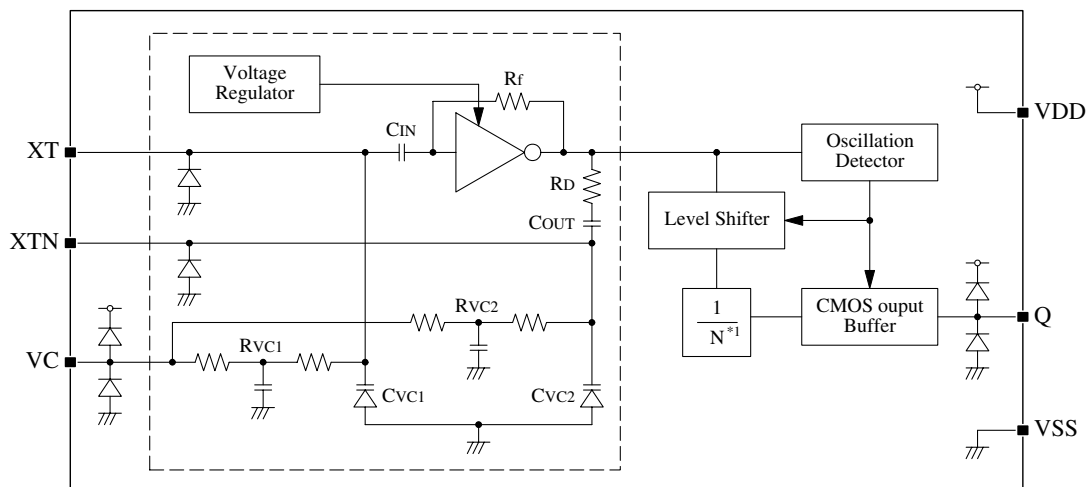
■ 5075B× (for Wire Bonding)



PAD DIMENSIONS PIN DESCRIPTION

Pad No.	Pad dimensions [μm]		Pad No.		Pin	I/O	Description
	X	Y	5075A×	5075B×			
1	-189	-240	1	2	XT	I	Crystal connection pin (amplifier input)
2	189	-240	2	1	XTN	O	Crystal connection pin (amplifier output)
3	315	-21	3	6	VDD	–	(+) supply pin
4	315	225	4	5	Q	O	Clock output pin
5	-315	225	5	4	VSS	–	(–) supply pin
6	-315	-21	6	3	VC	I	Oscillation frequency control voltage input pin (positive polarity) (frequency increases with increasing voltage)

BLOCK DIAGRAM

*1. $N = 1, 2, 4, 8, 16$

ABSOLUTE MAXIMUM RATINGS

$V_{SS} = 0V$

Parameter	Symbol	Conditions	Rating	Unit
Supply voltage range	V_{DD}	Between VDD and VSS	-0.5 to 7.0	V
Input voltage range	V_{IN}	Input pins	-0.5 to $V_{DD} + 0.5$	V
Output voltage range	V_{OUT}	Output pins	-0.5 to $V_{DD} + 0.5$	V
Storage temperature range	T_{STG}	Wafer form, chip form	-65 to +150	°C
Output current	I_{OUT}	Q pin	20	mA

RECOMMENDED OPERATING CONDITIONS

$V_{SS} = 0V$

Parameter	Symbol	Conditions		Rating			Unit
				Min	Typ	Max	
Operating supply voltage	V _{DD}	C _{LOUT} ≤ 15pF		2.25	–	3.63	V
Input voltage	V _{IN}	Input pins		V _{SS}	–	V _{DD}	V
Operating temperature	T _{OPR}			–40	–	+85	°C
Oscillation frequency* ¹	f _O	5075×1 to 5075×5		20	–	40	MHz
		5075×J to 5075×N		40	–	55	MHz
Output frequency	f _{OUT}	C _{LOUT} ≤ 15pF	5075×1 to 5075×5	1.25	–	40	MHz
			5075×J to 5075×N	2.5	–	55	MHz

*1. The oscillation frequency is a yardstick value derived from the crystal used for NPC characteristics authentication. However, the oscillation frequency range is not guaranteed. Specifically, the characteristics can vary greatly due to crystal characteristics and mounting conditions, so the oscillation characteristics of components must be carefully evaluated.

ELECTRICAL CHARACTERISTICS

5075×1 to 5075×5

$V_{DD} = 2.25$ to 3.63 V, $V_C = 0.5V_{DD}$, $V_{SS} = 0$ V, $T_a = -40$ to $+85^\circ\text{C}$ unless otherwise noted.

Parameter	Symbol	Conditions		Rating			Unit
				Min	Typ	Max	
Current consumption	I _{DD}	5075×1 (f _O), Measurement circuit 1, no load, f _O = 27MHz, f _{OUT} = 27MHz	V _{DD} = 2.5V	–	0.7	1.4	mA
			V _{DD} = 3.3V	–	1.0	2.0	mA
		5075×2 (f _O /2), Measurement circuit 1, no load, f _O = 27MHz, f _{OUT} = 13.5MHz	V _{DD} = 2.5V	–	0.6	1.2	mA
			V _{DD} = 3.3V	–	0.8	1.6	mA
		5075×3 (f _O /4), Measurement circuit 1, no load, f _O = 27MHz, f _{OUT} = 6.75MHz	V _{DD} = 2.5V	–	0.5	1.0	mA
			V _{DD} = 3.3V	–	0.7	1.4	mA
		5075×4 (f _O /8), Measurement circuit 1, no load, f _O = 27MHz, f _{OUT} = 3.38MHz	V _{DD} = 2.5V	–	0.5	1.0	mA
			V _{DD} = 3.3V	–	0.6	1.2	mA
		5075×5 (f _O /16), Measurement circuit 1, no load, f _O = 27MHz, f _{OUT} = 1.69MHz	V _{DD} = 2.5V	–	0.4	0.8	mA
			V _{DD} = 3.3V	–	0.6	1.2	mA
HIGH-level output voltage	V _{OH}	Q pin, Measurement circuit 2, I _{OH} = –2.8mA		V _{DD} – 0.4	–	–	V
LOW-level output voltage	V _{OL}	Q pin, Measurement circuit 2, I _{OL} = 2.8mA		–	–	0.4	V
Oscillator block built-in resistance	R _{VC1}	Measurement circuit 3		210	420	840	kΩ
	R _{VC2}			210	420	840	kΩ
Oscillator block built-in capacitance	C _{VC1}	Design value (a monitor pattern on a wafer is tested), Excluding parasitic capacitance.	V _C = 0.3V	–	5.6	–	pF
			V _C = 1.65V	–	3.1	–	pF
			V _C = 3.0V	–	1.5	–	pF
	C _{VC2}		V _C = 0.3V	–	8.4	–	pF
			V _C = 1.65V	–	4.7	–	pF
			V _C = 3.0V	–	2.3	–	pF
VC input resistance	R _{VIN}	Measurement circuit 4, Ta = 25°C		10	–	–	MΩ
VC input impedance	Z _{VIN}	Measurement circuit 5, V _C = 0V, f = 10kHz, Ta = 25°C (a monitor pattern on a wafer is tested)		–	450	–	kΩ
VC input capacitance	C _{VIN}	Measurement circuit 5, V _C = 0V, f = 10kHz, Ta = 25°C (a monitor pattern on a wafer is tested)		–	37	–	pF
Modulation characteristics*1	fm	Measurement circuit 6, –3dB frequency, V _{DD} = 3.3V, V _C = 3.3Vp-p, Ta = 25°C, f _O = 27MHz		–	25	–	kHz

*1. The modulation characteristics may vary with the crystal used.

5075 series

5075×J to 5075×N

$V_{DD} = 2.25$ to $3.63V$, $V_C = 0.5V_{DD}$, $V_{SS} = 0V$, $T_a = -40$ to $+85^\circ C$ unless otherwise noted.

Parameter	Symbol	Conditions		Rating			Unit
				Min	Typ	Max	
Current consumption	I _{DD}	5075×J (f _O), Measurement circuit 1, no load, f _O = 48MHz, f _{OUT} = 48MHz	V _{DD} = 2.5V	–	1.2	2.4	mA
			V _{DD} = 3.3V	–	1.6	3.2	mA
		5075×K (f _O /2), Measurement circuit 1, no load, f _O = 48MHz, f _{OUT} = 24MHz	V _{DD} = 2.5V	–	0.9	1.8	mA
			V _{DD} = 3.3V	–	1.3	2.6	mA
		5075×L (f _O /4), Measurement circuit 1, no load, f _O = 48MHz, f _{OUT} = 12MHz	V _{DD} = 2.5V	–	0.8	1.6	mA
			V _{DD} = 3.3V	–	1.0	2.0	mA
		5075×M (f _O /8), Measurement circuit 1, no load, f _O = 48MHz, f _{OUT} = 6MHz	V _{DD} = 2.5V	–	0.7	1.4	mA
			V _{DD} = 3.3V	–	0.9	1.8	mA
		5075×N (f _O /16), Measurement circuit 1, no load, f _O = 48MHz, f _{OUT} = 3MHz	V _{DD} = 2.5V	–	0.7	1.4	mA
			V _{DD} = 3.3V	–	0.9	1.8	mA
HIGH-level output voltage	V _{OH}	Q pin, Measurement circuit 2, I _{OH} = –2.8mA		V _{DD} – 0.4	–	–	V
LOW-level output voltage	V _{OL}	Q pin, Measurement circuit 2, I _{OL} = 2.8mA		–	–	0.4	V
Oscillator block built-in resistance	R _{VC1}	Measurement circuit 3		210	420	840	kΩ
	R _{VC2}			210	420	840	kΩ
Oscillator block built-in capacitance	C _{VC1}	Design value (a monitor pattern on a wafer is tested), Excluding parasitic capacitance.	V _C = 0.3V	–	5.6	–	pF
			V _C = 1.65V	–	3.1	–	pF
			V _C = 3.0V	–	1.5	–	pF
	C _{VC2}		V _C = 0.3V	–	8.4	–	pF
			V _C = 1.65V	–	4.7	–	pF
			V _C = 3.0V	–	2.3	–	pF
VC input resistance	R _{VIN}	Measurement circuit 4, Ta = 25°C		10	–	–	MΩ
VC input impedance	Z _{VIN}	Measurement circuit 5, V _C = 0V, f = 10kHz, Ta = 25°C (a monitor pattern on a wafer is tested)		–	450	–	kΩ
VC input capacitance	C _{VIN}	Measurement circuit 5, V _C = 0V, f = 10kHz, Ta = 25°C (a monitor pattern on a wafer is tested)		–	37	–	pF
Modulation characteristics*1	fm	Measurement circuit 6, –3dB frequency, V _{DD} = 3.3V, V _C = 3.3Vp-p, Ta = 25°C, f _O = 48MHz		–	23	–	kHz

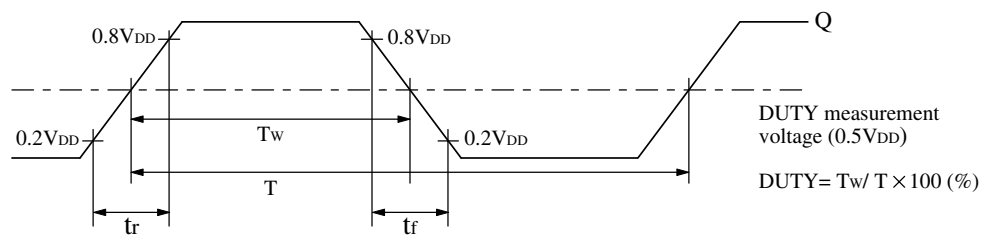
*1. The modulation characteristics may vary with the crystal used.

SWITCHING CHARACTERISTICS

$V_{DD} = 2.25$ to 3.63V , $V_C = 0.5V_{DD}$, $V_{SS} = 0\text{V}$, $T_a = -40$ to $+85^\circ\text{C}$ unless otherwise noted.

Parameter	Symbol	Conditions	Rating			Unit
			Min	Typ	Max	
Output rise time	t_r	Measurement circuit 7, $0.2V_{DD} \rightarrow 0.8V_{DD}$, $C_{LOUT} = 15\text{pF}$	–	2.1	4.0	ns
Output fall time	t_f	Measurement circuit 7, $0.8V_{DD} \rightarrow 0.2V_{DD}$, $C_{LOUT} = 15\text{pF}$	–	2.1	4.0	ns
Output duty cycle	Duty	Measurement circuit 7, $T_a = 25^\circ\text{C}$, $C_{LOUT} = 15\text{pF}$, $V_{DD} = 3.3\text{V}$	45	50	55	%

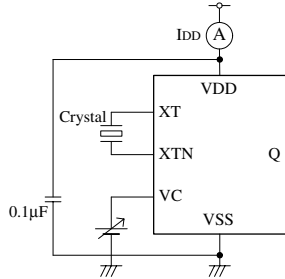
Switching Time Measurement Waveform



MEASUREMENT CIRCUITS

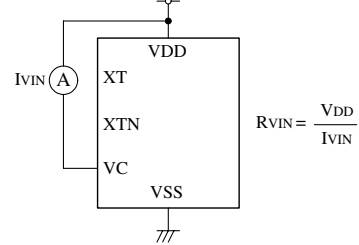
Measurement Circuit 1

Measurement parameter: I_{DD}



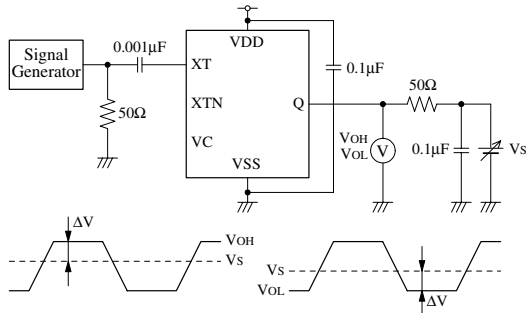
Measurement Circuit 4

Measurement parameter: R_{VIN}



Measurement Circuit 2

Measurement parameter: V_{OH} , V_{OL}



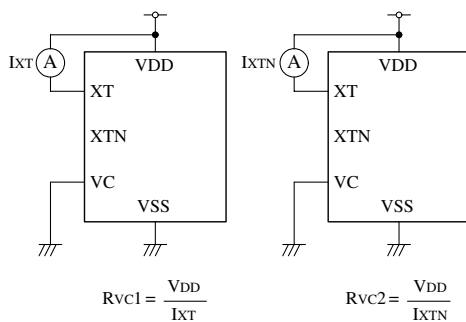
V_S adjusted such that $\Delta V = 50 \times I_{OH}$

V_S adjusted such that $\Delta V = 50 \times I_{OL}$

XT input signal: 1Vp-p, sine wave

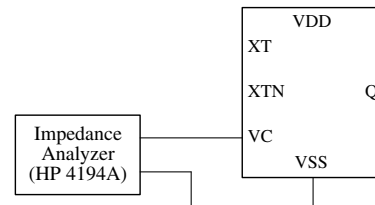
Measurement Circuit 3

Measurement parameter: R_{VC1} , R_{VC2}



Measurement Circuit 5

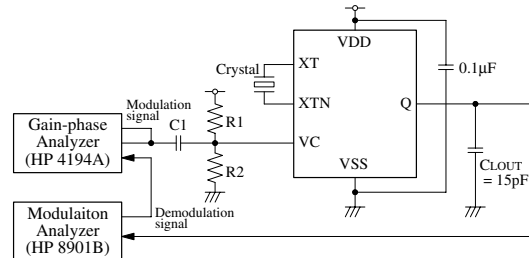
Measurement parameter: C_{VIN} , Z_{VIN}



VC input signal: 100Hz to 10kHz, 0.1Vp-p

Measurement Circuit 6

Measurement parameter: f_m

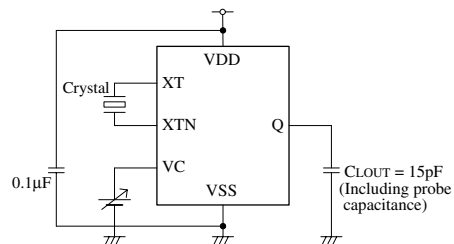


$C1 = 33\mu F$, $R1 = R2 = 1M\Omega$

VC modulation signal: 100Hz to 100kHz, 0 to V_{DDP-p}

Measurement Circuit 7

Measurement parameter: Duty, t_r , t_f



FUNCTIONAL DESCRIPTION

Oscillation Start-up Detector Function

The devices also feature an oscillation start-up detector circuit. This circuit functions to disable the outputs until the oscillation starts. This prevents unstable oscillator output at oscillator start-up when power is applied.

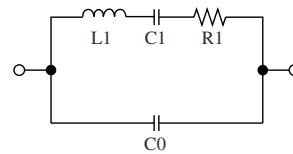
TYPICAL PERFORMANCE (5075B1)

The following characteristics measured using the crystal below. Note that the characteristics will vary with the crystal used.

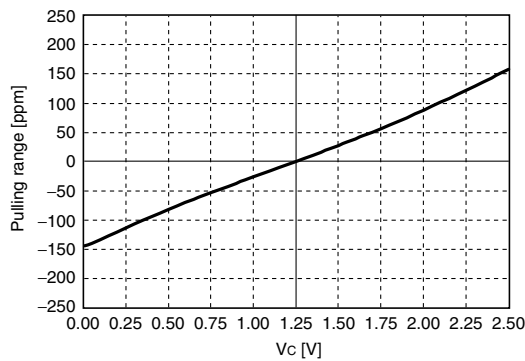
■ Crystal used for measurement

Parameter	$f_0 = 27\text{MHz}$
C_0 [pF]	1.5
$\gamma (= C_0/C_1)$	300

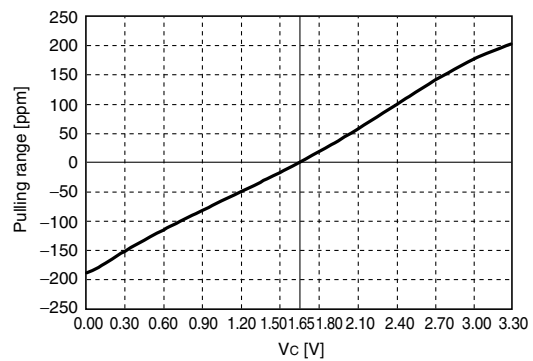
■ Crystal parameters



Frequency Pulling Range

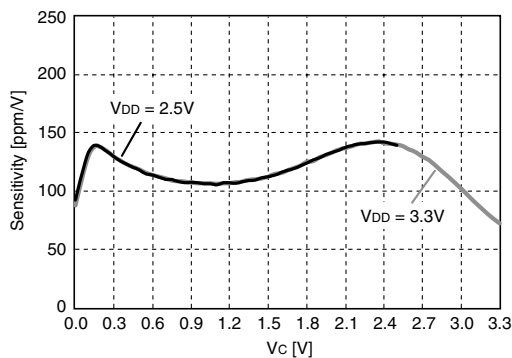


$V_{DD} = 2.5\text{V}$, $f_{OUT} = 27\text{MHz}$, $T_a = \text{R.T.}$



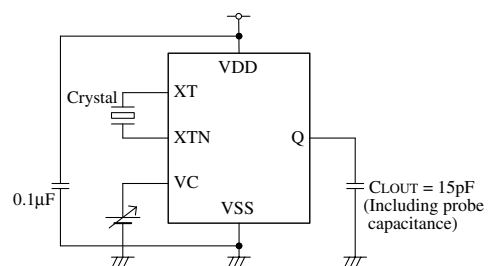
$V_{DD} = 3.3\text{V}$, $f_{OUT} = 27\text{MHz}$, $T_a = \text{R.T.}$

Pulling Sensitivity

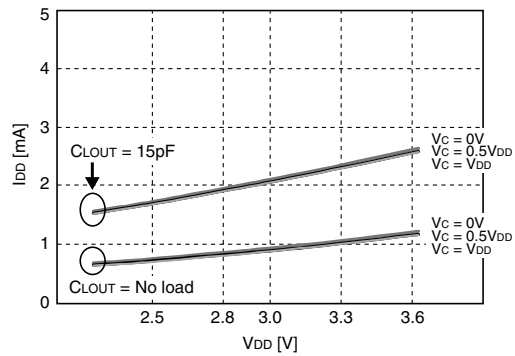


$V_{DD} = 2.5\text{V}, 3.3\text{V}$, $f_{OUT} = 27\text{MHz}$, $T_a = \text{R.T.}$

Measurement circuit

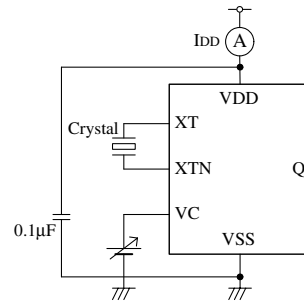


Current Consumption

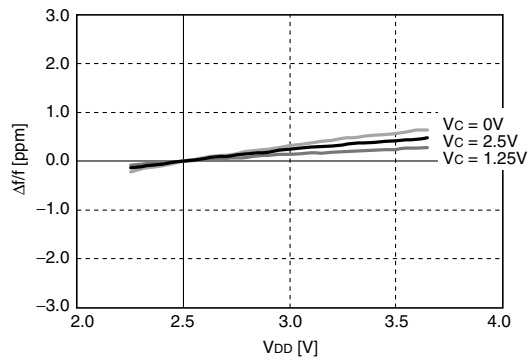


$f_{OUT} = 27\text{MHz}$, $T_a = \text{R.T.}$

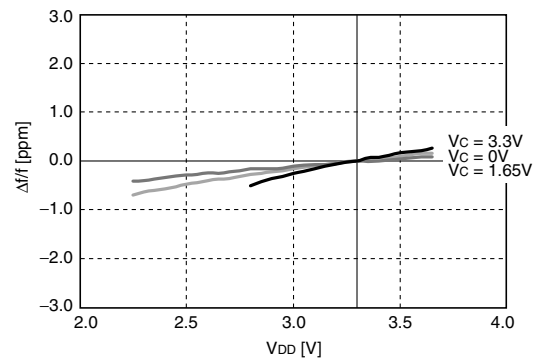
Measurement circuit



Frequency Stability by Supply Voltage Change

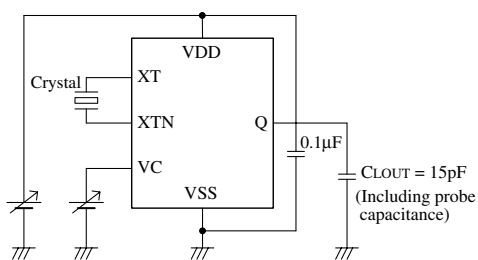


$f_{OUT} = 27\text{MHz}$, $\pm 0\text{ppm}$ at $V_{DD} = 2.5\text{V}$

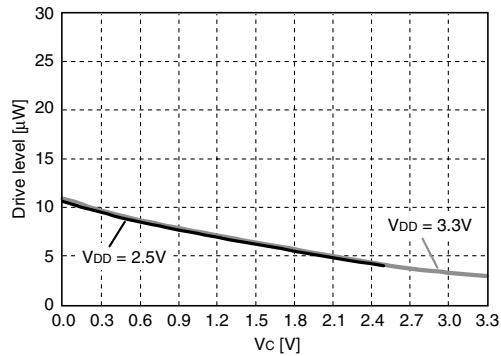


$f_{OUT} = 27\text{MHz}$, $\pm 0\text{ppm}$ at $V_{DD} = 3.3\text{V}$

Measurement circuit

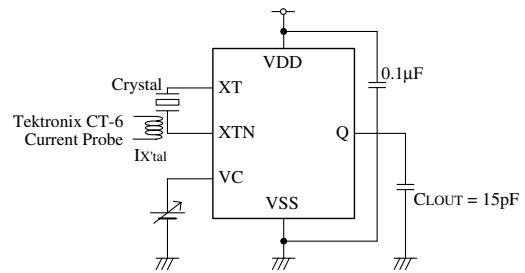


Drive Level



$V_{DD} = 2.5V, 3.3V, f_{OUT} = 27MHz, T_a = R.T.$

Measurement circuit



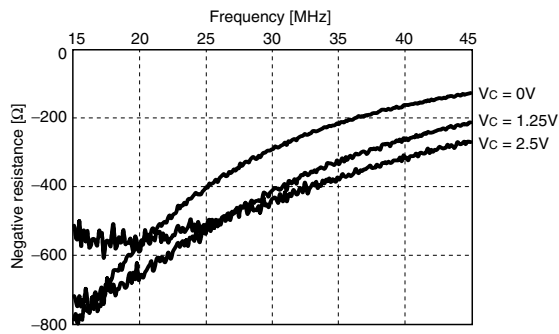
$$DL = (I_{X'tal})^2 \times Re$$

DL: drive level

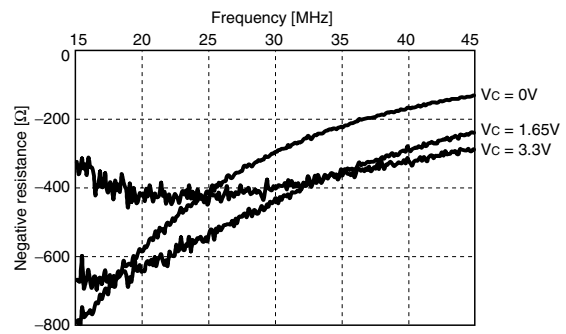
$I_{X'tal}$: current flowing to crystal (RMS value)

Re: crystal effective resistance

Negative Resistance

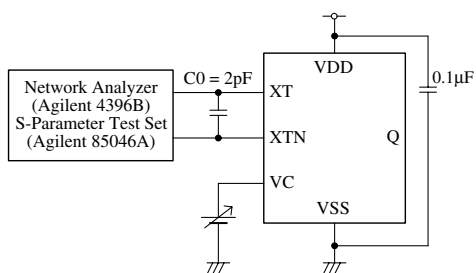


$V_{DD} = 2.5V, C_0 = 2pF, T_a = R.T.$



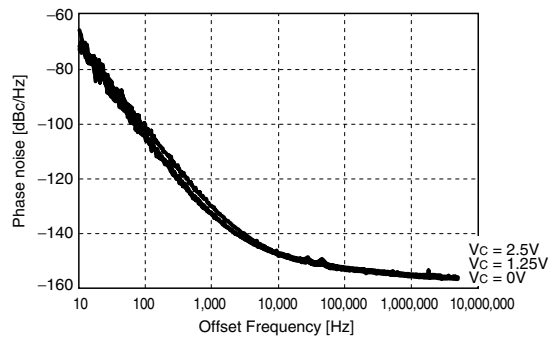
$V_{DD} = 3.3V, C_0 = 2pF, T_a = R.T.$

Measurement circuit

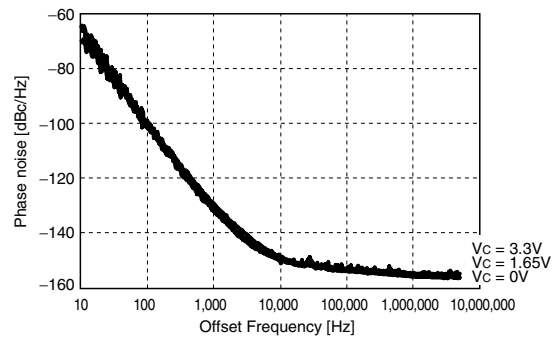


Note. "C0" value is set, concerning the actual crystal characteristics connected between XT and XTN. The data is measured with Agilent 4396B using NPC's original measurement jig. The values may vary with measurement jig and conditions.

Phase Noise

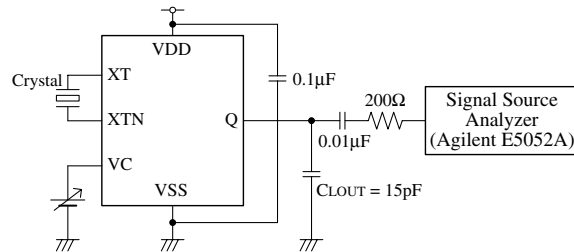


$V_{DD} = 2.5V$, $f_{OUT} = 27MHz$, $T_a = R.T.$

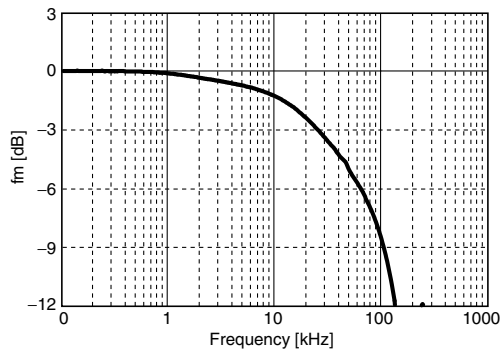


$V_{DD} = 3.3V$, $f_{OUT} = 27MHz$, $T_a = R.T.$

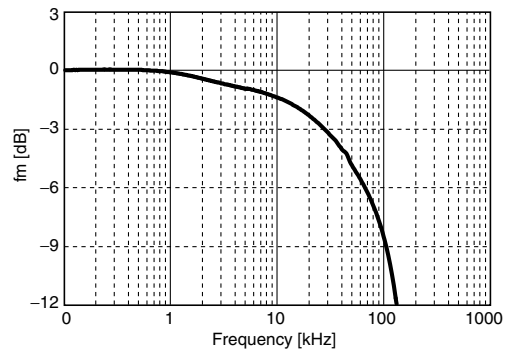
Measurement circuit



Modulation Characteristics

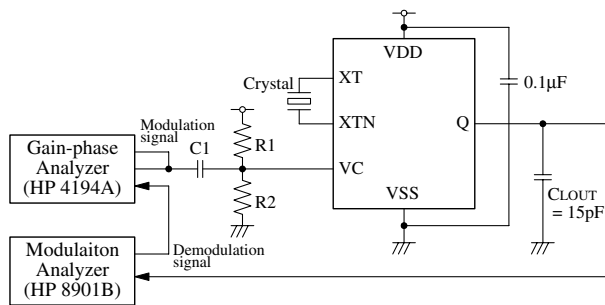


$V_{DD} = 2.5V$, $f_{OUT} = 27MHz$, $T_a = R.T.$



$V_{DD} = 3.3V$, $f_{OUT} = 27MHz$, $T_a = R.T.$

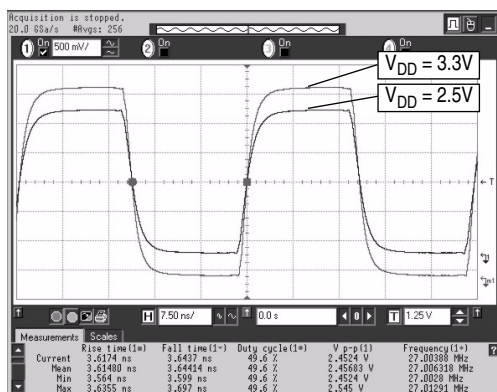
Measurement circuit



$C1 = 33\mu F$, $R1 = R2 = 1M\Omega$
 VC modulation signal: 100Hz to 100kHz, 0 to V_{DDp-p}

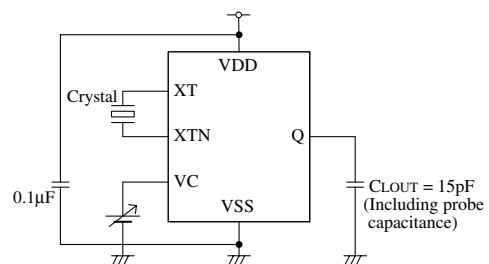
Output Waveform

Measurement equipment: Oscilloscope; DSO80604B (Agilent)



$V_{DD} = 2.5V, 3.3V$, $f_{OUT} = 27MHz$, $V_C = 0.5V_{DD}$,
 $C_{LOUT} = 15pF$, $T_a = R.T.$

Measurement circuit



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The logo for NPC (Nihombashi Kabutocho) is displayed in a large, bold, black, sans-serif font.

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