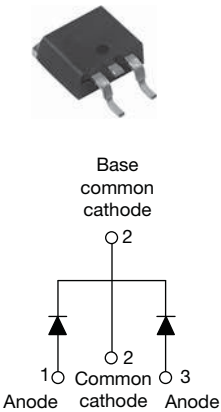
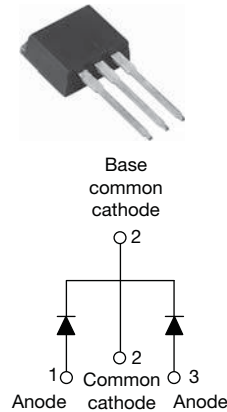


High Performance Schottky Rectifier, 2 x 8 A

VS-16CTQ...SPbF

D²PAK
VS-16CTQ...-1PbF

TO-262

PRODUCT SUMMARY

Package	TO-263AB (D ² PAK), TO-262AA
$I_{F(AV)}$	2 x 8 A
V_R	60 V to 100 V
V_F at I_F	0.58 V
I_{RM}	7 mA at 125 °C
T_J max.	175 °C
Diode variation	Common cathode
E_{AS}	7.5 mJ

FEATURES

- 175 °C T_J operation
- Center tap configuration
- Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	16	A
V_{RRM}		60 to 100	V
I_{FSM}	$t_p = 5 \mu s$ sine	850	A
V_F	8 Apk, $T_J = 125$ °C (per leg)	0.58	V
T_J	Range	-55 to 175	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	VS-16CTQ060SPbF VS-16CTQ060-1PbF	VS-16CTQ080SPbF VS-16CTQ080-1PbF	VS-16CTQ100SPbF VS-16CTQ100-1PbF	UNITS
Maximum DC reverse voltage	V_R	60	80	100	V
Maximum working peak reverse voltage	V_{RWM}				

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current per leg per device See fig. 5	$I_{F(AV)}$	50 % duty cycle at $T_C = 148$ °C, rectangular waveform	8 16	A
Maximum peak one cycle non-repetitive surge current per leg See fig. 7	I_{FSM}	5 μs sine or 3 μs rect. pulse 10 ms sine or 6 ms rect. pulse	850 275	A
Non-repetitive avalanche energy per leg	E_{AS}	$T_J = 25$ °C, $I_{AS} = 0.50$ A, $L = 60$ mH	7.50	mJ
Repetitive avalanche current per leg	I_{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical	0.50	A

**ELECTRICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	$V_{FM}^{(1)}$	8 A	0.72	V
		16 A	0.88	
		8 A	0.58	
		16 A	0.69	
Maximum reverse leakage current per leg See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	0.55	mA
		$T_J = 125\text{ }^{\circ}\text{C}$	7.0	
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$	0.415	V
Forward slope resistance	r_t		11.07	m Ω
Maximum junction capacitance per leg	C_T	$V_R = 5\text{ V}_{DC}$ (test signal range 100 kHz to 1 MHz), $25\text{ }^{\circ}\text{C}$	500	pF
Typical series inductance per leg	L_S	Measured lead to lead 5 mm from package body	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V_R	10 000	V/ μ s

Note

⁽¹⁾ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}		-55 to 175	$^{\circ}\text{C}$
Maximum thermal resistance, junction to case per leg	R_{thJC}	DC operation	3.25	$^{\circ}\text{C/W}$
Maximum thermal resistance, junction to case per package			1.63	
Typical thermal resistance, case to heatsink	R_{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight			2	g
			0.07	oz.
Mounting torque	minimum		6 (5)	kgf · cm
	maximum		12 (10)	(lbf · in)
Marking device		Case style D ² PAK	16CTQ...S	
		Case style TO-262	16CTQ...-1	

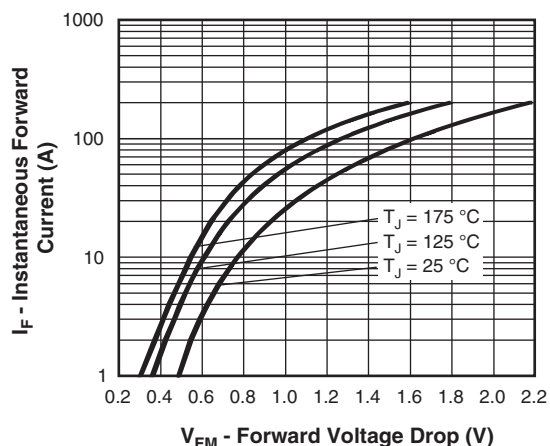


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

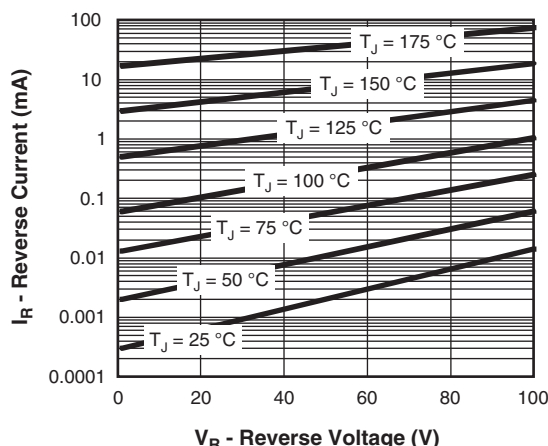


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

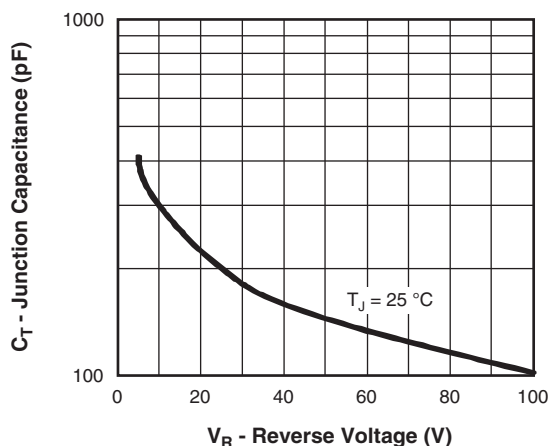


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

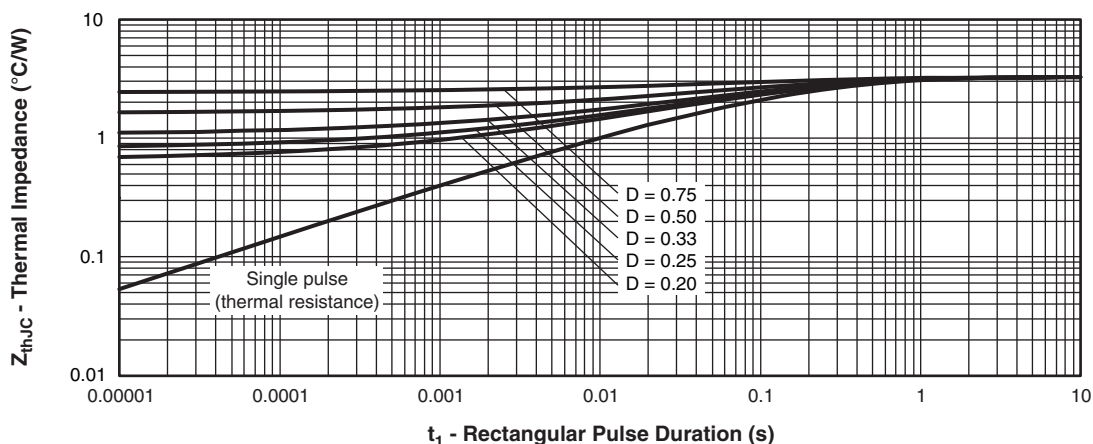


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

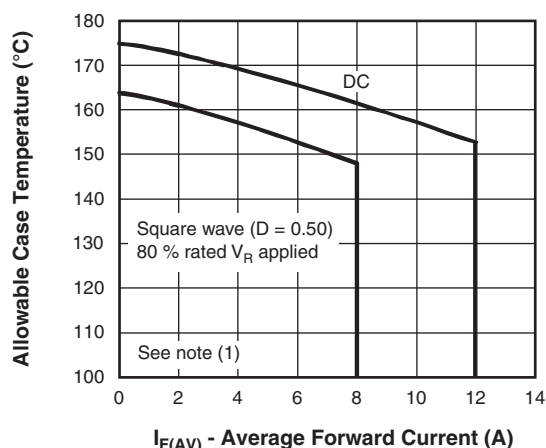


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

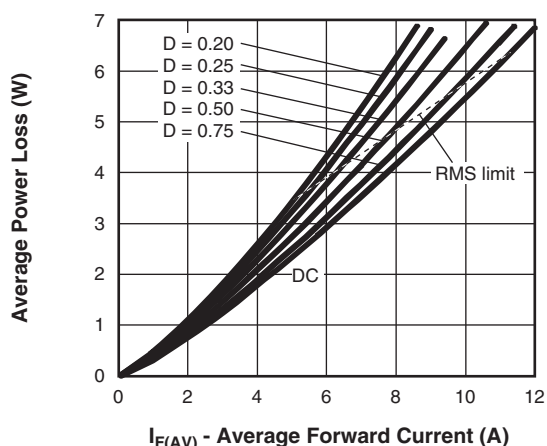


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

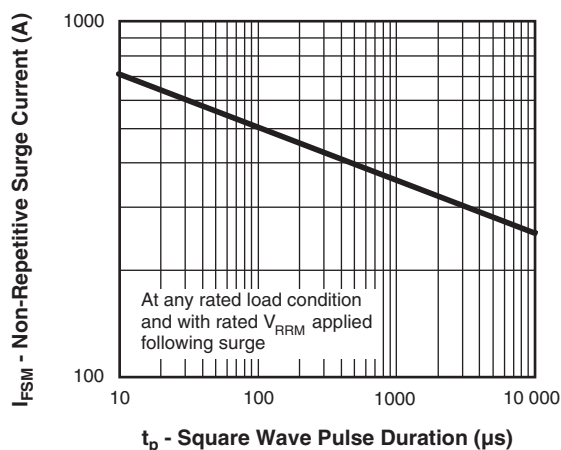


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

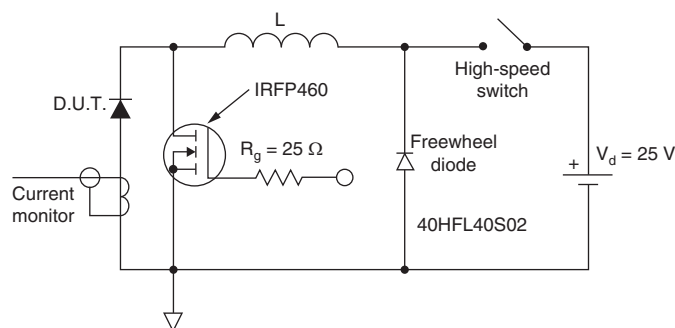


Fig. 8 - Unclamped Inductive Test Circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R applied



ORDERING INFORMATION TABLE

Device code	VS-	16	C	T	Q	100	S	TRL	PbF
	1	2	3	4	5	6	7	8	9

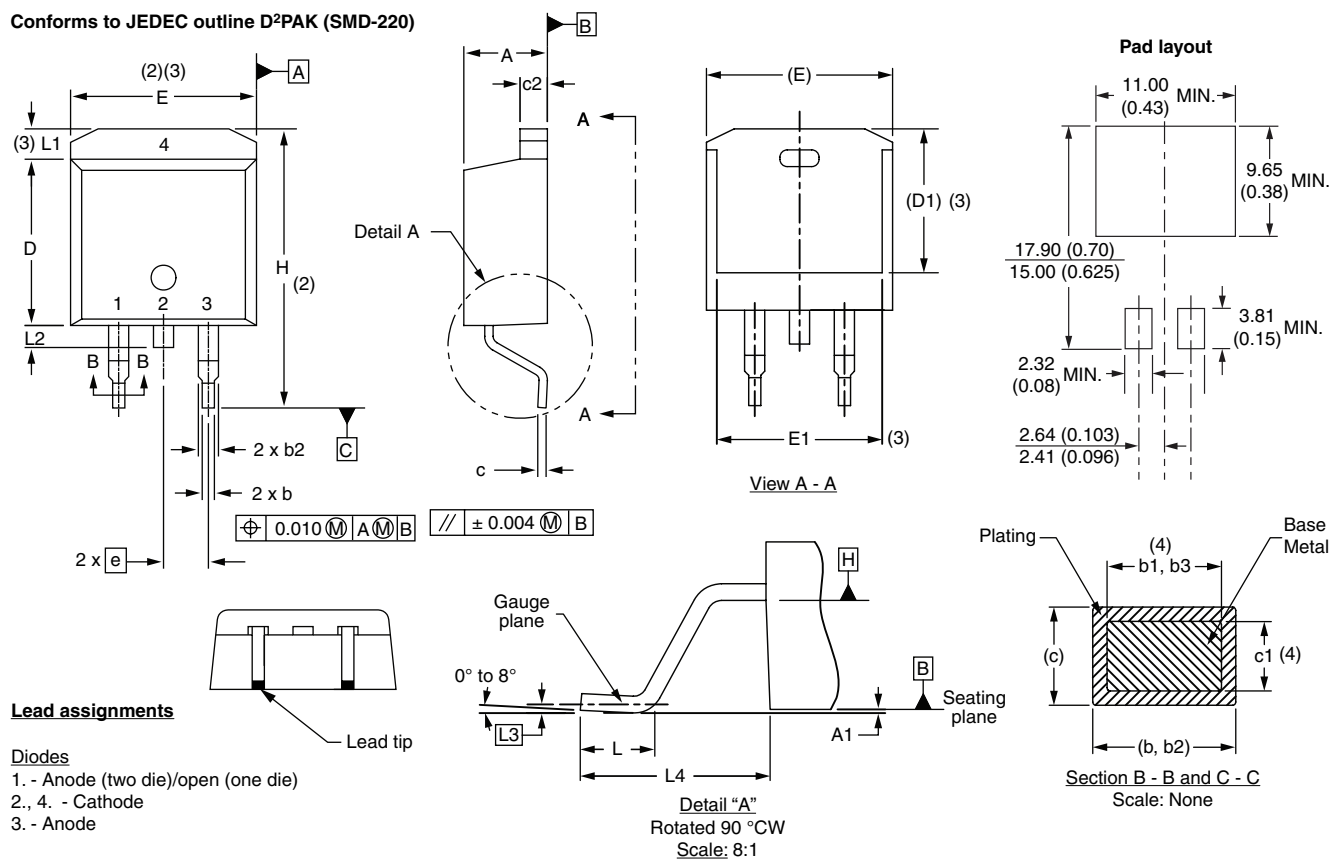
- | | | | |
|----------|---|--|---|
| 1 | - | Vishay Semiconductors product suffix | |
| 2 | - | Current rating (16 A) | |
| 3 | - | Circuit configuration: C = Common cathode | |
| 4 | - | T = TO-220 | |
| 5 | - | Schottky "Q" series | |
| 6 | - | Voltage ratings | 060 = 60 V
080 = 80 V
100 = 100 V |
| 7 | - | • S = D ² PAK
• -1 = TO-262 | |
| 8 | - | • None = Tube (50 pieces)
• TRL = Tape and reel (left oriented - for D ² PAK only)
• TRR = Tape and reel (right oriented - for D ² PAK only) | |
| 9 | - | PbF = Lead (Pb)-free | |

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95014
Part marking information	www.vishay.com/doc?95008
Packaging information	www.vishay.com/doc?95032
SPIICE model	www.vishay.com/doc?95279

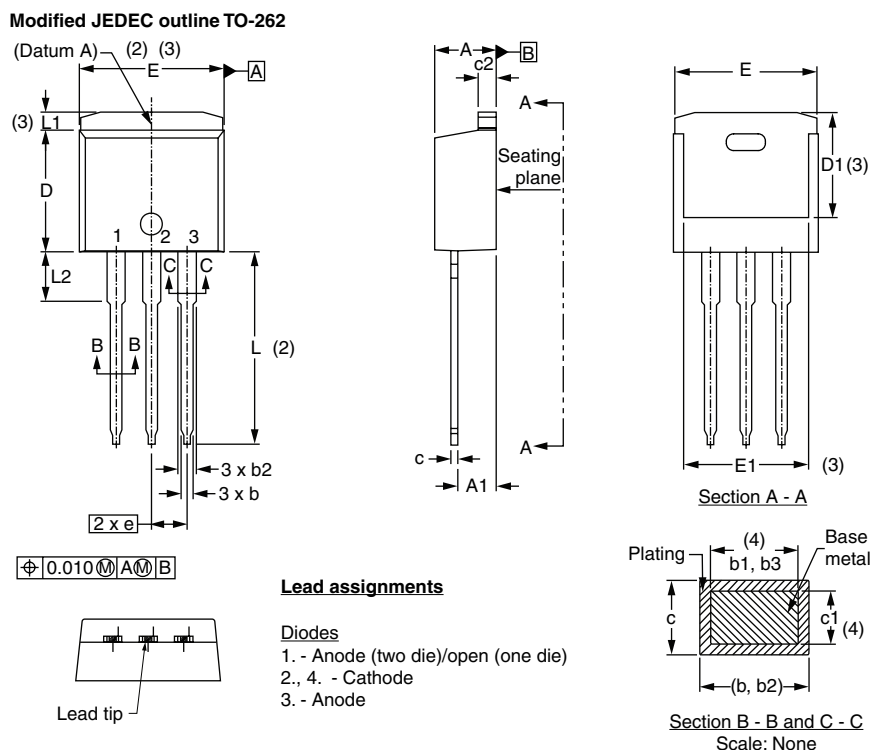
D²PAK, TO-262

DIMENSIONS FOR D²PAK in millimeters and inches

Conforms to JEDEC outline D²PAK (SMD-220)



DIMENSIONS FOR TO-262 in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
c	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
e	2.54 BSC		0.100 BSC		
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



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