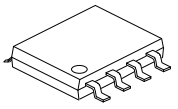
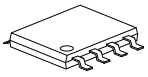
US3075-US3375

CMOS IC

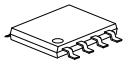
POWER DISTRIBUTION SWITCH



SOP-8



TSSOP-8



MSOP-8

■ DESCRIPTION

The UTC **US3075-US3375** is a power distribution switch. It particularly designed for self-powered and bus-powered Universal Serial Bus (USB) applications.

The $R_{DS(ON)}$ of the MOSFET switch is as low as 100mΩ. $\overline{OC}$ is open-drain output report over-current or over-temperature event which has deglitch typical 9ms timeout period typically 9ms.

The UTC **US3075-US3375** incorporates protection circuits including current limiting circuit with foldback function, thermal shutdown circuit designed to prevent catastrophic switch failure due to increasing power dissipation when continuous heavy loads or short circuit occurs. Besides, a built-in charge pump is used to drive the N-channel MOSFET that is free of parasitic body diode to eliminate any reversed current flow across the switch when it is powered off.

The UTC **US3075-US3375** is applied in high-side power protection switch, USB power management, USB host and self-powered bubs, USB bus-powered hubs, hot plug-in power supplies, battery-charger circuits.

■ FEATURES

* Operating on The Range of 3V to 5.5V
* High-Side MOSFET with 100mΩ $R_{DS(ON)}$ (TYP.)
* Quiescent Supply Current: 65μA
* Available With 4 Versions of Current Limits with Foldback
* Rise Time: 400μS (TYP.)
* UVLO (Under Voltage Lockout)
* Output Shutdown Pull-low Resister
* Shutdown Supply Current: 1μA (MAX.)
* Logic Level Enable Pin, Available with Active-High or Active-Low Version
* Reverse Current is Not Generated when in Power Off State
* Deglitched Open-Drain Over-Current Flag Output ($\overline{OC}$)

■ ORDERING INFORMATION

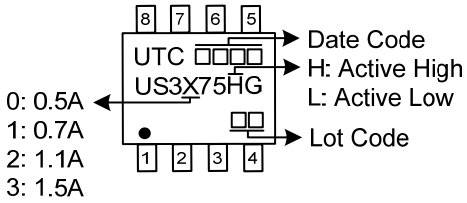
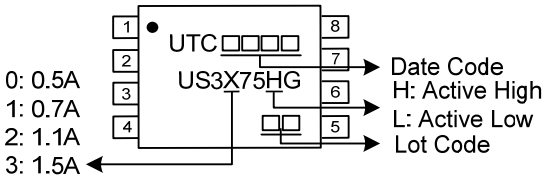
Ordering Number	Package	Packing
US3X75XG-S08-R	SOP-8	Tape Reel
US3X75XG-SM1-R	MSOP-8	Tape Reel
US3X75XG-P08-R	TSSOP-8	Tape Reel

US3X75XG-S08-R
- (1) Packing Type
- (2) Package Type
- (3) Green Package
- (4) Logic Level Enable Pin
- (5) Output Current

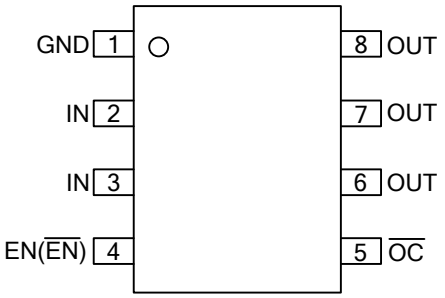
(1) R: Tape Reel
(2) S08: SOP-8, SM1: MSOP-8, P08: TSSOP-8
(3) G: Halogen Free and Lead Free
(4) H: Active High, L: Active Low
(5) 0: 0.5A, 1: 0.7A, 2: 1.1A, 3: 1.5A

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■ MARKING

SOP-8/MSOP-8	TSSOP-8
	

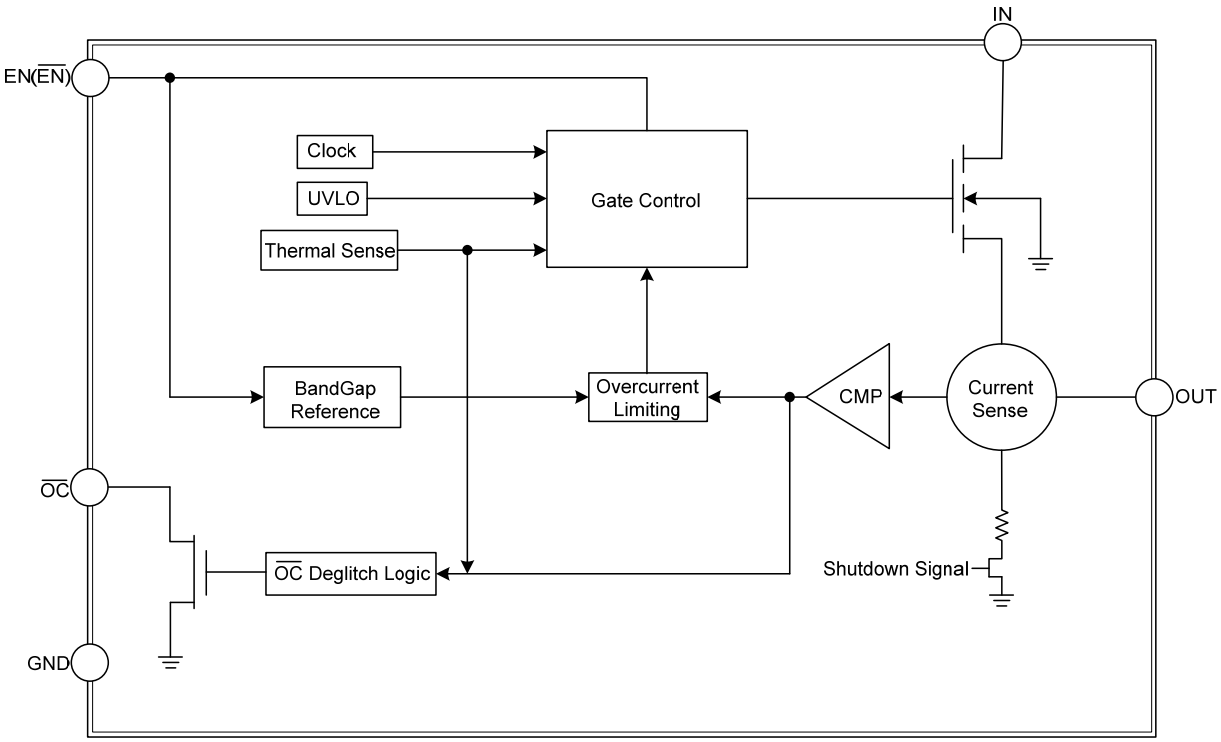
■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO	PIN NAME	DESCRIPTION
1	GND	Ground
2,3	IN	Input Supply: Output MOSFET Drain, which also supplies IC's internal circuitry. Connect to positive supply.
4	EN($\overline{EN}$)	Enable: Logic level enable input. Make sure EN pin never floating.
5	$\overline{OC}$	Over-Current: Open-Drain $\overline{OC}$ output.
6,7,8	OUT	Switch Output: Output MOSFET Source of switch. Typically connect to switched side of load.

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V_{IN}	6	V
Output Voltage	V_{OUT}	6	V
Output Current	I_{OUT}	Internally Limited	A
Enable Input	V_{EN}	-0.3 ~ 6	V
Storage Temperature	T_{STG}	-65~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ OPERATING RATINGS

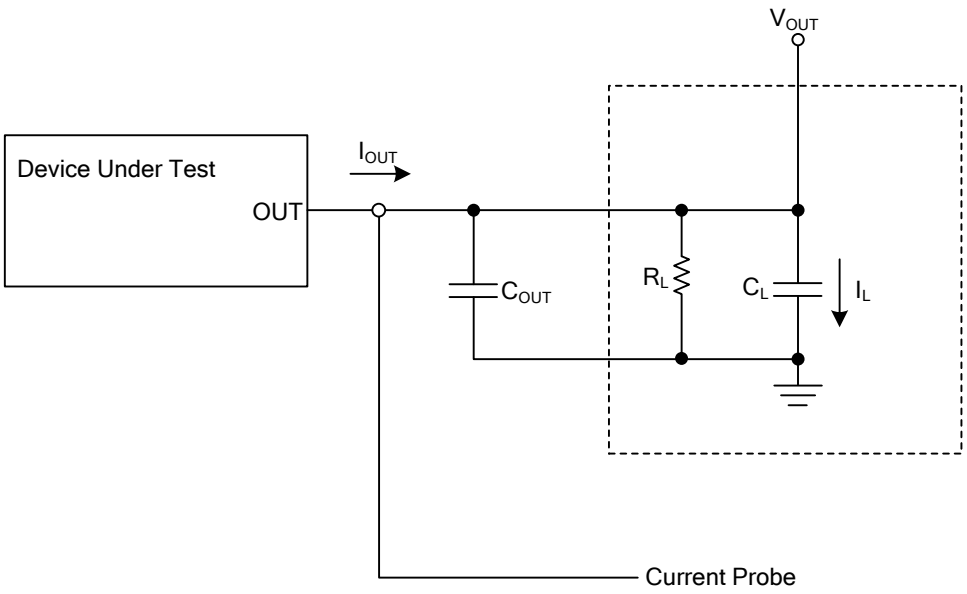
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V_{IN}	+3 ~ +5.5	V
Ambient Operating Temperature	T_{OPR}	-40~ +85	°C

■ ELECTRICAL CHARACTERISTICS

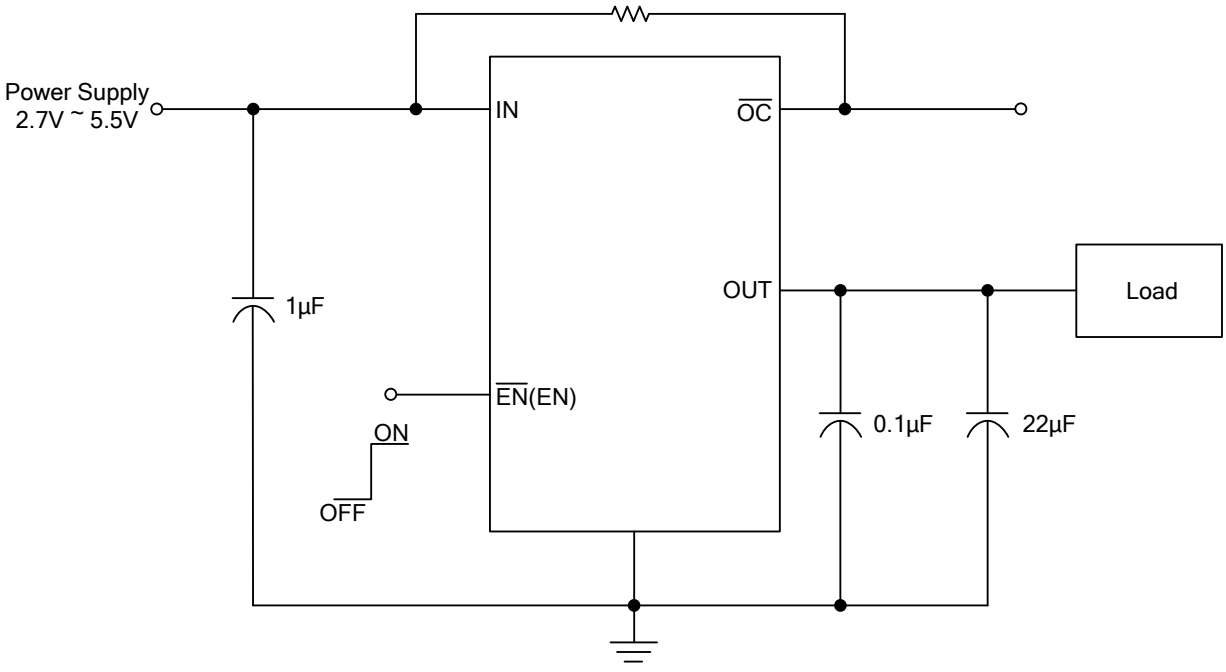
(V_{IN}=5V, C_{IN}=1μF, C_{OUT}=1μF, R_L=10Ω, T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Input Voltage	V_{IN}			3		5.5	V
Output MOS $R_{DS(ON)}$		US3375, I_{OUT} =1.5A			100	130	mΩ
		US3275, I_{OUT}=1A					
		US3175, I_{OUT}=0.5A					
		US3075, I_{OUT}=0.3A					
Supply Current	I_{IN}				65		μA
Output Turn-on Rising Time		R_L=10Ω, 90% Settling			400		μs
Current Limit Threshold	$I_{THD(LIMIT)}$	US3075	V_{OUT}=4V	0.5	0.75	1.4	A
		US3175		0.7	1	1.7	A
		US3275		1.1	1.5	2.1	A
		US3375		1.5	2	2.8	A
Short-Circuit Current	I_{SC}	US3075	V_{OUT}=0V, 2.7V<V_{IN}<5.5 V	0.2	0. 5	0.7	A
		US3175		0.2	0.67	1	A
		US3275		0.2	1	1.4	A
		US3375		0.2	1.3	1.9	A
EN Input Threshold				1.3	1.6	1.8	V
Shutdown Supply Current	I_{SHDN}				0.1	1	μA
Shutdown Pull Low Resistance					75	150	Ω
Output Leakage Current	$I_{O(LEAK)}$	EN="0", V_{OUT}=0V			0.5	1	μA
V_{IN} Under Voltage Lockout				2.2	2.5	3	V
V_{IN} Under Voltage Hysteresis					200		mV
Thermal Limit					135		°C
Thermal Limit Hysteresis					20		°C
$\overline{OC}$ Deglitch		$\overline{OC}$ assertion or deassertion		4	9	15	ms
$\overline{OC}$ Output Low Voltage		$I_{\overline{OC}}$ = 5mA				0.4	V
$\overline{OC}$ Off-State Current		$V_{\overline{OC}}$ = 5V				1	μA

■ TEST CIRCUIT



■ TYPICAL APPLICATION CIRCUIT



■ FUNCTIONAL DESCRIPTION

Input and Output

IN pin (Input) is the power supply connection to the logic circuitry and the drain of the output MOSFET. OUT pin (Output) is the source of the output MOSFET. In a typical application, current flows through the switch from input to output toward the load. Both OUT pins must be connected together to the load.

Thermal Shutdown

Thermal shutdown is designed to prevent this device from being damaged due to excessive power dissipation. It is used to turn off the output MOSFET when the if the temperature is higher than135°C, 20°C of hysteresis prevents the switch turn on until the die temperature drops to 115°C. Thermal shutdown circuit functions only when the switch is enabled.

Under Voltage Lockout

UVLO (Under voltage Lockout) turns off the MOSFET switch once the input voltage falls below 2.3V (TYP.) and if the input voltage exceeds 2.5V (TYP.), the switch will be turned on. UVLO detection functions only when the switch is enabled.

Current Limiting

The current limit value is fixed internally, typically 2A, 1.5A, 1A, 0.75A for **US3375/ US3275/ US3175/ US3075** respectively. There is foldback of current limit When V_{OUT} is less than 1.5V, there is foldback of current limit.

$\overline{OC}$ Function

The $\overline{OC}$ open-drain output is asserted (active low) when an over current or over temperature shutdown condition is encountered after a 9-ms deglitch timeout. The output keeps asserted until the over current or over temperature condition is removed.

■ APPLICATION INFORMATION

Supply Filtering

To avoid supply transients, 1μF bypass capacitor is strongly recommended to located between IN to GND. Without a bypass capacitor, an output short may cause sufficient ringing on the input (from supply lead inductance) to damage internal control circuitry.

Input transients value must be not higher than the absolute maximum supply voltage ($V_{IN\ max}$ = 6V) even for a short duration.

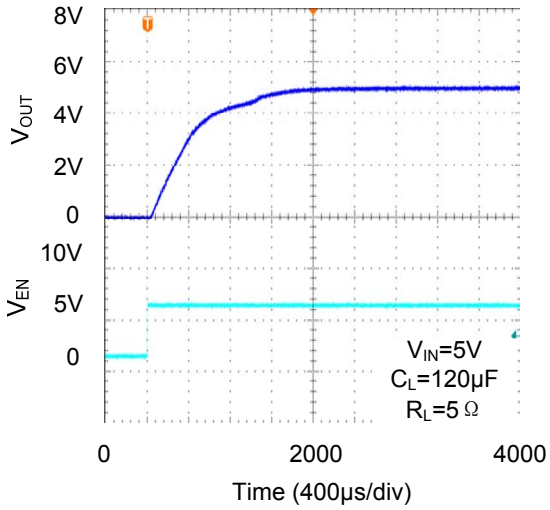
EN, the Enable Input

The enable pin, EN, must be driven logic high or logic low for a clearly defined input. The input is not allowed to float, or unpredictable operation may be occurs. EN should not be allowed to go negative with respect to GND.

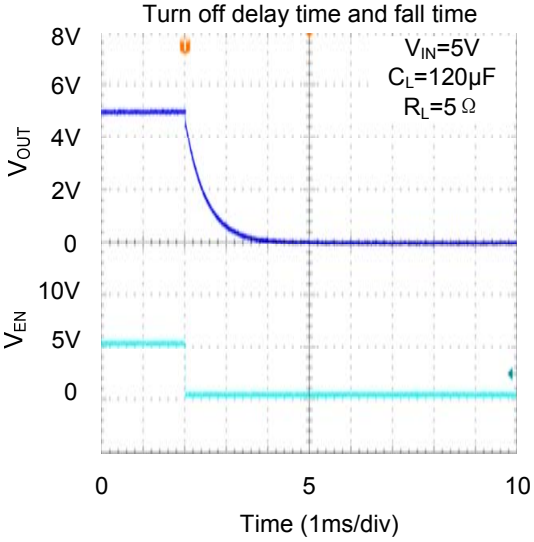
■ TYPICAL CHARACTERISTICS

(V_{IN}= 5V, 3275H, C_{IN}=1μF, C_{OUT}=1μF, V_{EN}=0V, T_A=25°C, unless otherwise specified.)

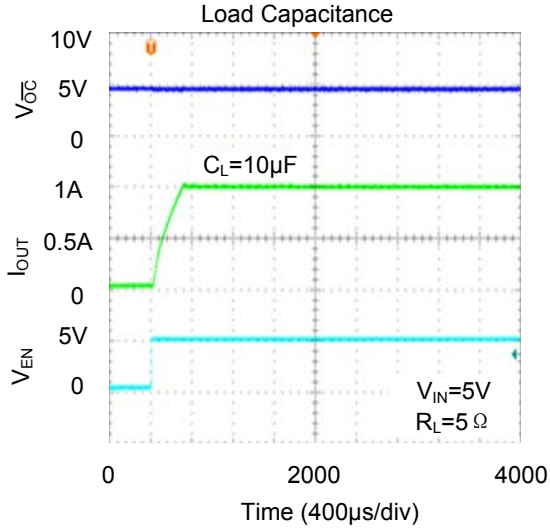
Turn on delay time and rise time



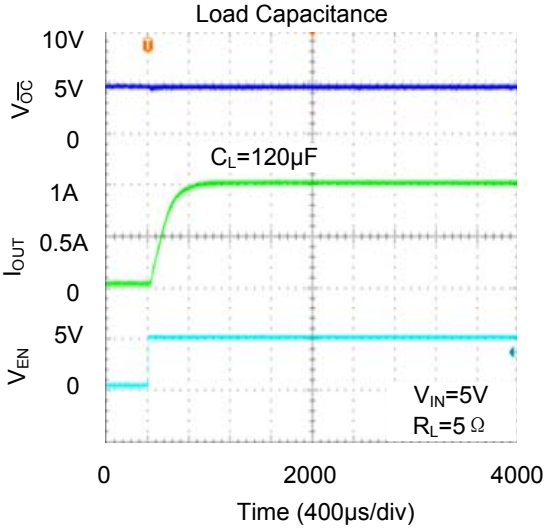
Turn off delay time and fall time



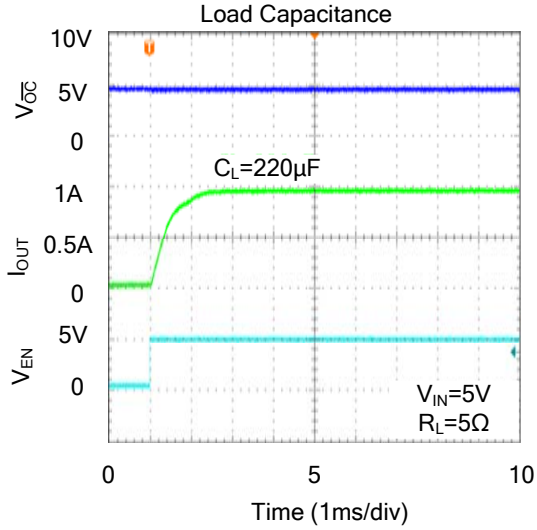
Inrush Current With Different Load Capacitance



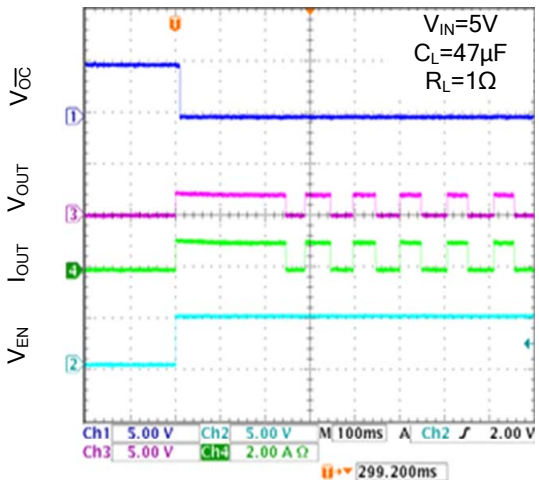
Inrush Current With Different Load Capacitance



Inrush Current With Different Load Capacitance

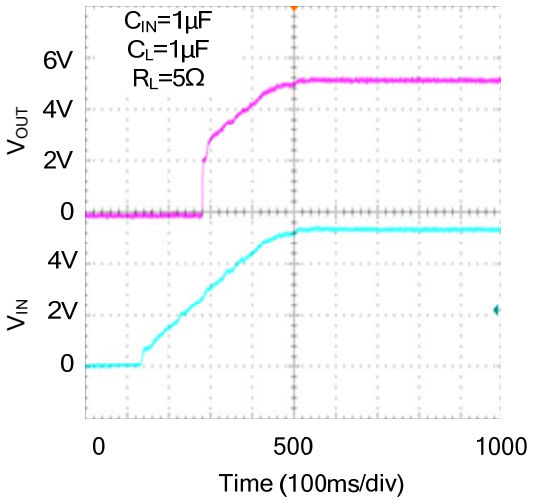


Thermal Shutdown Response

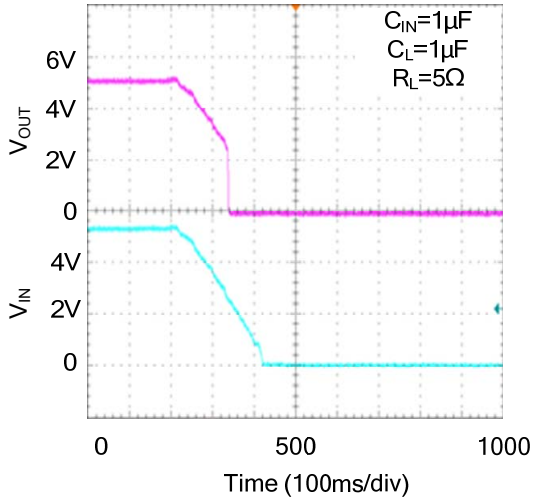


■ TYPICAL CHARACTERISTICS (Cont.)

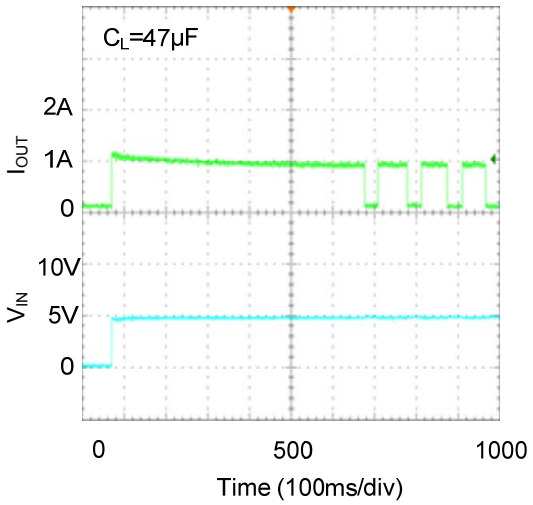
UVLO protection at rising



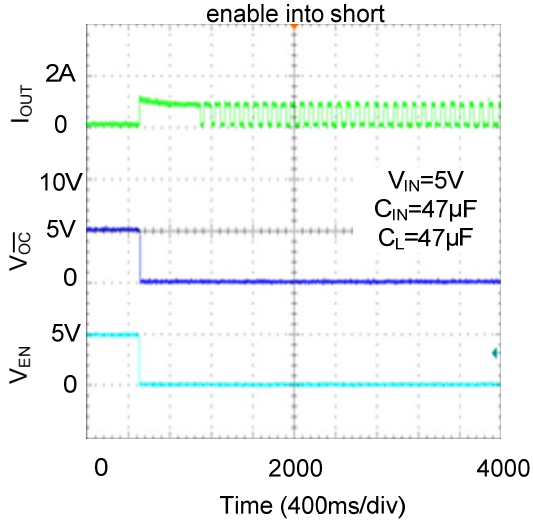
UVLO protection at falling



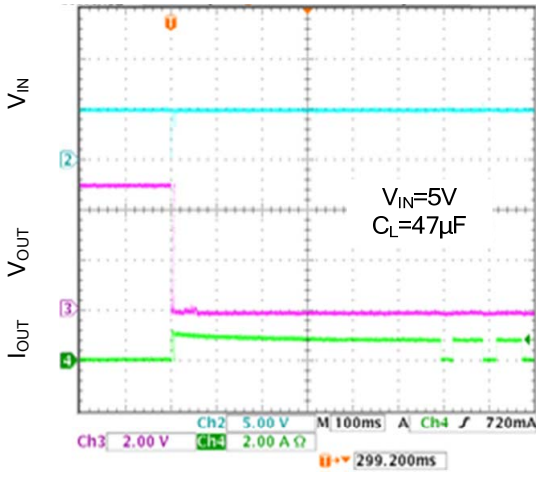
Short circuit response at start up



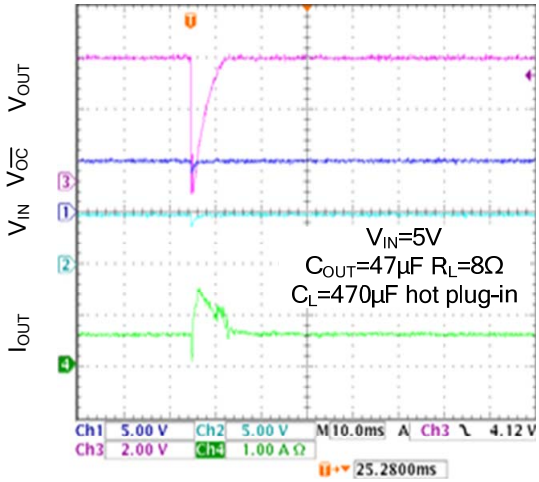
Short-circuit current, device enable into short



Inrush short circuit response

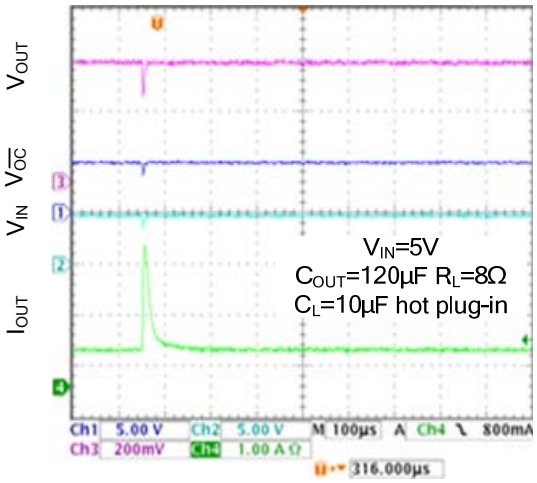


Capacitance Load Inrush Response



■ TYPICAL CHARACTERISTICS (Cont.)

Capacitance Load Inrush Response



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