



U74HC245

CMOS IC

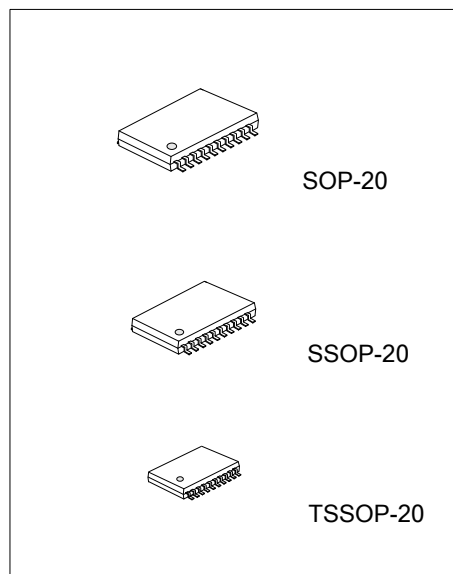
OCTAL BUS TRANSCEIVERS WITH 3-STATE OUTPUTS

■ DESCRIPTION

The **U74HC245** is designed for the asynchronous communication between data buses. While the direction-control(DIR) is high, data transmits from the A bus to the B bus. Data transmits from the B bus to the A bus if DIR is low. The output-enable($\overline{OE}$) will isolate the device from the buses when high voltage is applied on it.

■ FEATURES

- * Operate from 2V to 6V
- * Max t_{PD} is 18ns at 6V

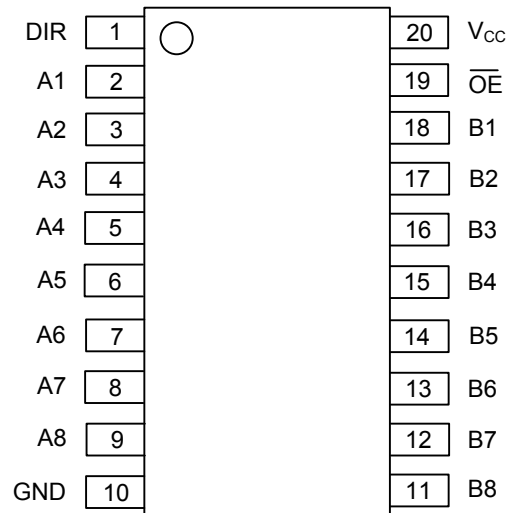


■ ORDERING INFORMATION

Ordering Number		Package	Packing
Lead Free	Halogen Free		
U74HC245L-S20-R	U74HC245G-S20-R	SOP-20	Tape Reel
U74HC245L-S20-T	U74HC245G-S20-T	SOP-20	Tube
U74HC245L-R20-R	U74HC245G-R20-R	SSOP-20	Tape Reel
U74HC245L-R20-T	U74HC245G-R20-T	SSOP-20	Tube
U74HC245L-P20-R	U74HC245G-P20-R	TSSOP-20	Tape Reel
U74HC245L-P20-T	U74HC245G-P20-T	TSSOP-20	Tube

U74HC245L-R20-R (1)Packing Type (2)Package Type (3)Lead Free	(1) R: Tape Reel, T: Tube (2) P20: TSSOP-20, R20: SSOP-20, S20: SOP-20 (3) G: Halogen Free, L: Lead Free
--	---

PIN CONFIGURATION

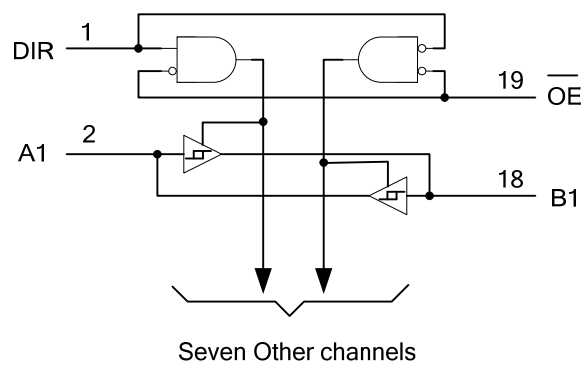


FUNCTION TABLE

INPUT		FUNCTION
$\overline{OE}$	DIR	
H	X	Isolation
L	H	Transmit data from A bus to B bus
L	L	Transmit data from B bus to A bus

Note: H: HIGH voltage level; L: LOW voltage level.

LOGIC DIAGRAM



■ **ABSOLUTE MAXIMUM RATING** (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V _{CC}	-0.5~7.0	V
Input Clamp Current (V _{IN} <0)	I _{IK}	±20	mA
Output Clamp Current (V _{OUT} <0)	I _{OK}	±20	mA
Output Current	I _{OUT}	±35	mA
V _{CC} or GND Current	I _{CC}	±70	mA
Storage Temperature	T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ **RECOMMENDED OPERATING CONDITIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V _{CC}		2	5	6	V
Input Voltage	V _{IN}		0		V _{CC}	V
Output Voltage	V _{OUT}		0		V _{CC}	V
Input Transition Rise or Fall Rate	t _R	V _{CC} =2V			1000	ns
		V _{CC} =4.5V			500	ns
		V _{CC} =6V			400	ns
Operating Temperature	T _{OPR}		-40		85	°C

■ **ELECTRICAL CHARACTERISTICS** (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
High-level input voltage	V _{IH}	V _{CC} =2V	1.5			V
		V _{CC} =4.5V	3.15			V
		V _{CC} =6V	4.2			V
Low-level output voltage	V _{IL}	V _{CC} =2V			0.5	V
		V _{CC} =4.5V			1.35	V
		V _{CC} =6V			1.8	V
High-Level Output Voltage	V _{OH}	V _{CC} =2V, I _{OH} =-20μA	1.9	1.998		V
		V _{CC} =4.5V, I _{OH} =-20μA	4.4	4.499		V
		V _{CC} =6V, I _{OH} =-20μA	5.9	5.999		V
		V _{CC} =4.5V, I _{OH} =-6mA	3.98	4.3		V
		V _{CC} =6V, I _{OH} =-7.8mA	5.48	5.8		V
Low-Level Output Voltage	V _{OL}	V _{CC} =2V, I _{OL} =20μA		0.002	0.1	V
		V _{CC} =4.5V, I _{OL} =20μA		0.001	0.1	V
		V _{CC} =6V, I _{OL} =20μA		0.001	0.1	V
		V _{CC} =4.5V, I _{OH} =6mA		0.17	0.26	V
		V _{CC} =6V, I _{OL} =7.8mA		0.15	0.26	V
Input Current of DIR or $\overline{\text{OE}}$	I _{I(LEAK)}	V _{CC} =6V, V _{IN} =V _{CC} or GND		±0.1	±100	nA
Output OFF-state current	I _{OZ}	V _{CC} =6V, V _{OUT} =V _{CC} or GND		±0.01	±0.5	μA
Quiescent Supply Current	I _Q	V _{CC} =6V, V _{IN} =V _{CC} or GND, I _{OUT} =0			8	μA
Input Capacitance of DIR or $\overline{\text{OE}}$	C _{IN}	V _{CC} =6V, V _{IN} =V _{CC} or GND		3	10	pF

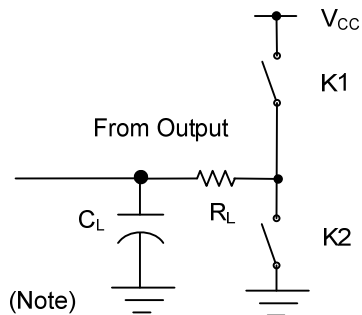
■ **SWITCHING CHARACTERISTICS** ($T_A=25^{\circ}\text{C}$, $C_L=50\text{pF}$, $R_L=1\text{k}\Omega$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation delay from input (A or B) to output (B or A)	t_{PD} (t_{PLH}/t_{PHL})	$V_{CC}=2\text{V}$		40	105	ns
		$V_{CC}=4.5\text{V}$		15	21	ns
		$V_{CC}=6\text{V}$		12	18	ns
3-state output enable time from input ($\overline{OE}$) to output (A or B)	t_{EN} (t_{PZL}/t_{PZH})	$V_{CC}=2\text{V}$		125	230	ns
		$V_{CC}=4.5\text{V}$		23	46	ns
		$V_{CC}=6\text{V}$		20	39	ns
3-state output disable time from input ($\overline{OE}$) to output (A or B)	t_{DIS} (t_{PLZ}/t_{PHZ})	$V_{CC}=2\text{V}$		74	200	ns
		$V_{CC}=4.5\text{V}$		25	40	ns
		$V_{CC}=6\text{V}$		21	34	ns
Output transition time, (A or B)	t_T (t_R/t_F)	$V_{CC}=2\text{V}$		20	60	ns
		$V_{CC}=4.5\text{V}$		8	12	ns
		$V_{CC}=6\text{V}$		6	10	ns

■ **OPERATING CHARACTERISTICS**

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	C_{PD}	No load		40		pF

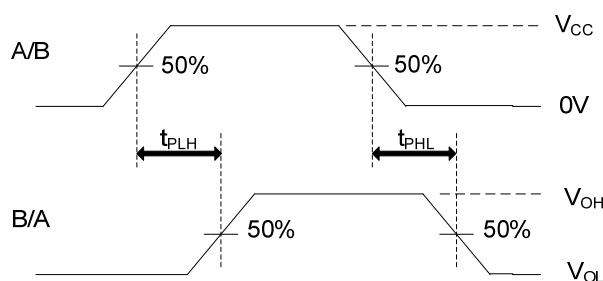
■ TEST CIRCUIT AND WAVEFORMS



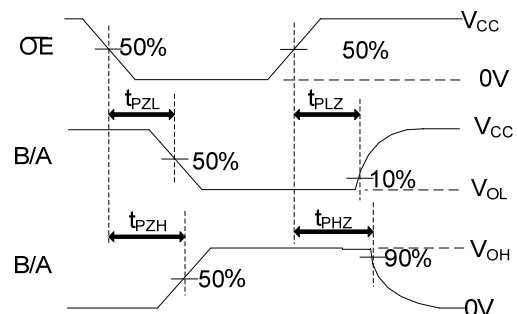
TEST	K1	K2
t_{PLH}/t_{PHL}	Open	Open
t_{PHZ}/t_{PZH}	Open	Close
t_{PLZ}/t_{PZL}	Close	Open

Note: C_L includes probe and jig capacitance.

$$P_{RR} \leq 1\text{MHz}, Z_O = 50\Omega, t_R \leq 6\text{ns}, t_F \leq 6\text{ns}$$



Propagation Delay Times



Enable and Disable Times

UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice.