

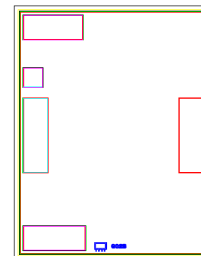
Ultra High Dynamic Range, Shutdown

Monolithic Amplifier Die TSS-13HLN-D+

50Ω 1MHz to 1 GHz

The Big Deal

- Ultra-High IP3, +42.9 dBm typ.
- Medium power, +28.4 dBm typ.
- Excellent Noise Figure, 1.4 dB typ.
- Shutdown feature
- Operates over wide DC input: +3V to +8V



Product Overview

TSS-13HLN-D+ (RoHS compliant) is an advanced wideband amplifier die with shutdown feature. It is fabricated using E-PHEMT technology and offers extremely high dynamic range over a broad frequency range and with low noise figure. In addition, the TSS-13HLN-D+ has good input and output return loss over a broad frequency range.

Key Features

Feature	Advantages
Broad Band: 1MHz to 1GHz	Broadband covering primary wireless communications bands: VHF, UHF, Cellular
Extremely High IP3 40.6 dBm typical at 1 MHz 42.9 dBm typical at 0.5 GHz	The TSS-13HLN-D+ matches industry leading IP3 performance relative to device size and power consumption. The combination of the design and E-PHEMT Structure provides enhanced linearity over a broad frequency range as evidence in the IP3 being approximately 14-15 dB above the P1dB point. This feature makes this amplifier ideal for use in: • Driver amplifiers for complex waveform up converter paths • Drivers in linearized transmit systems • Secondary amplifiers in ultra-High Dynamic range receivers
Shutdown feature	Allow users to turn on and off the amplifier with pulsed signals while keeping the power supply at constant voltage to minimize DC power consumption
Low Noise Figure 1.4 dB at 0.5 GHz	Enables lower system noise figure performance and along with High OIP3 provides high dynamic range
High P1dB, 28.4 dBm at 0.5 GHz	High P1dB, High OIP3, Low NF results in a very dynamic range preventing amplifier saturation under strong interfering signals.



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Monolithic Amplifier Die

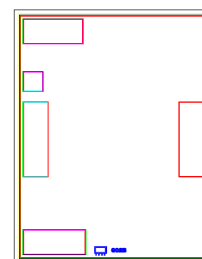
1 MHz to 1 GHz

Product Features

- High IP3, 42.9 dBm typ. at 0.5 GHz
- Gain, 23 dB typ. at 0.5 GHz
- Low noise figure, 1.4 dB at 0.5 GHz
- High P1dB 28.4 dBm 0.5 GHz
- Shutdown feature
- Operates over wide DC input: +3V to 8V

Typical Applications

- Base station infrastructure
- CATV
- Cellular



+RoHS Compliant

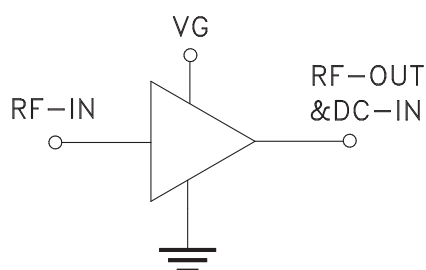
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Ordering Information: Refer to Last Page

General Description

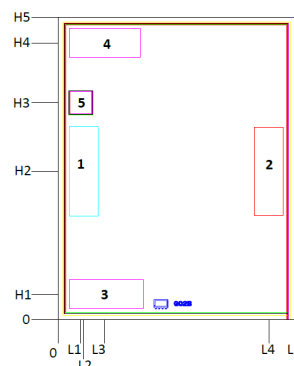
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Simplified Schematic and Pad description



Pad#	Function
1	RF-IN
2	RF-OUT & DC-IN
5	VG
3,4	Ground

Bonding Pad Position



Dimensions in μm , Typical

L1	L2	L3	L4	L5	H1	H2	H3	H4	H5	Thick-ness	Die Size	Pad Size 1	Pad Size 2	Pad Size 3	Pad Size 4	Pad Size 5
76.0	86.0	160.0	705.0	790.0	86.0	495.0	725.0	925.0	1010.0	100	1010 x 900	101 x 301	98 x 298	251 x 101	241 x 101	81 x 81



Electrical Specifications¹ at 25°C, unless noted

Parameter	Condition(MHz)	Vd = 8V		Vd = 5V		Vd = 3V		Units
		ON State	OFF State	ON State	OFF State	ON State	OFF State	
Frequency Range		1-1000	1-1000	1-1000	1-1000	1-1000	1-1000	MHz
Noise Figure	1	3	--	3.1	--	3	--	dB
	20	1.4	--	1.1	--	1.1	--	
	250	1.3	--	1.1	--	1.1	--	
	500	1.4	--	1.1	--	1.1	--	
	1000	1.6	--	1.2	--	1.3	--	
Gain	1	25	-24	24.7	-24	23.9	-24	dB
	20	24.4	-22	24.2	-21	23.5	-22	
	250	23.2	-21	23	-21	22.3	-21	
	500	23	-22	22.8	-22	21.9	-22	
	1000	21.1	-26	20.9	-27	19.5	-27	
Reversed Isolation	1-1000	26	27	26	27	26	27	dB
Input Return Loss	1	11	12	11	12	10	12	dB
	20	16	12	15	12	15	12	
	250	17	12	18	12	19	12	
	500	20	12	21	12	17	12	
	1000	14	10	14	10	10	10	
Output Return Loss	1	11	1	11	1	11	1	dB
	20	19	2	19	2	21	2	
	250	18	2	18	2	21	2	
	500	33	2	25	2	21	2	
	1000	12	2	11	2	10	2	
Output Power @ 1dB compression AMP-ON	1	25.2	--	20.3	--	14.2	--	dBm
	20	26.7	--	22.4	--	16.3	--	
	250	28	--	24.1	--	19.3	--	
	500	28.4	--	24.5	--	19.5	--	
	1000	26.7	--	23.4	--	18.3	--	
Output IP3 (Pout = 0dBm/Tone)	1	40.6	--	38.4	--	31.6	--	dBm
	20	41.3	--	40.3	--	33.2	--	
	250	41.2	--	39.6	--	34.4	--	
	500	42.9	--	39.2	--	33	--	
	1000	40.8	--	36.2	--	29.4	--	
Device Operating Voltage(VDD)		8	8	5	5	3	3	V
Device Operating Current(ID)		234	7.5	142	5	72	3	mA
Control Voltage (VG)		0	5	0	5	0	5	V
Device Current Variation vs. Temperature		-136 ²	--	10 ³	--	33 ³	--	uA/degC
Device Current Variation vs. Voltage		0.025	--	0.025	--	0.033	--	mA/mV
Thermal Resistance		23.3	--	23.3	--	23.3	--	degC/W

1. Measured on Mini-Circuits Characterization test board. Die is packaged in 3x3 mm, 12-lead MCL package and soldered on TB-TSS-13HLN+. See Characterization Test Circuit (Fig. 1)

2. (Current at 95°C - Current at -45°C)/140

3. (Current at 105°C - Current at -45°C)/150

Absolute Maximum Ratings⁴

Parameter	at 8V	at 3V&5V
Operating Temperature (ground lead)	-40°C to 95°C	-40°C to 105°C
Power Dissipation	3.3W	3.3W
Device Operating Current (ID)	241mA	151mA
Input Power (CW)	+28 dBm (5 minutes max) +10 dBm (continuous) for 1-30 MHz +18 dBm (continuous) for 0.03-1 GHz	+28 dBm (5 minutes max) +6 dBm (continuous) for 1-30 MHz +10 dBm (continuous) for 0.03-1 GHz
DC Voltage on Pad 2	10V	10V
DC Voltage on Pad 5	10V	10V

4. Permanent damage may occur if any of these limits are exceeded.
Electrical maximum ratings are not intended for continuous normal operation.

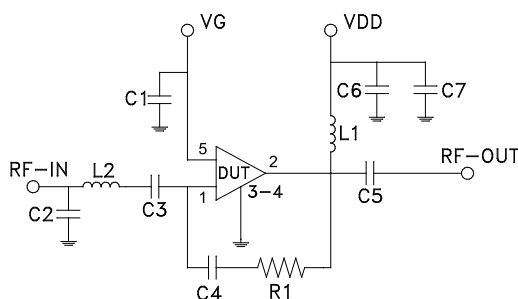
Switching Specifications

Parameter		8V Typ	5V Typ.	3V Typ.	Units
Amplifier ON to Shutdown	OFF TIME (50% Control to 10% RF)	5.3	4.8	6.2	μ s
	FALL TIME (90 to 10% RF)	7.3	7.4	3.6	
Amplifier Shutdown to ON	ON TIME (50% Control to 90% RF)	77.7	95.2	144.7	μ s
	RISE TIME (10% to 90% RF)	54.2	60.0	200.7	
Control Voltage Leakage		633.3	482.9	311.0	mV

Control Voltage (V_G) Fig. 1

	Min.	Typ.	Max.	Units
Amplifier-ON	—	0	0.7	V
Amplifier-OFF	1.9	5	—	V

Characterization Test Circuit / Recommended Application Circuit



Component	Size	Value	Part Number	Manufacturer
C1	0402	0.1 μ F	GRM155R71C104KA88D	Murata
C2	0402	1.5pF	GRM1555C1H1R5CA1D	Murata
C3	0603	2.2 μ F	GRM188C71E225KE11D	Murata
C4	0402	0.1 μ F	GRM155R71C104KA88D	Murata
C5	0603	2.2 μ F	GRM188C71E225KE11D	Murata
C6	0402	1000pF	GRM1555C1H102JA01D	Murata
C7	0805	10 μ F	GRM21BC71E106KE11L	Murata
L1	1210	15 μ H	LQH32DN150K53L	Murata
L2	0603	5.1nH	0603CS-5N1XJLU	Coilcraft
R1	0402	1.5K Ω	RK73H1ETTP1501F	Koa

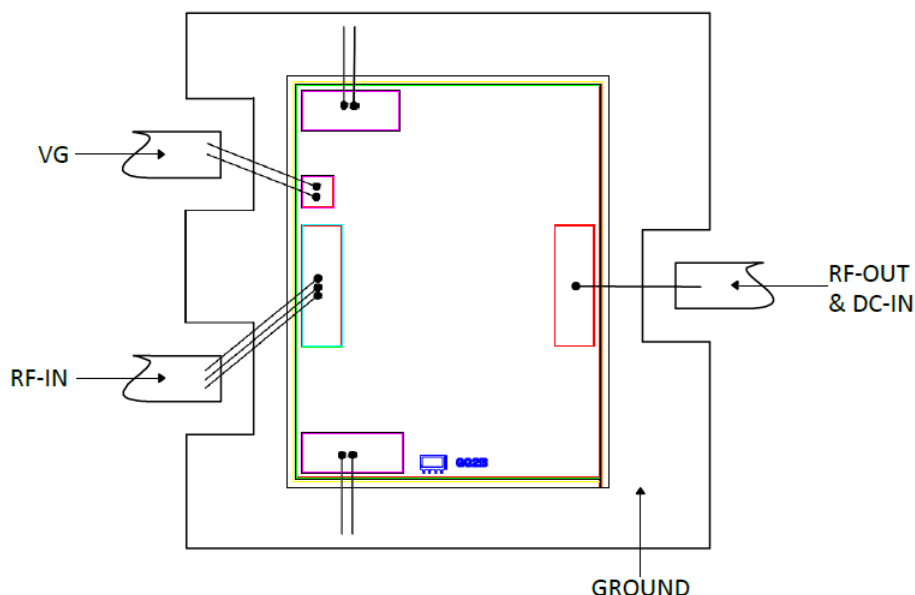
Fig 1. Block diagram of Test Circuit used for characterization. (DUT is packaged 3x3mm, 12-lead MCLP package and soldered on Mini-Circuits Characterization test board TB-TSS-13HLN+)

Gain, Return loss, Output power at 1dB compression (P_{1dB}), output IP3 (OIP3) and noise figure measured using Agilent's N5242A PNA-X microwave network analyzer.

Conditions:

- Gain and Return Loss: $P_{in} = -25$ dBm
- Output IP3 (OIP3): Two tones, spaced 1 MHz apart, +0 dBm/tone at output.
- Switching Time
RF Signal: $P_{in} = -25$ dBm, $f_{RF} = 500$ MHz.
 $V_{DD} = 8V, 5V$ or $3V$ DC, $V_G =$ Pulse signal at 1 KHz with $V_{HIGH} = 5V$, $V_{LOW} = 0V$, 50% duty cycle.

Assembly Diagram



Assembly and Handling Procedure

1. Storage
Dice should be stored in a dry nitrogen purged desiccators or equivalent.
2. ESD
MMIC E-PHEMT amplifier dice are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be opened in clean room conditions at an appropriately grounded anti-static workstation. Devices need careful handling using correctly designed collets, vacuum pickup tips or sharp antistatic tweezers to deter ESD damage to dice.
3. Die Attach
The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are DieMat DM6030HK-PT/H579 or Ablestik 84-1LMISR4. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total die periphery. Parts shall be cured in a nitrogen filled atmosphere per manufacturer's cure condition. It is recommended to use antistatic die pick up tools only.
4. Wire Bonding
Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the dice gold bond pads. Thermosonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1 mil diameter. Bonds must be made from the bond pads on the die to the package or substrate. All bond wires should be kept as short as low as reasonable to minimize performance degradation due to undesirable series inductance.

Additional Detailed Technical Information*additional information is available on our dash board.*

Performance Data	Data Table	
	Swept Graphs	
	S-Parameter (S2P Files) Data Set with and without port extension(.zip file)	
Case Style	Die	
Die Ordering and packaging information	Quantity, Package	Model No.
	Small, Gel - Pak: 5,10,50,100 KGD*	TSS-13HLN-DG+
	Medium†, Partial wafer: KGD*<1672	TSS-13HLN-DP+
	Large†, Full Wafer	TSS-13HLN-DF+
	†Available upon request contact sales representative	
	Refer to AN-60-067	
Environmental Ratings	ENV80	

*Known Good Dice ("KGD") means that the dice in question have been subjected to Mini-Circuits DC test performance criteria and measurement instructions and that the parametric data of such dice fall within a predefined range. While DC testing is not definitive, it does help to provide a higher degree of confidence that dice are capable of meeting typical RF electrical parameters specified by Mini-Circuits.

ESD Rating**

Human Body Model (HBM): Class 1A (pass 250V) in accordance with ANSI/ESD STM 5.1 - 2001

** Tested in industry standard 3X3 mm, 12 lead MCLP package.

Additional Notes

- Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
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