

TO-220

**TO-263
(D²PAK)**


Pin Definition:
TS39150
1. Input
2. Ground (tab)
3. Output

TO-220-5L

**TO-263-5L
(D²PAK)**


Pin Definition:

TS39151_{xx}
1. Enable
2. Input
3. Ground
4. Output
5. Flag

TS39152
1. Enable
2. Input
3. Ground
4. Output
5. Adjust

TS39153
1. Enable
2. Input
3. Ground
4. Output
5. Sense

General Description

TS39150/1/2/3 are 1.5A ultra low dropout linear voltage regulators that provide low voltage, high current output from an extremely small package. These regulator offers extremely low dropout (typically 400mV at 1.5A) and very low ground current (typically 12mA at 1.5A).

TS39150/1/2/3 are fully protected against over current faults, reversed input polarity, reversed lead insertion, over temperature operation, positive and negative transient voltage spikes, logic level enable control and error flag which signals whenever the output falls out of regulation.

On the TS39150/1/2/3, the enable pin may be tied to V_{IN} if it is not required for enable control.

Features

- Dropout voltage typically 0.4V @ $I_o=1.5A$
- Output Current up to 1.5A
- Low Ground Current
- Extremely Fast Transient Response
- Reversed Leakage & Reverse Battery Protection
- Error Flag Signals Output (TS39151 only)
- Internal Current Limit
- Thermal Shutdown Protection

Application

- PowerPC™ Power Supplies
- Battery Powered Equipment
- Consumer and Personal Electronics
- High Efficiency Linear Power Supplies
- SMPS Post Regulator and DC to DC Modules
- High-efficiency Post Regulator for Switching Supply
- Low-Voltage microcontrollers and Digital Logic

Ordering Information

Part No.	Package	Packing
TS39150CZ _{xx} C0	TO-220	50pcs / Tube
TS39150CM _{xx} RN	TO-263	800pcs / 13" Reel
TS39151CZ5 _{xx} C0	TO-220-5L	50pcs / Tube
TS39151CM5 _{xx} RN	TO-263-5L	800pcs / 13" Reel
TS39152CZ5 C0	TO-220-5L	50pcs / Tube
TS39152CM5 RN	TO-263-5L	800pcs / 13" Reel
TS39153CZ5 _{xx} C0	TO-220-5L	50pcs / Tube
TS39153CM5 _{xx} RN	TO-263-5L	800pcs / 13" Reel

Note: Where **xx** denotes voltage option, available are

18=1.8V

25=2.5V

33=3.3V

50=5.0V

Contact to factory for addition output voltage option.

Pin Description

Pin Configuration	Pin Description
Enable	TTL/COMS compatible input. Logic high is enable; logic low or open is shutdown
Input	Unregulated input: +16V maximum supply
Ground	Ground: Ground pin and TAB / heat sink are internally connected.
Output	Regulator output
Flag (fixed voltage)	Error Flag (output): Open-collector output. Active low indicates an output fault condition, if no used, leave open.
Feed Back (adjustable)	Adjustment input: Feedback input. Connect to resistive voltage-divider network.

Absolute Maximum Rating (Note 1)

Supply Voltage	V_{IN}	-20V ~ +20	V
Enable Voltage	V_{EN}	+20	V
Storage Temperature Range	T_{STG}	-65 ~ +150	°C
Lead Soldering Temperature (260 °C)		5	S
ESD		(Note 3)	

Operating Rating (Note 2)

Operation Input Voltage	V_{IN} (operate)	+2.25 ~ +16	V
Operation Enable Voltage	V_{EN} (operate)	+2.25 ~ +16	V
Power Dissipation (Note 4)	P_D	Internally Limited	W
Operating Junction Temperature Range	T_J	-40 ~ +125	°C

Electrical Characteristics

$V_{IN} = V_{OUT} + 1V$, $V_{IN} = 2.5V$ for fixed reference output voltage, $V_{enable} = 2.25V$, $T_a = 25^\circ C$, unless otherwise specified.

Parameter	Conditions	Min	Typ	Max	Unit
Fixed Output Voltage	$10mA \leq I_L \leq 1.5A$, $V_O + 1V \leq V_{IN} \leq 8V$	$0.980 V_O $	V_{OUT}	$1.020 V_O $	V
Fixed Reference Output Voltage	$I_L = 10mA$	$0.980 V_O $	1.24 (V_{REF})	$1.020 V_O $	V
Fixed Reference Output Voltage	$10mA \leq I_L \leq 1.5A$, $2.5V \leq V_{IN} \leq 8V$	$0.970 V_O $		$1.030 V_O $	
Line Regulation	$I_L = 10mA$, $V_O + 1V \leq V_{IN} \leq 16V$	--	0.05	0.5	%
Load Regulation	$V_{IN} = V_{out} + 1V$, $10mA \leq I_L \leq 1.5A$	--	0.2	1.0	%
Output Voltage Temp. Coefficient		--	40	100	ppm/°C
Dropout Voltage (Note 5)	$\Delta V_{OUT} = -1\%$	$I_L = 100mA$	--	100	mV
		$I_L = 750mA$	--	275	
		$I_L = 1.5A$	--	400	
Quiescent Current (Note 6)	$V_{IN} = V_{OUT} + 1V$	$I_L = 100mA$	--	5	mA
		$I_L = 750mA$	--	7	
		$I_L = 1.5A$	--	25	
Current Limited	$V_{OUT} = 0$, $V_{IN} = V_{OUT} + 1V$	--	2.0	2.5	A

Reference (TS39152)

Reference Voltage		$0.980 V_O $	1.24	$1.020 V_O $	V
Reference Voltage	(Note 7)	$0.970 V_O $		$1.030 V_O $	
Adjust Pin Bias Current		--	40	120	nA
Reference Voltage Temp. Coefficient	(Note 8)	--	20	--	ppm/°C
Adjust Pin Bias Current Temp. Coefficient		--	0.1	--	nA/°C

Enable Input (TS39151/2/3)

Input Logic Voltage	Low (OFF)	--	--	0.8	V
	High (ON)	2.25	--	--	
Enable Pin Input Current	$V_{EN} = 2.25V$	--	--	75	uA
	$V_{EN} = 0.8V$	--	--	4	

Electrical Characteristics (Continue)

Flag Output (TS39151/3)

Output Leakage Current	$V_{OH}=16V$	--	--	2	uA
Output Low Voltage (Note 9)	$V_{IN}=0.9 * V_{OUT}, I_{OL}=250uA$	--	--	400	mV
Upper Threshold Voltage	% of V_{OUT}	--	--	99	%
Lower Threshold Voltage	% of V_{OUT}	93	--	--	%
Hysteresis		--	1	--	%

Thermal Performance

Condition	Package type	Typ	Unit
Thermal Resistance	TO-220 / TO-220-5L	80	°C/W
Junction to Case	TO-263 / TO-263-5L	80	

Note 1: Absolute Maximum Rating is limits beyond which damage to the device may occur. For guaranteed specifications and test conditions see the Electrical Characteristics.

Note 2: The device is not guaranteed to operate outside its operating rating.

Note 3: Devices are ESD sensitive. Handling precautions recommended.

Note 4: The maximum allowable power dissipation is a function of the maximum junction temperature, T_J , the junction to ambient thermal resistance, Θ_{JA} , and the ambient temperature, T_a . Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. The effective value of Θ_{JA} can be reduced by using a heat sink, $Pd_{(max)} = (T_{J(max)} - T_a) / \Theta_{JA}$.

Note 5: Dropout voltage is defined as the input to output differential at which the output voltage drops -1% below its nominal value measured at 1V differential.

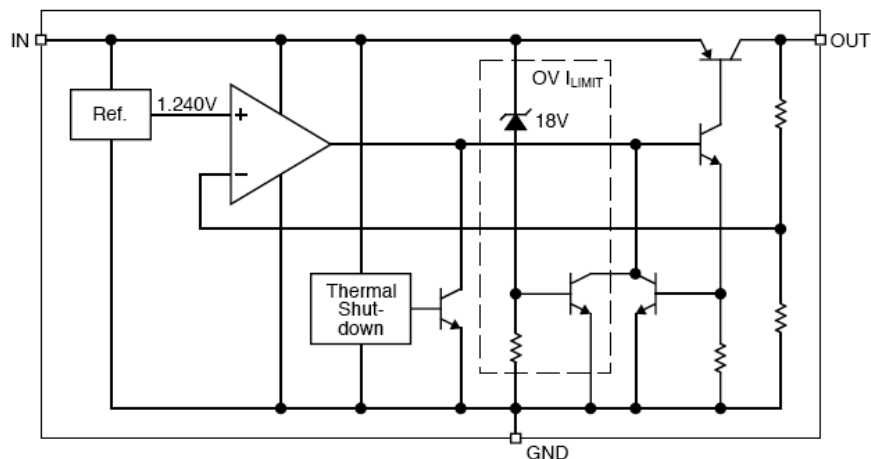
Note 6: Ground pin current is the regulator quiescent current. The total current drawn from the source is the sum of the ground pin current and output load current, $I_{GND} = I_{IN} - I_{OUT}$ for fix output voltage, , $I_{GND} = I_{IN} - I_{OUT} + 10mA$ for fix reference output voltage in full load regulation.

Note 7: $V_{REF} \leq V_{OUT} \leq (V_{IN} - 1V)$, $2.25V \leq V_{IN} \leq 16V$, $10mA \leq I_L \leq 1.0A$.

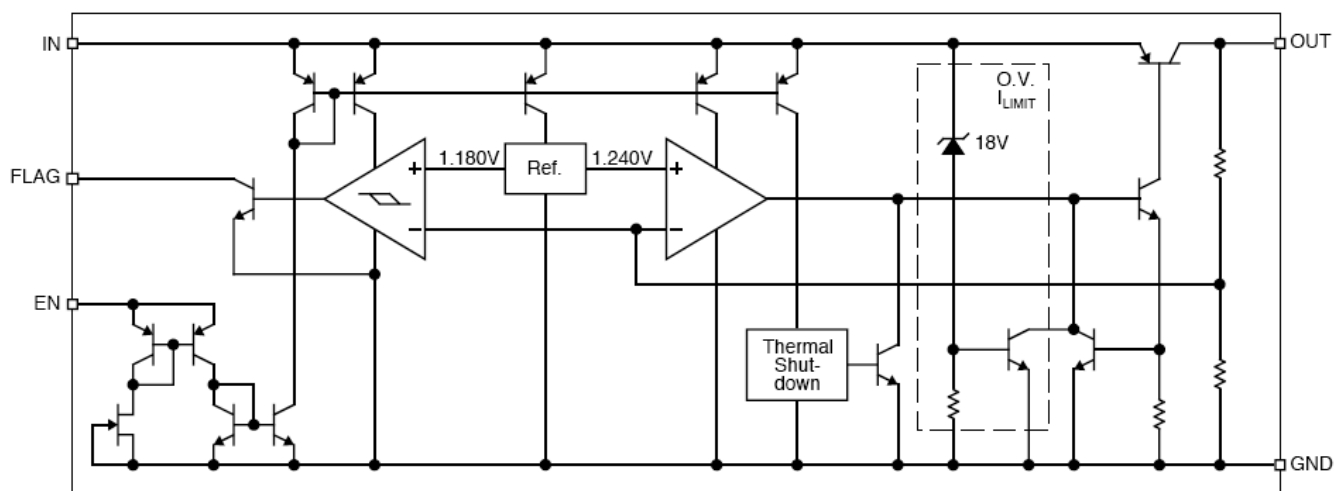
Note 8: Output voltage temperature coefficient is ΔV_{OUT} (worse cast) / $(T_{J(max)} - T_{J(MIN)})$ where is $T_{J(max)} +125^{\circ}C$ and $T_{J(MIN)}$ is $0^{\circ}C$.

Note 9: For adjustable device and fixed device with $V_{OUT} > 2.25V$.

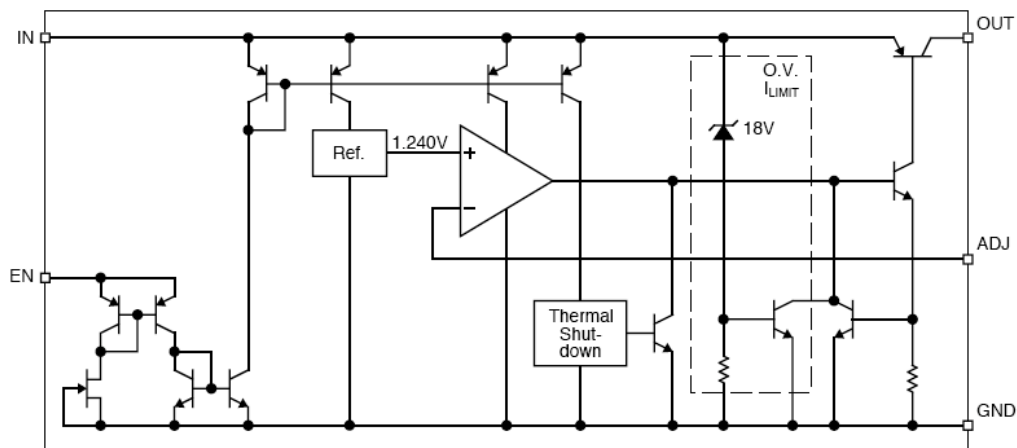
Functional Diagram



TS39150 Fixed Regulator Block Diagram



TS39151/3 Fixed Regulator with Flag, Sense and Enable Block Diagram



TS39152 Adjustable Regulator Block Diagram

Application Information

The TS3915x series are high performance with low dropout voltage regulator suitable for moderate to high current and voltage regulator application. Its 630mV dropout voltage at full load and over temperature makes it especially valuable in battery power systems and as high efficiency noise filters in post regulator applications. Unlike normal NPN transistor design, where the base to emitter voltage drop and collector to emitter saturation voltage limit the minimum dropout voltage, dropout performance of the PNP output of these devices is limited only by low V_{CE} saturation voltage. The TS3915x series is fully protected from damage due to fault conditions. Linear current limiting is provided. Output current during overload conditions is constant. Thermal shutdown the device when the die temperature exceeds the maximum safe operating temperature. Transient protection allows device survival even when the input voltage spikes above and below nominal. The output structure of these regulators allows voltages in excess of the desired output voltage to be applied without reverse current flow.

Output Capacitor Requirement

The TS3915x series requires an output capacitor to maintain stability and improve transient response is necessary. The value of this capacitor is dependent upon the output current, lower currents allow smaller capacitors. TS3915x series output capacitor selection is dependent upon the ESR of the output capacitor to maintain stability. When the output capacitor is 10uF or greater, the output capacitor should have an ESR less than 2Ω . This will improve transient response as well as promote stability. Ultra low ESR capacitors ($<100m\Omega$), such as ceramic chip capacitors, may promote instability. These very low ESR levels may cause an oscillation and/or under damped transient response. A low ESR solid tantalum capacitor works extremely well and provides good transient response and stability over temperature aluminum electrolytes can also be used, as long as the ESR of the capacitor is $<2\Omega$. The value of the output capacitor can be increased without limit. Higher capacitance values help to improve transient response and ripple rejection and reduce output noise.

Input Capacitor Requirement

An input capacitor of 1uF or greater is recommended when the device is more than 4" away from the bulk AC supply capacitance or when the supply is a battery. Small, surface mount, ceramic chip capacitors can be used for bypassing. Larger values will help to improve ripple rejection by bypassing the input to the regulator, further improving the integrity of the output voltage.

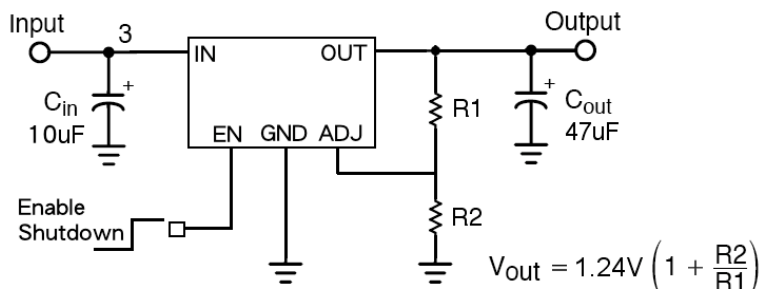
Minimum Load Current

The TS3915x series is specified between finite loads. If the output current is too small leakage currents dominate and the output voltage rises. A 10mA minimum load current is necessary for proper regulation.

Adjustable Regulator Design

The adjustable regulator versions (TS39152) is allow to programming the output voltage anywhere between 1.25 and the 16V maximum operating rating of the family.

Two resistors are used. Resistors can be quite large up to $1M\Omega$, because of the very high input impedance and low bias current of the sense comparator, the resistor values are calculated by:



Where V_{OUT} is the desired output voltage. Above application circuit shows component definition. Applications with widely varying load currents may scale the resistors to draw the minimum load current required for proper operation.

Application Information (Continue)

Enable Input

TS39151/2/3 versions feature an active-high enable (EN) input that allows ON/OFF control of the regulator. Current drain reduces to “zero” when the device is shutdown, with only micro-amperes of leakage current. The EN input has TTL/CMOS compatible thresholds for simple interfacing with logic interfacing. EN may be directly tied to V_{IN} and pulled up to the maximum supply voltage.

Error Flag

TS39151 versions feature an Error Flag, which looks at the output voltage and signals an error condition when this voltage drops 5% below its expected value. The error flag is an open-collector output that pulls low under fault conditions. It may sink 10mA. Low output voltage signifies a number of possible problems, including an over-current fault (the device is in current limit) or low input voltage. The flag output is inoperative during over temperature shutdown conditions. A pull-up resistor from error flag to either V_{IN} or V_{OUT} is required for proper operation. For information regarding the minimum and maximum values of pull-up resistance, refer the graph as follow:

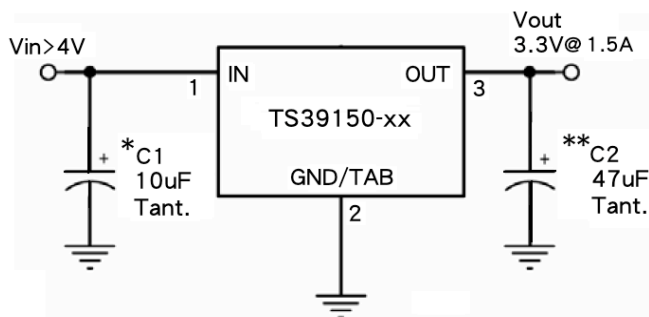
Transient Response and 3.3V to 2.5V or 2.5V to 1.8V Conversion

TS3915x has excellent transient response to variations in input voltage and load current. The device have been designed to respond quickly to load current variations and input voltage variations. Large output capacitors are not required to obtain this performance. A standard 10uF output capacitor, preferably tantalum, is all that is required. Larger values help to improve performance even further. By virtue of its low dropout voltage, this device does not saturate into dropout as readily as similar NPN base designs. When converting from 3.3V to 2.5V or 2.5V to 1.8V, the NPN based regulators are already operating in dropout, with typical dropout requirements of 1.2V or greater,. To convert down to 2.5V or 1.8V without operating in dropout, NPN based regulators require an input voltage of 3.7V at the very least. The TS3915x regulator will provide excellent performance with an input as low as 3.0V or 2.5V respectively. This gives the PNP based regulators a distinct advantage over older, NPN based linear regulators.

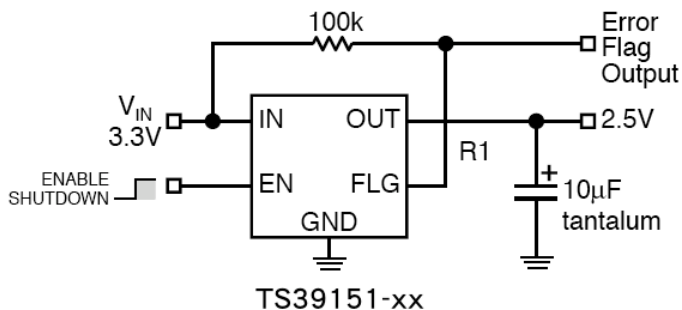
Output Sense

The fixed voltage regulator versions (TS39153) is connected to the output pin of the regulator. In critical applications, small voltage drops are caused by the resistance (R_p) of PC traces between the regulator and the load. These may be eliminated by connecting the sense pin to the output at the load as follow:
(Kelvin Sense Connection).

Typical Application Circuit



2.5V/1.5A Regulator



2.5V/1.5A Regulator with Error Flag

Application Information (Continue)**Thermal Characteristics**

TS3915x series linear regulators are simple to use, the most complicated design parameters to consider are thermal characteristics, thermal design requires the following application specification parameters:

- Maximum ambient temperature, T_A
- Output current, I_{OUT}
- Output voltage, V_{OUT}
- Input voltage, V_{IN}

Calculate the power dissipation of the regulator from these numbers and the device parameters from this datasheet, where the ground current is taken from data sheet.

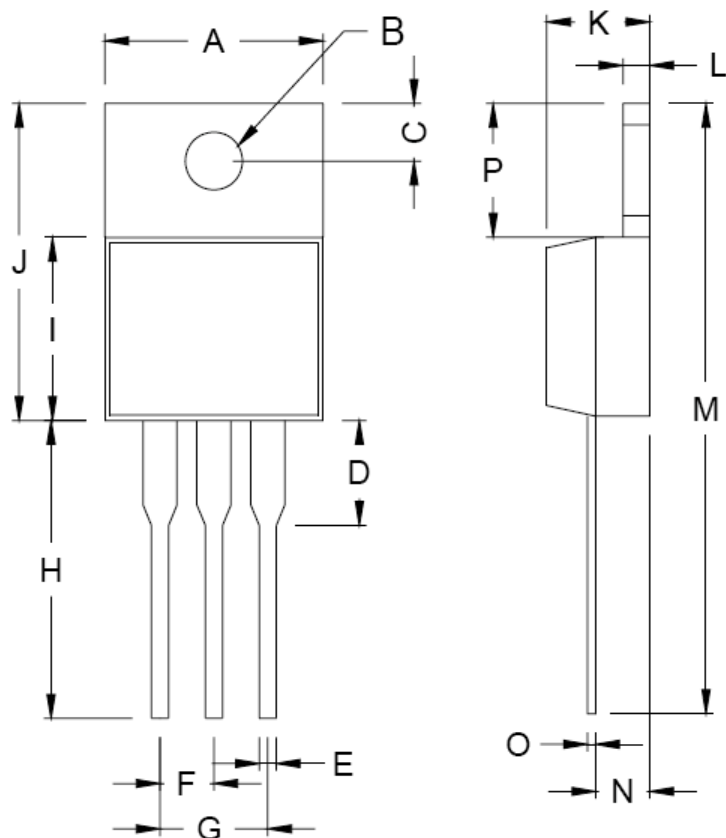
$$P_D = (V_{IN} - V_{OUT}) \times I_{OUT} + V_{IN} \times I_{GND}$$

The heat sink thermal resistance is determined by:

$$\Theta_{SA} = (T_{J(MAX)} - T_A) / P_D - (\Theta_{JC} + \Theta_{CS})$$

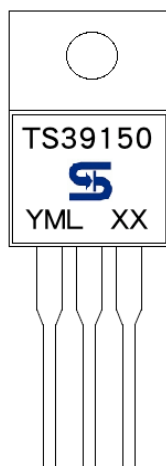
Where $T_{J(MAX)} \leq 125^\circ\text{C}$ and Θ_{CS} is between 0°C/W and 2°C/W . The heat sink may be significantly reduced in applications where the minimum input voltage is known and is large compared with the dropout voltage and distribute the heat between this resistor and the regulator. The low dropout properties of vertical PNP regulators allow significant reductions in regulator power dissipation and the associated heat sink without compromising performance. When this technique is employed, a capacitor of at least 1uF is needed directly between the input and regulator ground.

TO-220 Mechanical Drawing



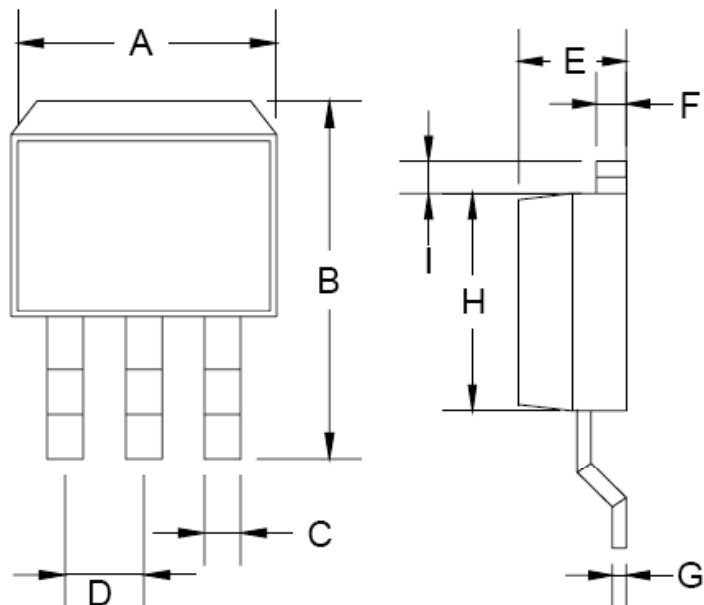
TO-220 DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	10.000	10.500	0.394	0.413
B	3.740	3.910	0.147	0.154
C	2.440	2.940	0.096	0.116
D	-	6.350	-	0.250
E	0.381	1.106	0.015	0.044
F	2.345	2.715	0.092	0.107
G	4.690	5.430	0.185	0.214
H	12.700	14.732	0.500	0.580
I	8.382	9.017	0.330	0.355
J	14.224	16.510	0.560	0.650
K	3.556	4.826	0.140	0.190
L	0.508	1.397	0.020	0.055
M	27.700	29.620	1.091	1.166
N	2.032	2.921	0.080	0.115
O	0.255	0.610	0.010	0.024
P	5.842	6.858	0.230	0.270

Marking Diagram



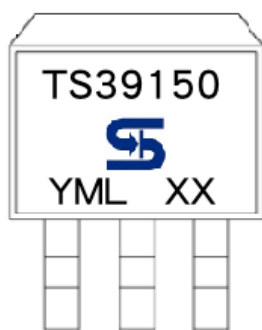
- XX** = Output Voltage
(**18**=1.8V, **25**=2.5V, **33**=3.3V, **50**=5V)
- Y** = Year Code
- M** = Month Code
(**A**=Jan, **B**=Feb, **C**=Mar, **D**=Apr, **E**=May, **F**=Jun, **G**=Jul, **H**=Aug, **I**=Sep, **J**=Oct, **K**=Nov, **L**=Dec)
- L** = Lot Code

TO-263 Mechanical Drawing



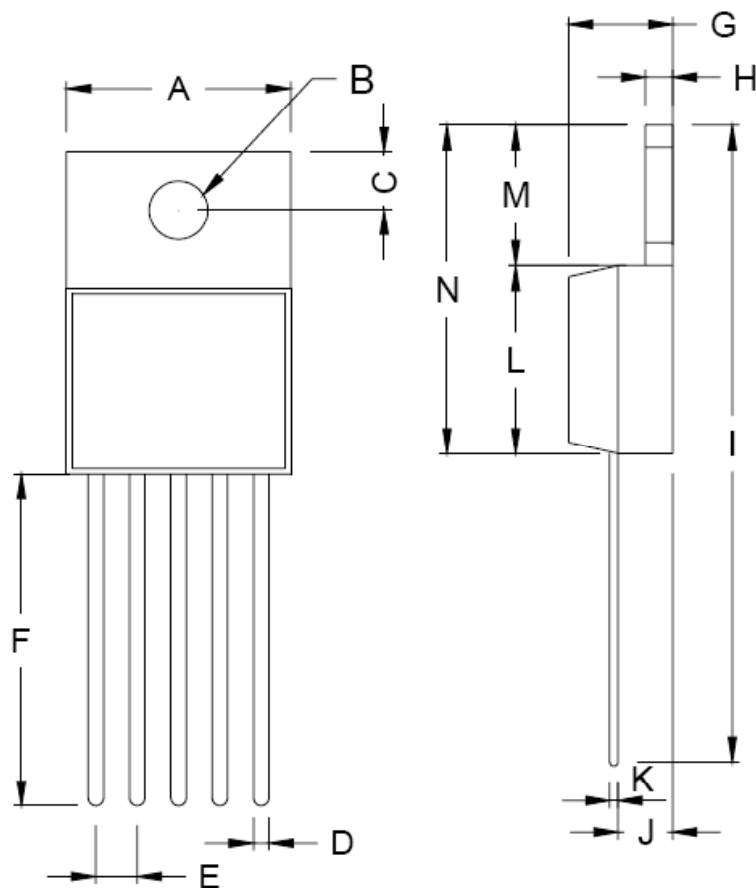
DIM	TO-263 DIMENSION			
	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	10.000	10.500	0.394	0.413
B	14.605	15.875	0.575	0.625
C	0.508	0.991	0.020	0.039
D	2.420	2.660	0.095	0.105
E	4.064	4.830	0.160	0.190
F	1.118	1.400	0.045	0.055
G	0.450	0.730	0.018	0.029
H	8.280	8.800	0.325	0.346
I	1.140	1.400	0.044	0.055
J	1.480	1.520	0.058	0.060

Marking Diagram



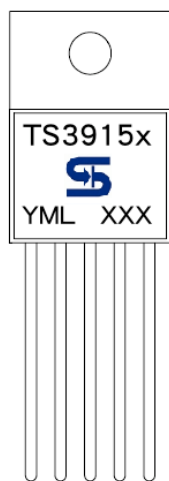
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(18=1.8V, 25=2.5V, 33=3.3V, 50=5V)
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- L** = Lot Code

TO-220-5L Mechanical Drawing



DIM	TO-220-5L DIMENSION			
	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	10.00	10.50	0.394	0.413
B	3.240	4.440	0.128	0.175
C	2.440	2.940	0.096	0.116
D	0.260	1.020	0.010	0.040
E	1.570	1.830	0.062	0.072
F	13.31	14.13	0.524	0.556
G	4.475	5.225	0.176	0.206
H	1.170	1.370	0.046	0.054
I	27.60	29.44	1.087	1.159
J	2.175	2.925	0.086	0.115
K	0.297	0.477	0.012	0.019
L	8.280	8.800	0.326	0.346
M	6.010	6.510	0.237	0.256
N	14.29	15.31	0.563	0.603

Marking Diagram



TS39151 / TS39152 / TS39153

Y = Year Code

M = Month Code

(**A**=Jan, **B**=Feb, **C**=Mar, **D**=Apr, **E**=May, **F**=Jun, **G**=Jul, **H**=Aug, **I**=Sep, **J**=Oct, **K**=Nov, **L**=Dec)

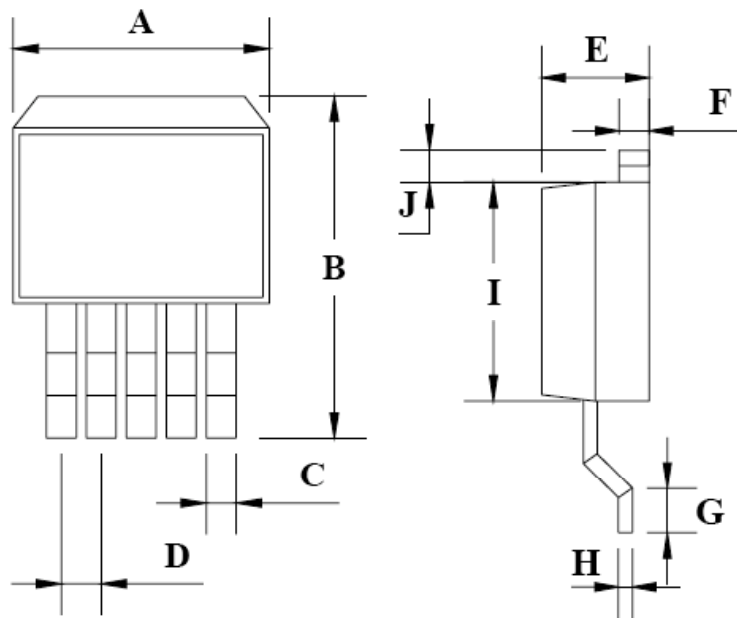
L = Lot Code

XX = Output Voltage – TS39151 & TS39153 only

(**18**=1.8V, **25**=2.5V, **33**=3.3V, **50**=5V)

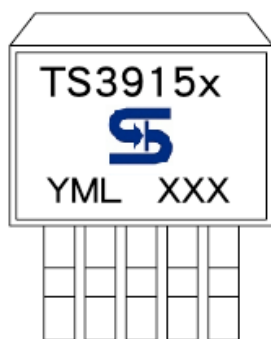
XXX = CZ5 – TS39152 only

TO-263-5L Mechanical Drawing



TO-263-5L DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	10.220	10.260	0.402	0.404
B	14.600	15.870	0.575	0.625
C	0.750	0.770	0.030	0.030
D	1.573	1.827	0.062	0.072
E	4.560	4.570	0.179	0.180
F	1.240	1.270	0.049	0.050
G	2.280	2.790	0.090	0.110
H	0.280	0.320	0.011	0.013
I	8.240	8.280	0.324	0.326
J	1.540	1.800	0.060	0.071

Marking Diagram



TS39151 / TS39152 / TS39153

Y = Year Code

M = Month Code

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L = Lot Code

XX = Output Voltage – TS39151 & TS39153 only

(**18**=1.8V, **25**=2.5V, **33**=3.3V, **50**=5V)

XXX = CM5 – TS39152 only

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