TRSL-7380CG / TRSL-7380G / TRSL-7380ACG / TRSL-7380AG
3.3V / 1550 nm / 1.25 Gbps RoHS Compliant SFF LC SINGLE-MODE TRANSCEIVER

FEATURES

- Duplex LC Single Mode Transceiver
- IEEE 802.3z 1000BASE-ZX Compliant
- Fiber Channel 1X SM-LC-L FC-PI Compliant
- Small Form Factor, RJ-45 size, 2X5 pin Package
- 1550 nm DFB LD Transmitter
- 24 dB Power Budget at Least
- AC/AC Coupled Signal Input / Output
- LVTTL Transmitter Disable Input
- LVTTL Signal Detect Output :TRSL-7380CG
- LVPECL Signal Detect Output :TRSL-7380G
- Single +3.3 V Power Supply
- 0 to 70°C Operating : TRSL-7380CG
- -20 to 85°C Operating : TRSL-7380ACG
- Wave Solderable and Aqueous Washable
- Class 1 Laser International Safety Standard IEC-60825 Compliant

APPLICATIONS

- Gigabit Ethernet Switches and Routers
- Fiber Channel Switch Infrastructure
- Metro Edge Switching

DESCRIPTION

The TRSL-7380CG series single mode transceivers is small form factor, low power, high performance module for bi-directional serial optical data communications such as IEEE 802.3z Gigabit Ethernet 1000BASE-ZX and Fiber Channel 1X SM-LC-L FC-PI. This module is designed for single mode fiber and operates at a nominal wavelength of 1550 nm. A guaranteed minimum optical link budget of 24 dB is offered which can correspond to a link distance of over 80 km (assuming worst case fiber loss of 0.25 dB/km). The transmitter section uses a multiple quantum well DFB laser and is a class 1 laser compliant according to International Safety Standard IEC-60825. The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC. A LVPECL logic interface simplifies interface to external circuitry.

LASER SAFETY

This single mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

ORDER INFORMATION

P/No.	Bit Rate (Gb/s)	1000 BASE	Distance (km)	Wavelength (nm)	Package	Temp. (°C)	TX Power (dBm)	RX Sens. (dBm)	RoHS Cpmpliant
TRSL-7380CG	1.25/1.063	ZX	80	1550 DFB	2X5 LC	0 to 70	5 to 0	-24	Yes
TRSL-7380ACG	1.25/1.063	ZX	80	1550 DFB	2X5 LC	-20 to 85	5 to 0	-24	Yes

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	Tstg	-40	85	°C	
Operating Temperature	Topr	0 -20	70 85	°C	TRSL-7380CG TRSL-7380ACG
Soldering Temperature	---		260	°C	10 seconds on leads only
Power Supply Voltage	Vcc	0	4.5	V	
Input Voltage	---	GND	Vcc	V	
Output Current	Iout	0	30	mA	

Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Units / Notes
Power Supply Voltage	Vcc	3.13	3.3	3.47	V
Operating Temperature	Topr	0 -20		70 85	°C / TRSL-7380CG °C / TRSL-7380ACG air flow 1m/sec
Data Rate		1000	1250		Mb/s
Power Supply Current	Icc		200	280	mA

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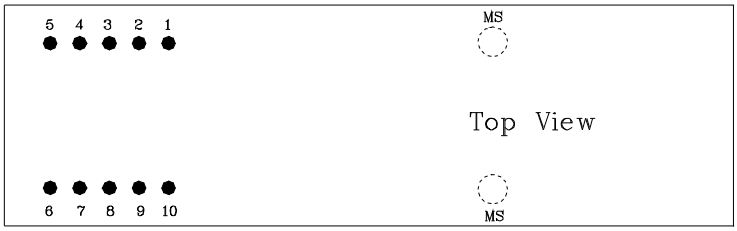
Transmitter Specifications (0^oC < Topr < 70^oC, 3.13V < Vcc < 3. 47V)						
Parameter	**Symbol**	**Min**	**Typ**	**Max**	**Units**	**Notes**
Optical						
Optical Transmit Power	Po	0	---	5	dBm	1
Output Center Wavelength	λ	1520		1580	nm	
Output Spectrum Width	Δλ	---	---	1	nm	-20 dB Width
Side Mode Suppression Ratio	SMSR	30			dB	
Extinction Ratio	E_R	9	---	---	dB	
Output Eye	Compliant with IEEE 802.3z					
Optical Rise Time	t_r			0.26	ns	20% to 80% Values
Optical Fall Time	t_f			0.26	ns	20% to 80% Values
Relative Intensity Noise	RIN			-120	dB/Hz	
Total Jitter	TJ			0.227	ns	2
Electrical						
Data Input Current – Low	I_{IL}	-350			μA	
Data Input Current – High	I_{IH}			350	μA	
Differential Input Voltage	V_{IH} - V_{IL}	300			mV	
Data Input Voltage – Low	V_{IL} - V_{CC}	-2.0		-1.58	V	3
Data Input Voltage -- High	V_{IH} - V_{CC}	-1.1		-0.74	V	3
Disable Input Voltage -- Low	$V_{TDIS,L}$	0		0.5	V	TX Output Enabled
Disable Input Voltage -- High	$V_{TDIS,H}$	Vcc – 1.3		Vcc	V	TX Ouput Disabled
Shut Off Time for TxDis	t_{DIS}			1	ms	

Notes: 1. Output power is power coupled into a 9/125 μm single mode fiber.
2. Measured with a 2^7-1 PRBS.
3. These inputs are compatible with 10K, 10KH and 100K ECL and PECL inputs.

Receiver Specifications (0^oC < Topr < 70^oC, 3.13V < Vcc < 3. 47V)						
Parameter	**Symbol**	**Min**	**Typ**	**Max**	**Units**	**Notes**
Optical						
Sensitivity	---	---	---	-24	dBm	1
Maximum Input Power	Pin	-3		---	dBm	
Signal Detect -- Asserted	Pa	---	---	-24	dBm	Transition: low to high
Signal Detect -- Deasserted	Pd	-36	---	---	dBm	Transition: high to low
Signal detect -- Hysteresis		1.0	---		dB	
Wavelength of Operation		1100	---	1600	nm	
Electrical						
Data Output Voltage – Low	V_{OL} - V_{CC}	-2.0		-1.58	V	2
Data Output Voltage – High	V_{OH} - V_{CC}	-1.1		-0.74	V	2
Signal Detect Output Voltage -- Low	V_{OL}			0.5	V	TRSL-7380CG
Signal Detect Output Voltage -- High	V_{OH}	2.0			V	
Signal Detect Output Voltage -- Low	V_{OL} - V_{CC}	-2.0		-1.58	V	TRSL-7380G
Signal Detect Output Voltage -- High	V_{OH} - V_{CC}	-1.1		-0.74	V	

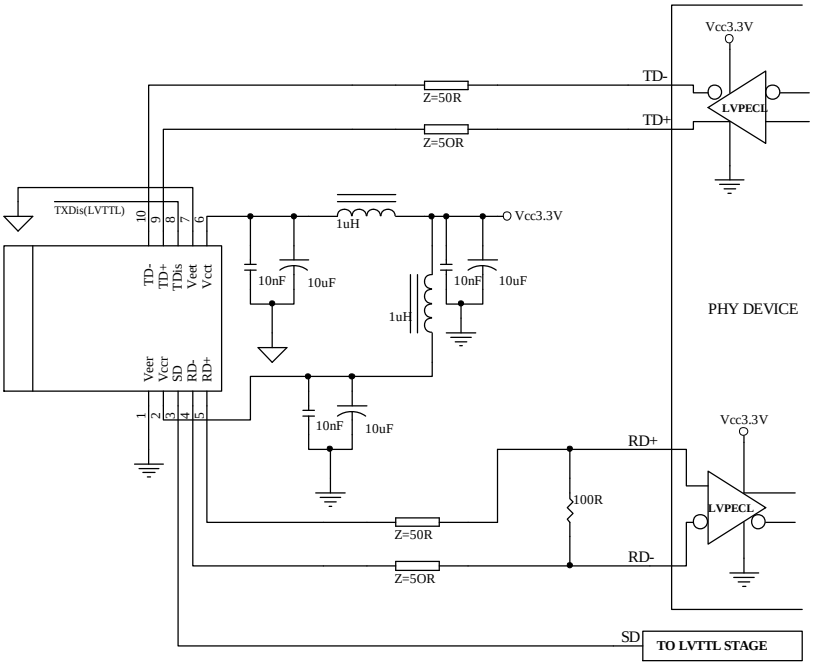
Notes: 1. Minimum sensitivity and saturation levels at BER=1E-12 for a 2^7-1 PRBS.
2. These outputs are compatible with 10K, 10KH and 100K ECL and PECL outputs.

CONNECTION DIAGRAM



PIN	Symbol	Notes
1	V_{EE}r	Directly connect this pin to the receiver ground plane
2	V_{CC} r	+3.3V dc power for the receiver section
3	SD	Active high on this indicates a received optical signal.
4	RD–	Receiver Dataout Bar. See recommended circuit schematic
5	RD+	Receiver Dataout. See recommended circuit schematic
6	V_{CC} t	+3.3V dc power for the transmitter section
7	V_{EE}t	Directly connect this plan to the transmitter ground plane
8	TDis	Transmitter Disable. Connect this pin to +3.3V TTL logic "1" to disable module To enable module connect to TTL logic low "0"
9	TD+	Transmitter Data In. See recommended circuit schematic
10	TD-	Transmitter Data In Bar. See recommended circuit schematic
MS	MS	Mounting Studs. Connect to Chassis Ground

RECOMMENDED CIRCUIT SCHEMATIC



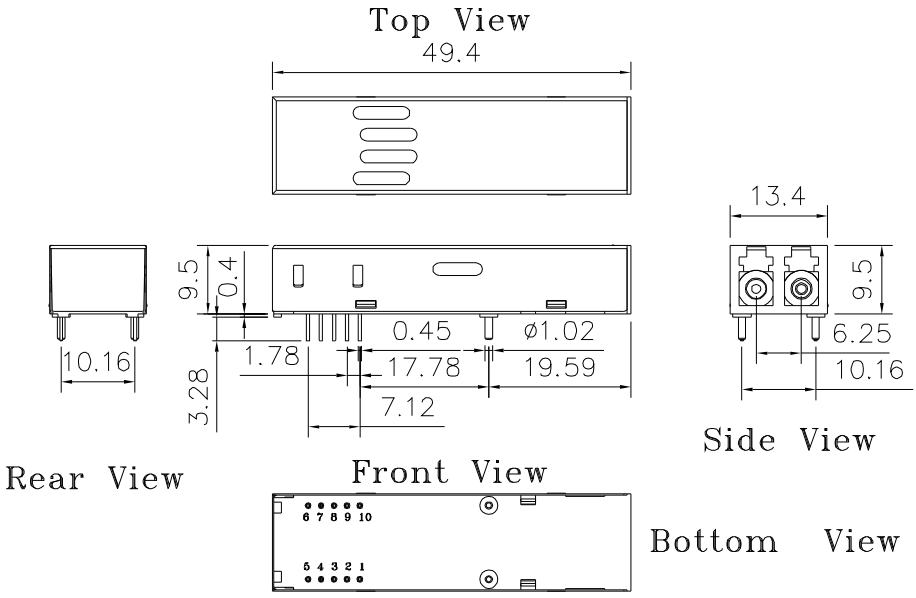
Note: 1. TX input is terminated inside the module.

2. 1000 Ω SD Output pull-down resistor required for TRSL-7XX0G / TRSL-7XX0AG (LVPECL SD Output).
3. Veer and Veet are not internally connected to each other.
4. 50 Ω line pattern and component placements on TD+/TD- and RD+/RD- lines shall be symmetrical for better impedance matching.

PACKAGE DIAGRAM

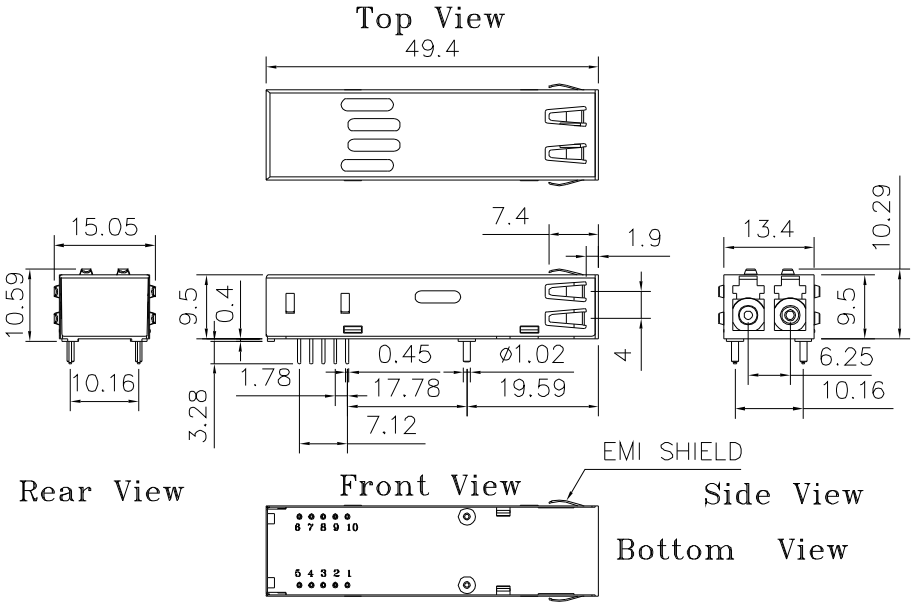
Units in mm

1) Standard Case



TRSL-7380G / TRSL-7380CG / TRSL-7380AG / TRSL-7380ACG

2) Extended Case



TRSL-7380EG / TRSL-7380CEG / TRSL-7380AEG / TRSL-7380ACEG

Note: Specifications subject to change without notice.