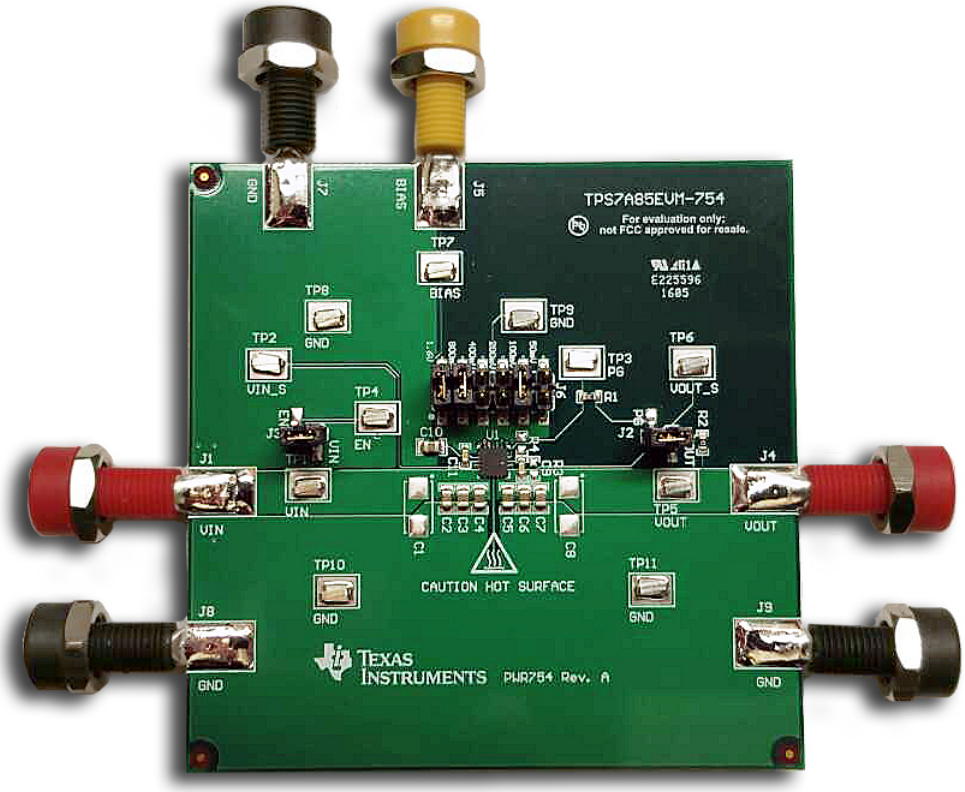
TPS7A85EVM-754 Evaluation Module



This user's guide describes the operational use of the TPS7A85EVM-754 evaluation module (EVM) as a reference design for engineering demonstration and evaluation of the TPS7A8500RGR, low-dropout linear regulator (LDO). Included in this user's guide are setup and operating instructions, thermal and layout guidelines, a printed circuit board (PCB) layout, a schematic diagram, and a bill of materials (BOM).

Throughout this document, the terms *demonstration kit*, *evaluation board*, and *evaluation module* are synonymous with the TPS7A85EVM-754.

The following related documents are available through the Texas Instruments web site at www.ti.com.

Related Documentation

Device	Literature Number
TPS7A85	SBVS267

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1 Introduction

The Texas Instruments' TPS7A85EVM-754 EVM helps design engineers evaluate the operation and performance of the TPS7A85 family of linear regulators for possible use in their own circuit application. This particular EVM configuration contains a single low-noise, high-PSRR linear regulator for high-speed communication systems. The regulator is capable of delivering up to 4 A to the load with ultralow VIN to VOUT dropout voltage. For stability, use a 47-µF (or larger) output capacitor for the TPS7A85.

1.1 Before You Begin

The following warnings and cautions are noted for the safety of anyone using or working close to the TPS7A85EVM-754. Observe all safety precautions.



Warning Warning Hot surface. Contact may cause burns. Do not touch.

CAUTION

The circuit module may be damaged by overtemperature. To avoid damage, monitor the temperature during evaluation and provide cooling, as needed, for your system environment.

CAUTION

Some power supplies can be damaged by application of external voltages. If using more than one power supply, check your equipment requirements and use blocking diodes or other isolation techniques, as needed, to prevent damage to your equipment.

CAUTION

The circuit module is not a finished product or electrical appliance. The module does not contain current or voltage thresholds for circuit protection. It must be used by qualified personnel with additional equipment for evaluation only.

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2 EVM Setup

This section describes how to properly connect and setup the TPS7A85EVM-754, including the jumpers and connectors on the EVM board.

2.1 Input/Output Connectors and Jumper Descriptions

2.1.1 J1 – VIN

Input power-supply voltage connector. Twist together the positive input lead and ground return lead from the input power supply, and keep them as short as possible to minimize input inductance. Add additional bulk capacitance between the input supply and ground (use the C1 footprint) if the supply leads are greater than six inches. For example, an additional 47-µF electrolytic capacitor connected from the input supply (J1) to ground can improve the transient response of the TPS7A85 and eliminates unwanted ringing on the input because of long wire connections.

2.1.2 J2 – PG

Pullup-voltage selector for PG. This EVM is designed so that PG can be pulled up either to VOUT by shorting J2, or pulled up to another voltage by applying an external voltage to the PG post.

2.1.3 J3 – EN

Output enable. To enable the output, connect a jumper to short VIN to EN.

2.1.4 J4 – VOUT

Regulated output voltage connector.

2.1.5 J5 – VBIAS

If the input supply (VIN) voltage is less than 1.4 V but greater than 1.1 V, use a VBIAS voltage of 3.0 V to 6.5 V to provide power to the TPS7A85. If the input voltage is greater than 1.4 V, the VBIAS pin does not have to be connected. The VBIAS supply pin typically consumes 2.3 mA.

2.1.6 J6 – AnyOut

The output voltage of the TPS7A85 is selectable in accordance with the names given to the output voltage setting pins: 50 mV, 100 mV, 200 mV, 400 mV, 800 mV, and 1.6 V. For each pin connected to the ground, the output voltage setting increases by the value associated with that pin name, starting from the value of the reference voltage of 0.8 V; floating the pins has no effect on the output voltage.

2.1.7 J7 – GND

Bias ground return connector.

2.1.8 J8 – GND

Input ground return connector.

2.1.9 J9 – GND

Output ground return connector.

2.1.10 TP1 – VIN

Input test point.

2.1.11 TP2 – VIN_S

Input sense test point.

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2.1.12 TP3 – PG

PG test point.

2.1.13 TP4 – EN

Enable test point.

2.1.14 TP5 – VOUT

Output test point.

2.1.15 TP6 – VOUT_S

Output sense test point.

2.1.16 TP7 – BIAS

Bias test point.

2.1.17 TP8 – GND

Ground test point.

2.1.18 TP9 – GND

Ground test point.

2.1.19 TP10 – GND

Ground test point.

2.1.20 TP11 – GND

Ground test point.

2.2 Soldering Guidelines

To avoid damaging the integrated circuit (IC), use a hot-air system for any solder rework to modify the EVM for the purpose of repair or other application reasons.

2.3 Equipment Connection

1. Set the input and bias power supplies to 6.5 V (max), and turn the power supplies off.
2. Connect the positive voltage lead from the input power supply to VIN at the J1 connector of the EVM.
3. Connect the ground lead from the input power supply to GND at the J2 connector of the EVM.
4. Connect a 0-A to 4-A load between OUT and GND. The connector used depends on the desired output current.
5. Disable the output by floating J3.

3 Operation

1. Turn on the power supplies.
2. Enable the output by jumping J3, the EN pin, to VIN.
3. Vary the respective load and input voltage as necessary for test purposes.

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4 PCB Layout

Figure 1 to Figure 5 illustrate the PCB layout for this EVM.

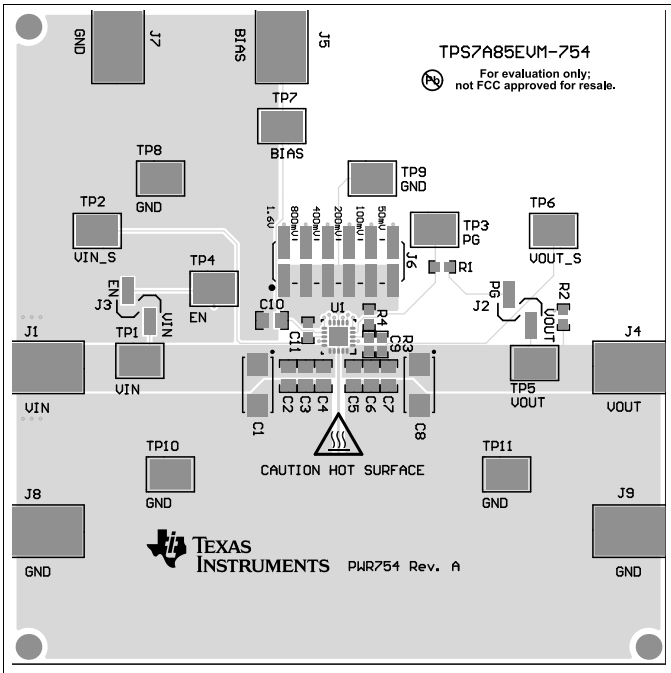

Figure 1. Assembly Layer

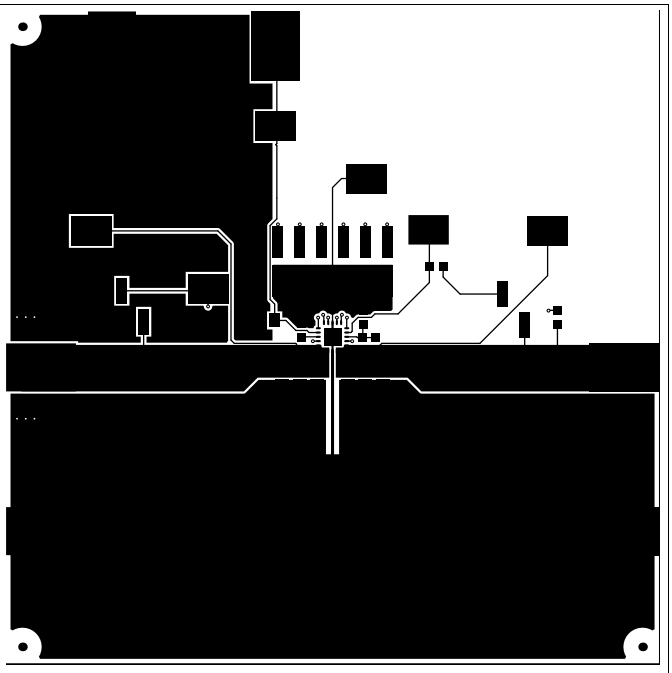

Figure 2. Top Layer Routing

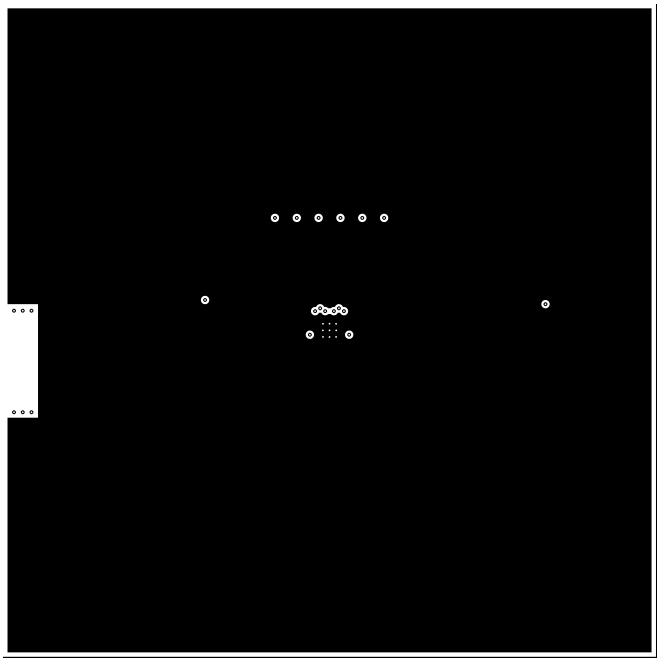

Figure 3. Signal Layer 1 Routing

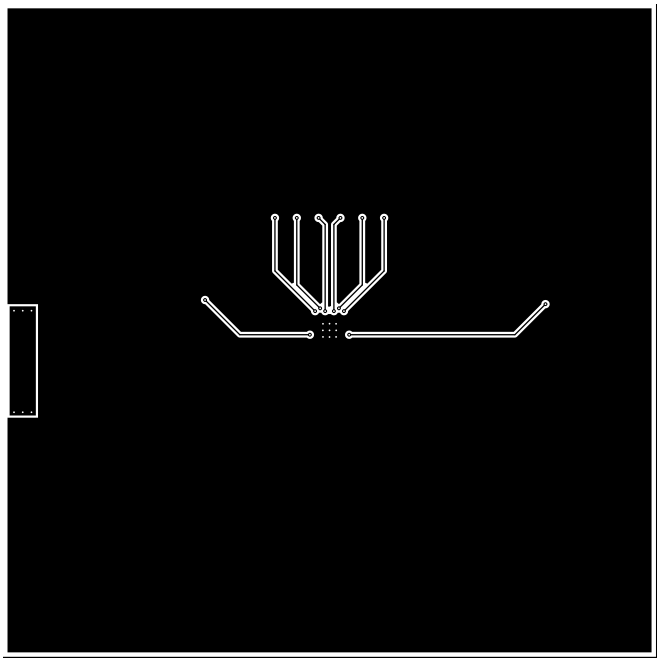

Figure 4. Signal Layer 2 Routing

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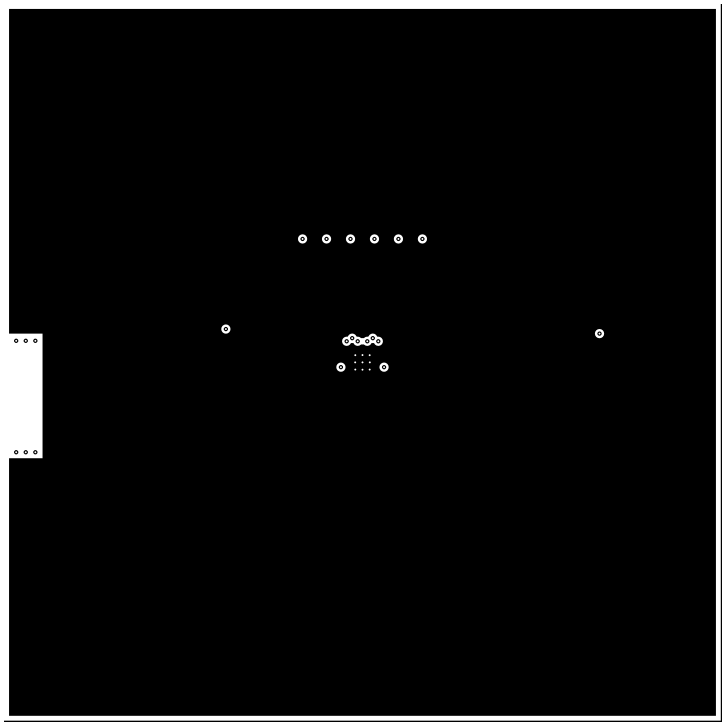

Figure 5. Bottom Layer Routing

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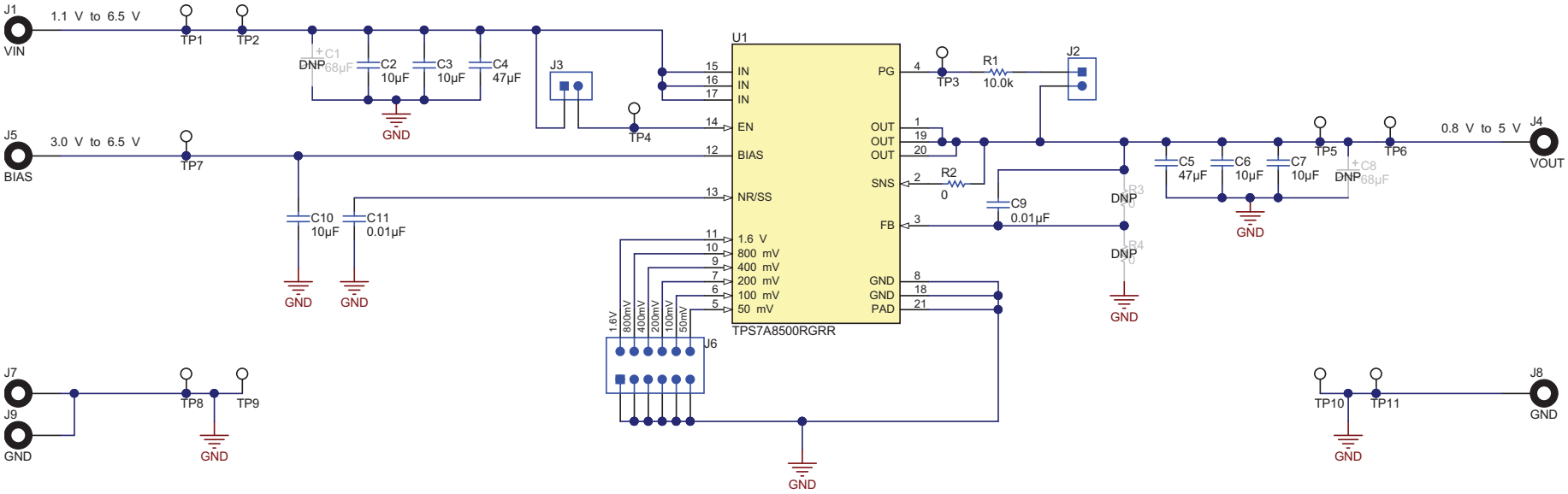
5 Schematic

Figure 6 is the schematic for this EVM.



NOTE: Capacitors C1 and C8 and resistors R3 and R4 do not come populated.

Figure 6. TPS7A85EVM-754 Schematic

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6 Bill of Materials

The BOM for this EVM is shown in Table 1.

Table 1. TPS7A85EVM-754 BOM(1)(2)(3)

Designator	Quantity	Value	Description	Package Reference	PartNumber	Manufacturer	Alternate PartNumber	Alternate Manufacturer
!PCB1	1		Printed Circuit Board		PWR754	Any	-	-
C2, C3, C6, C7, C10	5	10uF	CAP, CERM, 10 µF, 16 V, +/- 10%, X5R, 0805	0805	GRM21BR61C106KE15L	MuRata		
C4, C5	2	47uF	CAP, CERM, 47 µF, 4 V, +/- 20%, X5R, 0805	0805	GRM219R60G476ME44D	MuRata		
C9, C11	2	0.01uF	CAP, CERM, 0.01 µF, 50 V, +/- 10%, X7R, 0603	0603	GRM188R71H103KA01D	MuRata		
FID1-FID3	3		Fiducial mark. There is nothing to buy or mount.	Fiducial	N/A	N/A		
J1, J4	2		Standard Banana Jack, Insulated, Red	6091	6091	Keystone		
J2, J3	2		Header, 2.54 mm, 2x1, Gold, R/A, SMT	Header, 2.54 mm, 2x1, R/A, SMT	87898-0204	Molex		
J5	1		BANANA JACK, 15A, Insulated, Nylon,Yellow	940x438x438mil	108-0907-001	Emerson Network Power		
J6	1		Header, 100mil, 6x2, SMT	Header, 6x2, SMT	0015912120	Molex		
J7-J9	3		Standard Banana Jack, Insulated, Black	6092	6092	Keystone		
R1	1	10.0k	RES, 10.0 k, 1%, 0.1 W, 0603	0603	CRCW060310K0FKEA	Vishay-Dale		
R2	1	0	RES, 0, 5%, 0.1 W, 0603	0603	CRCW06030000Z0EA	Vishay-Dale		
SH-J1, SH-J2, SH-J3, SH-J4, SH-J5	5	1x2	Shunt, 100mil, Gold plated, Black	Shunt	969102-0000-DA	3M	SNT-100-BK-G	Samtec
TP1-TP11	11	SMT	Test Point, Compact, SMT	Testpoint_ Keystone_ Compact	5016	Keystone		
U1	1		4-A, 6-µVRMS, RF, LDO Voltage Regulator, RGR0020A	RGR0020A	TPS7A8500RGRR	Texas Instruments	TPS7A8500RGRT	Texas Instruments
C1, C8	0	68uF	CAP, TA, 68 µF, 16 V, +/-10%, 0.2 ohm, SMD	6032-28	TPSC686K016R0200	AVX		
R3, R4	0	0	RES, 0, 5%, 0.1 W, 0603	0603	CRCW06030000Z0EA	Vishay-Dale		

(1) These assemblies are ESD sensitive, observe ESD precautions.
(2) These assemblies must be clean and free from flux and all contaminants. Use of no-clean flux is not acceptable.
(3) These assemblies must comply with workmanship standards IPC-A-610 Class 2.

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3 *Regulatory Notices:*

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      3.1.1 *Notice applicable to EVMs not FCC-Approved:*

      This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

      3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

      CAUTION

      This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

      Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

      FCC Interference Statement for Class A EVM devices

      NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 *Canada*

3.2.1 *For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210*

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

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http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page

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If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required by Radio Law of Japan to follow the instructions below with respect to EVMs:

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

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4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

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