TPS563201EVM-715 3-A, SWIFT™ Regulator Evaluation Module

This user's guide contains information for the TPS563201 as well as support documentation for the TPS563201EVM-715 evaluation module. Included are the performance specifications, schematic, and the bill of materials of the TPS563201EVM-715.

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1 Introduction

The TPS563201 is a single, adaptive on-time, D-CAP2™ mode, synchronous buck converter requiring a very low external component count. The D-CAP2 control circuit is optimized for low-ESR output capacitors such as POSCAP, SP-CAP, or ceramic types and features fast transient response with no external compensation. The switching frequency is internally set at a nominal 580 kHz and enters plus skip mode in light load conditions. The high-side and low-side switching MOSFETs are incorporated inside the TPS563201 package along with the gate-drive circuitry. The low drain-to-source on resistance of the MOSFETs allows the TPS563201 to achieve high efficiencies and helps keep the junction temperature low at high output currents. The TPS563201 dc/dc synchronous converter is designed to provide up to a 3-A output from an input voltage source of 4.5 V to 17 V. The output voltage range is from 0.768V to 7 V. Rated input voltage and output current ranges for the evaluation module are given in Table 1.

The TPS563201EVM-715 evaluation module (EVM) is a single, synchronous buck converter providing 1.05 V at 3 A from 4.5-V to 17-V input. This user's guide describes the TPS563201EVM-715 performance.

Table 1. Input Voltage and Output Current Summary

EVM	Input Voltage (V_{IN}) Range	Output Current (I_{OUT}) Range
TPS563201EVM-715	4.5 V to 17 V	0 A to 3 A

2 Performance Specification Summary

A summary of the TPS563201EVM-715 performance specifications is provided in Table 2. Specifications are given for an input voltage of 12 V and an output voltage of 1.05 V, unless otherwise noted. The ambient temperature is 25°C for all measurement, unless otherwise noted.

Table 2. TPS563201EVM-715 Performance Specifications Summary

SPECIFICATIONS		TEST CONDITIONS	MIN	TYP	MAX	UNIT
V_{IN}	Input voltage		4.5	12	17	V
CH1	Output voltage			1.05		V
	Operating frequency	V_{IN} = 12 V, I_{OUT} = 3 A		580		kHz
	Output current range		0		3	A
	Overcurrent limit	V_{IN} = 12 V, L_{OUT} = 2.2 µH		4.2		A
	Output ripple voltage	V_{IN} = 12 V, I_{OUT} = 3 A		20		mV_{PP}

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3 Modifications

These evaluation modules are designed to provide access to the features of the TPS563201. Some modifications can be made to this module.

3.1 Output Voltage Setpoint

To change the output voltage of the EVMs, it is necessary to change the value of resistor R1. Changing the value of R1 can change the output voltage above 0.768 V. The value of R1 for a specific output voltage can be calculated using Equation 1.

$$R1 = \frac{R2 \times (V_{OUT} - 0.768\ V)}{0.768\ V} \quad (1)$$

Table 3 lists the R5 values for some common output voltages. Note that the values given in Table 3 are standard values and not the exact value calculated using Table 3.

Table 3. TPS563201EVM-715 Output Voltages

Output Voltage (V)	R1 (kΩ)	R2 (kΩ)	L1 (µH)			C5 + C6 +C7 (µF)
			MIN	TYP	MAX	
1.0	3.09	10.0	1.5	2.2	4.7	20 - 68
1.05	3.74	10.0	1.5	2.2	4.7	20 - 68
1.2	5.76	10.0	1.5	2.2	4.7	20 - 68
1.5	9.53	10.0	1.5	2.2	4.7	20 - 68
1.8	13.7	10.0	1.5	2.2	4.7	20 - 68
2.5	22.6	10.0	2.2	2.2	4.7	20 - 68
3.3	33.2	10.0	2.2	2.2	4.7	20 - 68
5.0	54.9	10.0	3.3	3.3	4.7	20 - 68
6.5	75.0	10.0	3.3	3.3	4.7	20 - 68

4 Test Setup and Results

This section describes how to properly connect, set up, and use the TPS563201EVM-715. The section also includes test results typical for the evaluation modules and efficiency, output load regulation, output line regulation, load transient response, output voltage ripple, input voltage ripple, start-up, and switching frequency.

4.1 Input/Output Connections

The TPS563201EVM-715 is provided with input/output connectors and test points as shown in Table 4. A power supply capable of supplying 3 A must be connected to J1 through a pair of 20-AWG wires. The load must be connected to J2 through a pair of 20-AWG wires. The maximum load current capability is 3 A. Wire lengths must be minimized to reduce losses in the wires. Test point TP1 provides a place to monitor the V_{IN} input voltages with TP2 providing a convenient ground reference. TP7 is used to monitor the output voltage with TP8 as the ground reference.

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Table 4. Connection and Test Points

REFERENCE DESIGNATOR	FUNCTION
J1	V_{IN} (see Table 1 for V_{IN} range)
J2	V_{OUT}, 1.05 V at 3-A maximum
JP1	EN control. Shunt EN to GND to disable, shunt EN to V_{IN} to enable.
TP1	V_{IN} positive monitor point
TP2	GND monitor test point
TP3	EN test point
TP4	Switch node test point
TP5	Test point for loop response measurements
TP6	V_{OUT} positive monitor point
TP7	GND monitor test point
TP8	GND monitor test point

4.2 Start-Up Procedure

1. Ensure that the jumper at JP1 (Enable control) pins 1 and 2 are covered to shunt EN to GND, disabling the output.
2. Apply appropriate input voltage to VIN (J1-2) and GND (J1-1).
3. Move the jumper at JP1 (Enable control) from pins 1 and 2 (EN and GND), to pins 2 and 3 (EN and V_{IN}) enabling the output.

4.3 Efficiency

Figure 1 shows the efficiency for the TPS563201EVM-715 at an ambient temperature of 25°C.

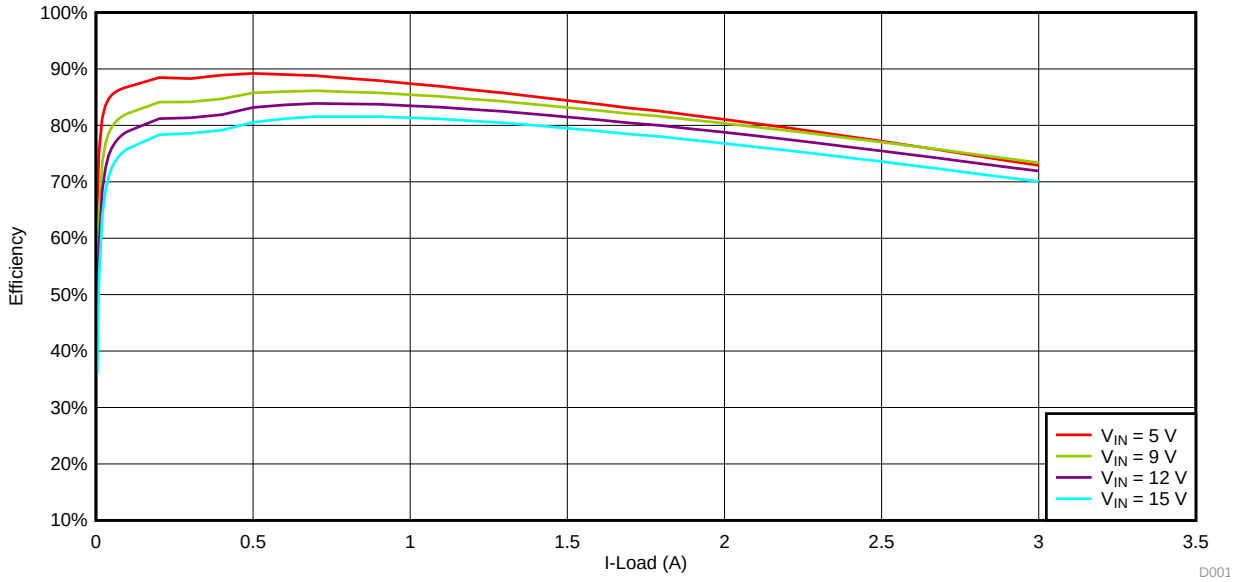


Figure 1. Efficiency

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Figure 2 shows the efficiency at light loads for the TPS563201EVM-715 at an ambient temperature of 25°C.

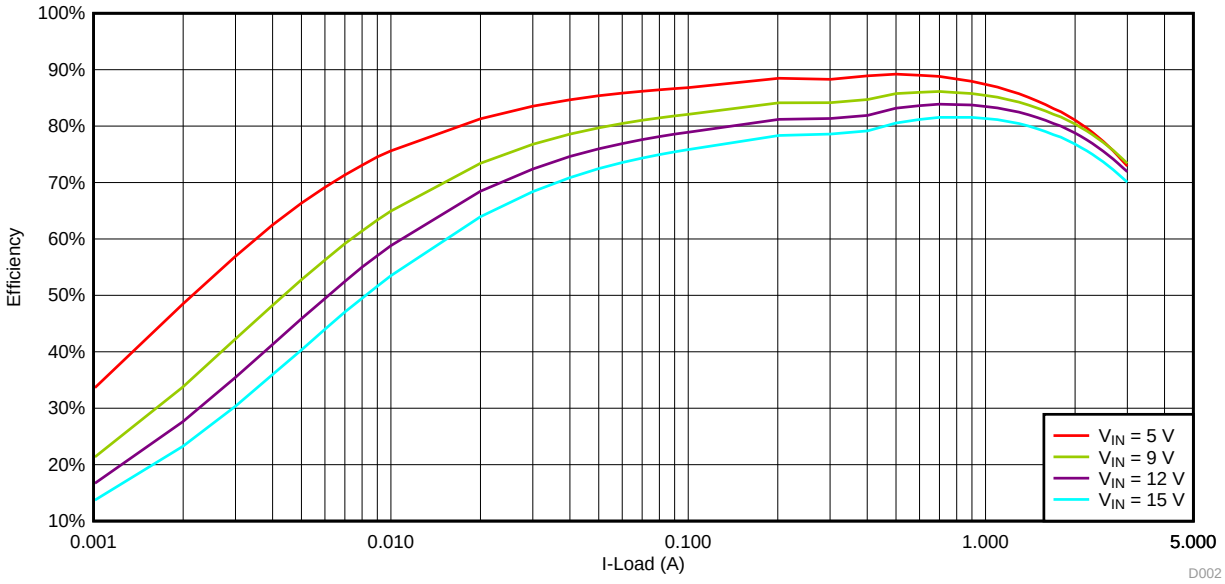


Figure 2. Light Load Efficiency

4.4 Load Regulation

The load regulation for the TPS563201EVM-715 is shown in Figure 3.

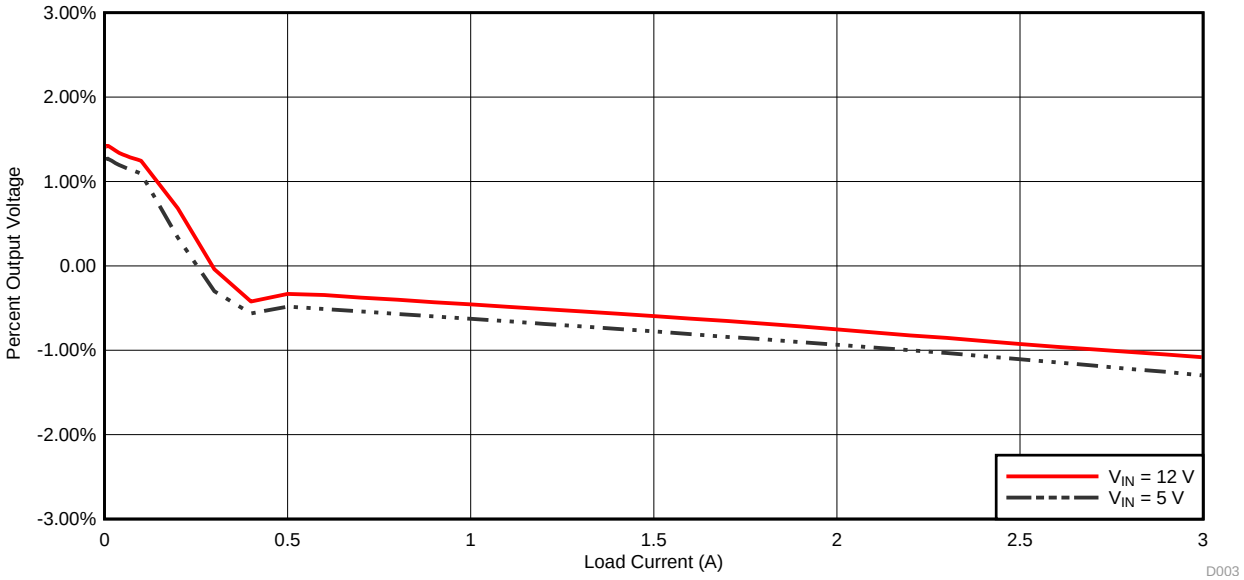


Figure 3. Load Regulation

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4.5 Line Regulation

The line regulation for the TPS563201EVM-715 is shown in Figure 4.

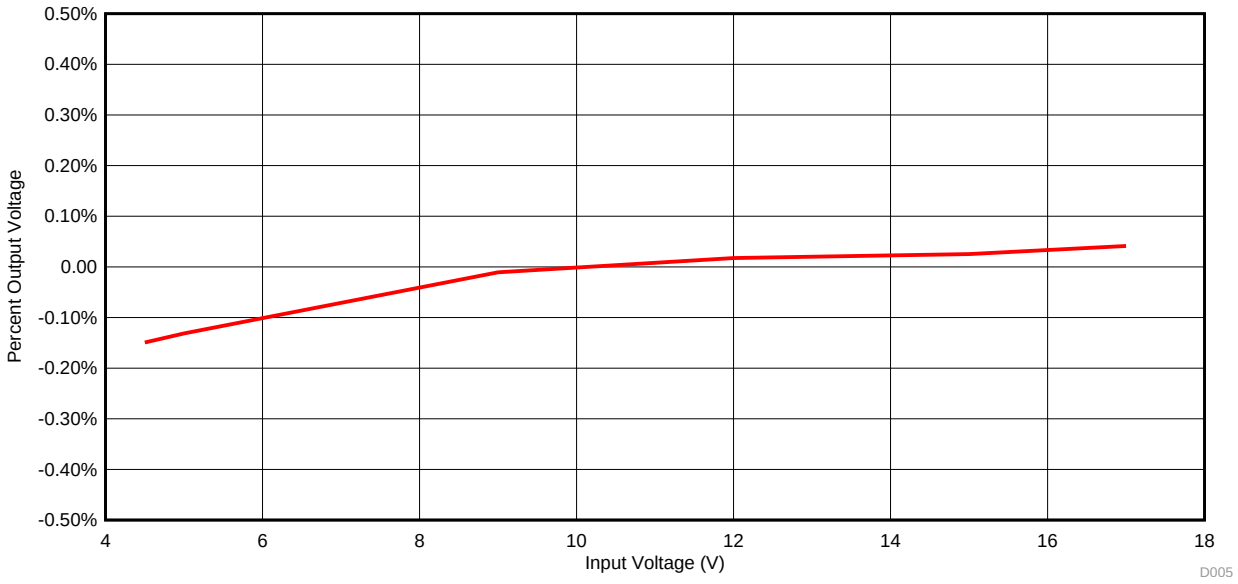


Figure 4. Line Regulation

4.6 Load Transient Response

The TPS563201EVM-715 response to load transient is shown in Figure 5. The current steps and slew rates are indicated in the figures. Total peak-to-peak voltage variation is as shown.

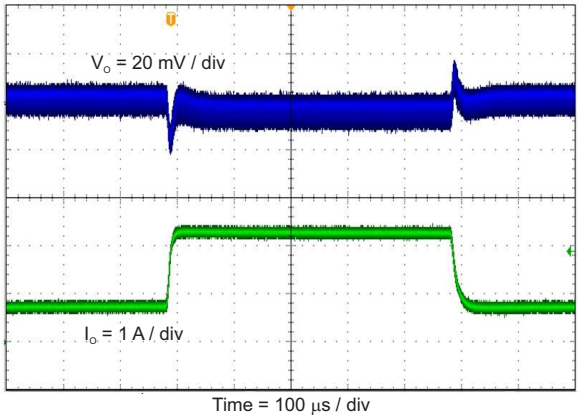


Figure 5. Load Transient Response, 25% to 75% Load Step

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4.7 Output Voltage Ripple

The TPS563201EVM-715 output voltage ripple is shown in Figure 6, Figure 7, and Figure 8. The output currents are as indicated.

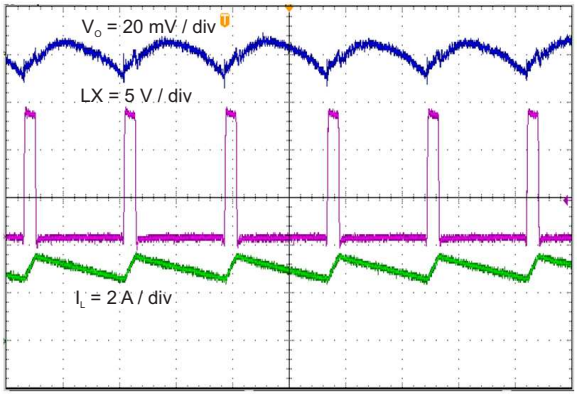


Figure 6. Output Voltage Ripple, I_{OUT} = 3 A

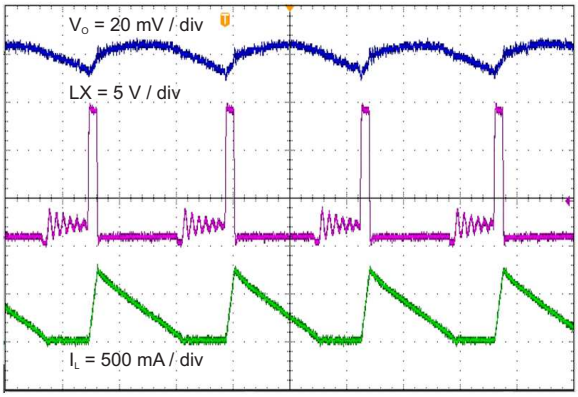


Figure 7. Output Voltage Ripple, I_{OUT} = 250 mA

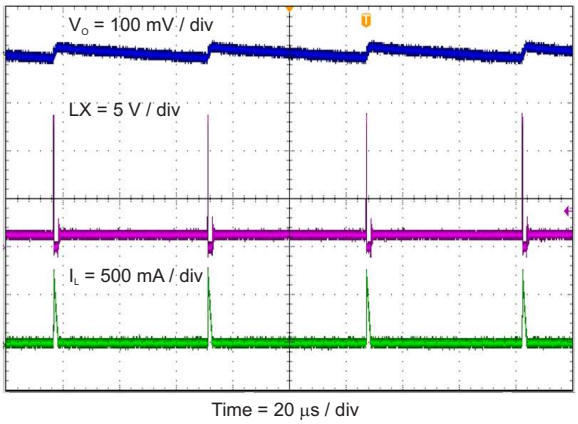


Figure 8. Output Voltage Ripple, I_{OUT} = 10 mA

4.8 Input Voltage Ripple

The TPS563201EVM-715 input voltage ripple is shown in Figure 9. The output current is as indicated.

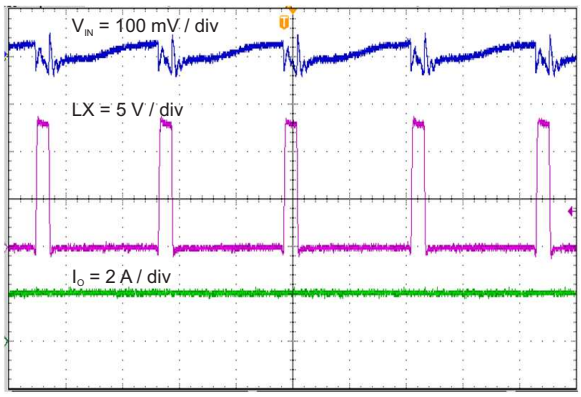


Figure 9. Input Voltage Ripple, I_{OUT} = 3 A

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4.9 Start-Up

The TPS563201EVM-715 start-up waveform relative to V_{IN} is shown in Figure 10. Load = 1 Ω resistive.

The TPS563201EVM-715 start-up waveform relative to enable (EN) is shown in Figure 11. Load = 1 Ω resistive.

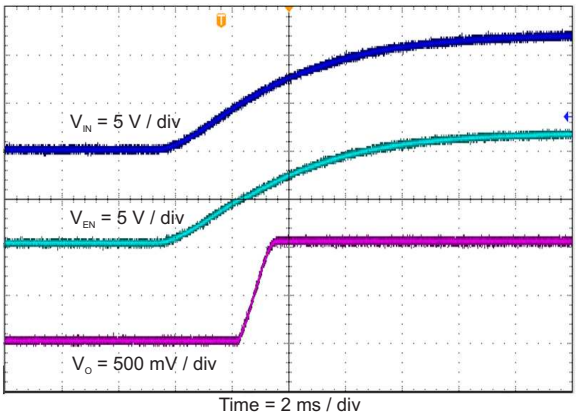


Figure 10. Start-Up Relative to V_{IN}

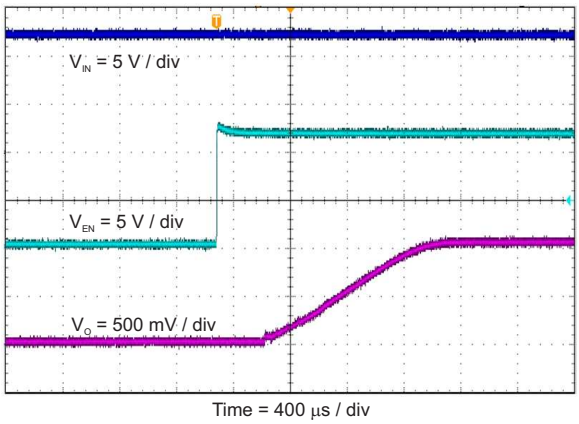


Figure 11. Start-Up Relative to EN

4.10 Shut-Down

The TPS563201EVM-715 shut-down waveform relative to V_{IN} is shown in Figure 12. Load = 1 Ω resistive.

The TPS563201EVM-715 shut-down waveform relative to EN is shown in Figure 13. Load = 1 Ω resistive.

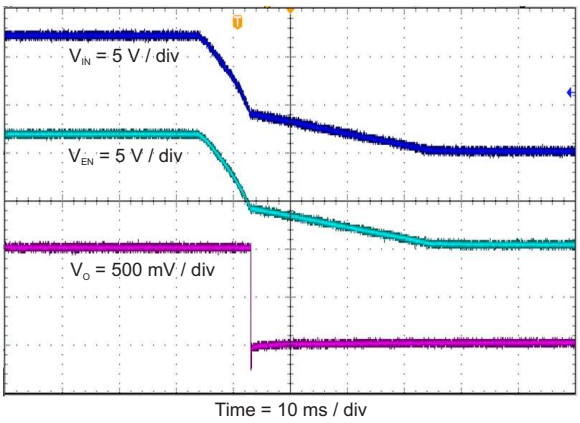


Figure 12. Shut-Down Relative to V_{IN}

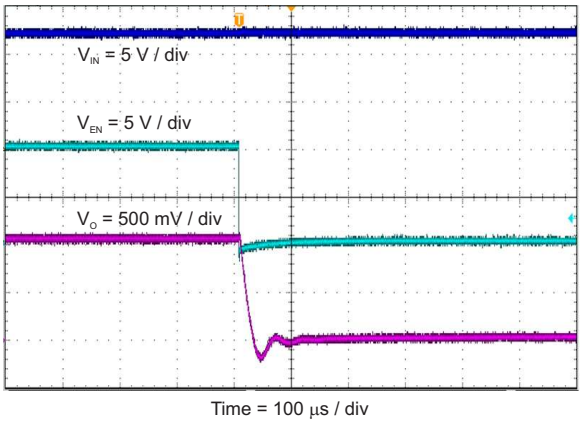


Figure 13. Shut-Down Relative to EN

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5 Board Layout

This section provides a description of the TPS563201EVM-715, board layout, and layer illustrations.

5.1 Layout

The board layout for the TPS563201EVM-715 is shown in Figure 14, Figure 15 and Figure 16. The top layer contains the main power traces for VIN, VOUT, and ground. Also on the top layer are connections for the pins of the TPS563201 and a large area filled with ground. Most of the signal traces are also located on the top side. The input decoupling capacitors, C1, C2, and C3 are located as close to the IC as possible. The input and output connectors, test points, and all of the components are located on the top side. The bottom layer is a ground plane along with the switching node copper fill, signal ground copper fill and the feed back trace from the point of regulation to the top of the resistor divider network.

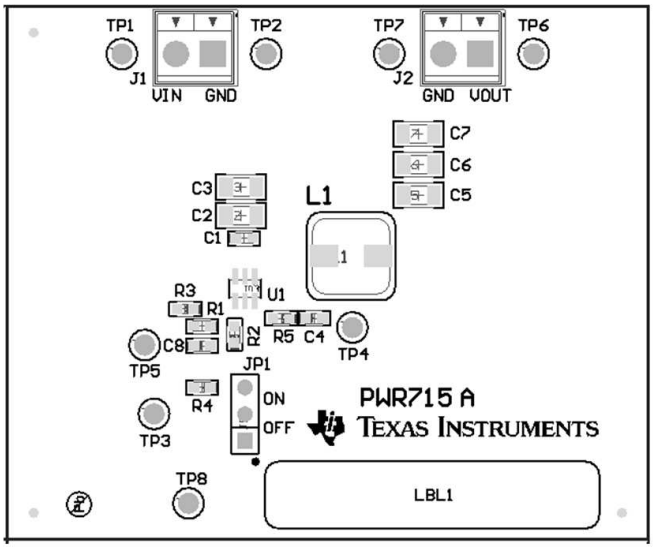


Figure 14. Top Assembly

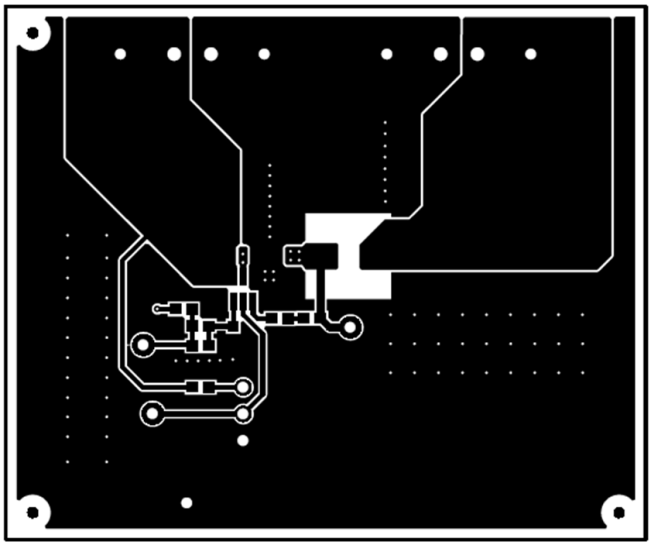


Figure 15. Top Layer

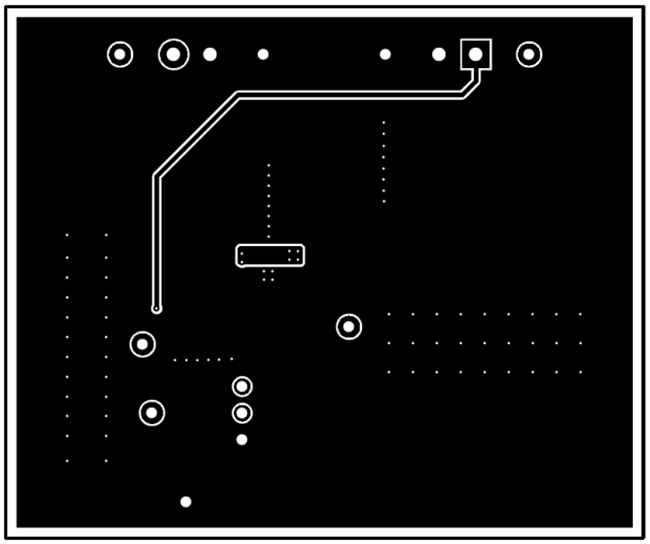


Figure 16. Bottom Layer

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6 Schematic, Bill of Materials, and Reference

6.1 Schematic

Figure 17 is the schematic for the TPS563201EVM-715.

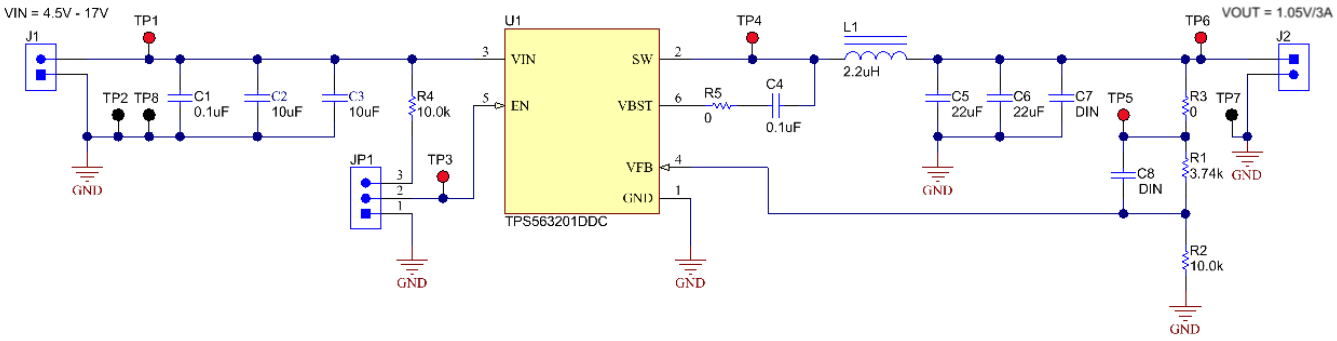


Figure 17. TPS563201EVM-715 Schematic Diagram

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6.2 Bill of Materials

Table 5. Bill of Materials

Designator	Qty	Value	Description	PackageReference	PartNumber	ManµFacturer
!PCB1	1		Printed Circuit Board		PWR715	Any
C1, C4	2	0.1 µF	Capacitor, ceramic, 0.1µF, 25 V, ±10%, X5R, 0603	0603	GRM188R61E104KA01D	Murata
C2, C3	2	10 µF	Capacitor, ceramic, 10µF, 25 V, ±10%, X5R, 1206	1206	GRM31CR61E106KA12L	Murata
C5, C6	2	22 µF	Capacitor, ceramic, 22µF, 10 V, ±10%, X7R, 1206	1206	GRM31CR71A226KE15L	Murata
C8	0	10 pF	Capacitor, ceramic, 10 pF, 100 V, ±5%, C0G/NP0, 0603	0603	GRM1885C2A100JA01D	Murata
J1, J2	2		Terminal Block, 6A, 3.5mm Pitch, 2-Pos, TH	7.0x8.2x6.5mm	ED555/2DS	On-Shore Technology
JP1	1		Header, 100 mil, 3x1, Tin, TH	Header, 3 PIN, 100mil, Tin	PEC03SAAN	Sullins Connector Solutions
L1	1	2.2 µH	Inductor, Shielded Drum Core, Superflux, 2.2 µH, 9 A, 0.0115 Ω, SMD	WE-HC4	744311220	Wurth Elektronik eiSos
LBL1	1		Thermal Transfer Printable Labels, 1.250" W x 0.250" H - 10,000 per roll	PCB Label 1.25"H x 0.250"W	THT-13-457-10	Brady
R1	1	3.74 kΩ	Resistor, 3.74 kΩ, 1%, 0.1 W, 0603	0603	CRCW06033K74FKEA	Vishay-Dale
R2, R4	2	10.0 kΩ	Resistor, 10.0 kΩ, 1%, 0.1 W, 0603	0603	CRCW060310K0FKEA	Vishay-Dale
R3, R5	2	0	Resistor, 0 Ω, 5%, 0.1 W, 0603	0603	ERJ-3GEY0R00V	Panasonic
SH-JP1	1	1 × 2	Shunt, 100 mil, Gold plated, Black	Shunt	969102-0000-DA	3M
TP1, TP3, TP4, TP5, TP6	5	Red	Test Point, Miniature, Red, TH	Red Miniature Testpoint	5000	Keystone
TP2, TP7, TP8	3	Black	Test Point, Miniature, Black, TH	Black Miniature Testpoint	5001	Keystone
U1	1		4.5 V to 17 V Input, 3-A Synchronous Step-Down Voltage Regulator	DDC0006A	TPS563201DDC	Texas Instruments
C8	0	10 pF	Capacitor, ceramic, 10 pF, 100 V, ± 5%, C0G/NP0, 0603	0603	GRM1885C2A100JA01D	Murata
C7	0	22 µF	Capacitor, ceramic, 22µF, 10 V, ±10%, X7R, 1206	1206	GRM31CR71A226KE15L	Murata

6.3 Reference

1. *TPS56320x 4.5 V to 17 V Input, 3-A Synchronous Step-Down Voltage Regulator in SOT-23* data sheet (SLVSD90)

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STANDARD TERMS AND CONDITIONS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, or documentation (collectively, an “EVM” or “EVMs”) to the User (“User”) in accordance with the terms and conditions set forth herein. Acceptance of the EVM is expressly subject to the following terms and conditions.

   1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM (“Software”) shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms and conditions that accompany such Software

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   2.1 These terms and conditions do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.

   2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for any defects that are caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI. Moreover, TI shall not be liable for any defects that result from User's design, specifications or instructions for such EVMs. Testing and other quality control techniques are used to the extent TI deems necessary or as mandated by government requirements. TI does not test all parameters of each EVM.

   2.3 If any EVM fails to conform to the warranty set forth above, TI's sole liability shall be at its option to repair or replace such EVM, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

3 *Regulatory Notices:*

   3.1 *United States*

      3.1.1 *Notice applicable to EVMs not FCC-Approved:*

      This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

      3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

      CAUTION

      This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

      Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

      FCC Interference Statement for Class A EVM devices

      NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 *Canada*

3.2.1 *For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210*

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 *Japan*

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required by Radio Law of Japan to follow the instructions below with respect to EVMs:

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page

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4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

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