TLV751-EVM Evaluation Module

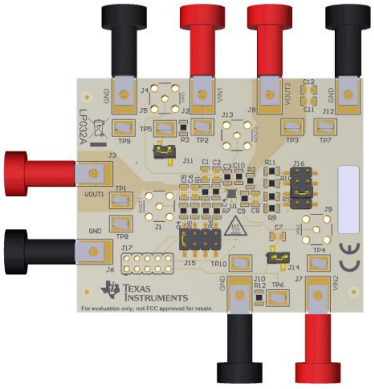


Figure 1. TLV751-EVM Evaluation Module

This user's guide describes the operational use of the TLV751-EVM evaluation module (EVM) as a reference design for engineering demonstration and evaluation of the TLV75101DSQ, a dual, high-accuracy, adjustable linear regulator (LDO). Included in this user's guide are setup and operating instructions, thermal and layout guidelines, a printed circuit board (PCB) layout, a schematic diagram, and a bill of materials (BOM).

Throughout this document, the terms *demonstration kit*, *evaluation board*, and *evaluation module* are synonymous with the TLV751-EVM.

Table 1 lists the related documentation available through the Texas Instruments web site at www.ti.com.

Table 1. Related Documentation

DEVICE	LITERATURE NUMBER
TLV751	SBVS385

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Contents

1 Introduction 3
1.1 Before You Begin 3
2 EVM Setup 4
2.1 Inputs/Outputs Connectors and Jumper Descriptions 4
2.2 Soldering Guidelines 5
2.3 Equipment Connection 6
3 Operation 6
4 PCB Layout 7
5 Schematic 9
6 Bill of Materials 10

List of Figures

1 TLV751-EVM Evaluation Module 1
2 Assembly Layer 7
3 Top Layer Routing 7
4 First Middle Layer 7
5 Second Middle Layer 7
6 Bottom Layer Routing 7
7 TLV751-EVM Schematic 9

List of Tables

1 Related Documentation 1
2 TLV751-EVM BOM 10

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1 Introduction

Texas Instruments' TLV751-EVM helps design engineers evaluate the operation and performance of the TLV751 family of linear regulators for possible use in their own circuit application. This particular EVM configuration contains a single high-accuracy, small size, dual-input linear regulator for a wide range of applications. The regulator is capable of delivering up to 500 mA to the load for each input channel with low V_{IN} to V_{OUT} dropout voltage. For stability, use a minimum derated capacitance of 0.47-µF (or larger) output capacitor for each output channel.

1.1 Before You Begin

The following warnings and cautions are noted for the safety of anyone using or working close to the TLV751-EVM. Observe all safety precautions.



Warning Warning Hot surface. Contact may cause burns. Do not touch.

CAUTION

The circuit module can be damaged by overtemperature. To avoid damage, monitor the temperature during evaluation and provide cooling, as needed, for your system environment.

CAUTION

Some power supplies can be damaged by application of external voltages. If you are using more than one power supply, check your equipment requirements and use blocking diodes or other isolation techniques, as needed, to prevent damage to your equipment.

CAUTION

The circuit module is not a finished product or electrical appliance. The module does not contain current or voltage thresholds for circuit protection. It must be used by qualified personnel with additional equipment for evaluation only.

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2 EVM Setup

This section describes how to properly connect and set up the TLV751-EVM, including the jumpers and connectors on the EVM board.

2.1 Inputs/Outputs Connectors and Jumper Descriptions

2.1.1 J1 – OUT1_S

Regulated output voltage one sense

2.1.2 J2 – IN1

Input power supply one voltage connector. Twist together the positive input lead and ground return lead from the input power supply one, and keep them as short as possible to minimize input inductance.

2.1.3 J3 – OUT1

Regulated output voltage one connector

2.1.4 J4 – IN1_S

Input voltage one sense

2.1.5 J5 –GND

Input supply one ground return connector

2.1.6 J6 – GND

Regulated output voltage one ground return connector

2.1.7 J7 – IN2

Input power supply two voltage connector. Twist together the positive input lead and ground return lead from the input power supply two, and keep them as short as possible to minimize input inductance.

2.1.8 J8 – OUT2

Regulated output voltage two connector

2.1.9 J9 – IN2_S

Input voltage two sense

2.1.10 J10 – GND

Input supply two ground return connector

2.1.11 J11 – EN1

Output voltage one enable. To enable output voltage one, connect a jumper to short VIN1 to EN1.

2.1.12 J12 – GND

Regulated output voltage two ground return connector

2.1.13 J13 – OUT2_S

Regulated output voltage two sense

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2.1.14 J14 – EN2

Output voltage two enable. To enable output voltage two, connect a jumper to short VIN2 to EN2.

2.1.15 J15 – OUT1 Set

For convenience, the EVM is prepopulated with four resistor divider options. Place a shunt on J15 next to the schematic label of your desired output voltage one.

2.1.16 J16 – OUT2 Set

For convenience, the EVM is prepopulated with four resistor divider options. Place a shunt on J15 next to the schematic label of your desired output voltage two.

2.1.17 J17 – Test Hookup

Intended for test purposes only

2.1.18 TP1 – OUT1_S

Regulated output voltage one test point

2.1.19 TP2 – IN1_S

Input voltage one test point

2.1.20 TP3 – OUT2_S

Regulated output voltage two test point

2.1.21 TP4 – IN2_S

Input voltage two test point

2.1.22 TP5 – EN1

Output voltage one enable test point

2.1.23 TP6 – EN2

Output voltage two enable test point

2.1.24 TP7 – GND

Ground test point

2.1.25 TP8 – GND

Ground test point

2.1.26 TP9 – GND

Ground test point

2.1.27 TP10 – GND

Ground test point

2.2 Soldering Guidelines

To avoid damaging the integrated circuit (IC), use a hot-air system for any solder rework to modify the EVM for the purpose of repair or other application reasons.

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2.3 Equipment Connection

Connect the equipment as shown in the following steps:

1. Set the input power supplies one and two up to 6 V (max), and turn them off.
2. Connect the GND leads of both supplies together.
3. Connect the positive voltage lead from the input power supply one to IN1 at the J2 connector of the EVM.
4. Connect the ground lead from the input power supplies one and two to GND at the J5 connector of the EVM.
5. Connect the positive voltage lead from the input power supply two to IN2 at the J7 connector of the EVM.
6. Connect the ground lead from the input power supplies one and two to GND at the J10 connector of the EVM.
7. Connect a 0-A to 500-mA load between OUT1 and GND.
8. Connect a 0-A to 500-mA load between OUT2 and GND.
9. Disable output voltage one by floating J11.
10. Disable output voltage two by floating J14.

3 Operation

Operate the equipment using the following steps:

1. Turn on the power supplies.
2. Enable the outputs by jumping J11 (the EN1 pin) to VIN1 and J14 (the EN2 pin) to VIN2.
3. Vary the respective loads and input voltages, as necessary, for test purposes.

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4 PCB Layout

Figure 2 to Figure 6 illustrate the PCB layout for this EVM.

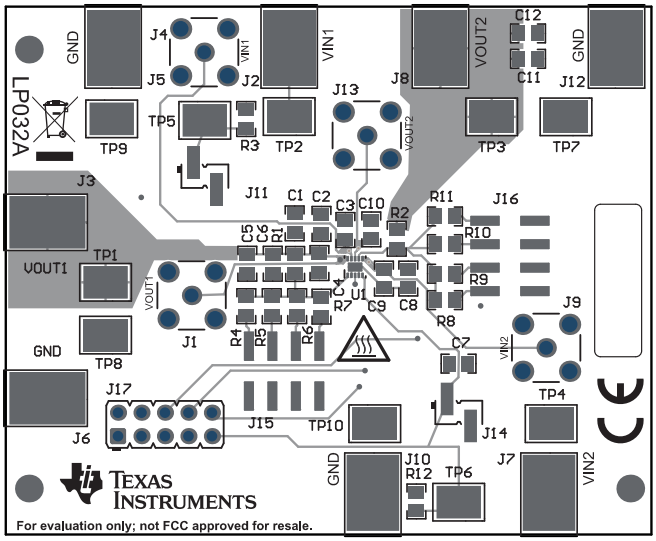


Figure 2. Assembly Layer

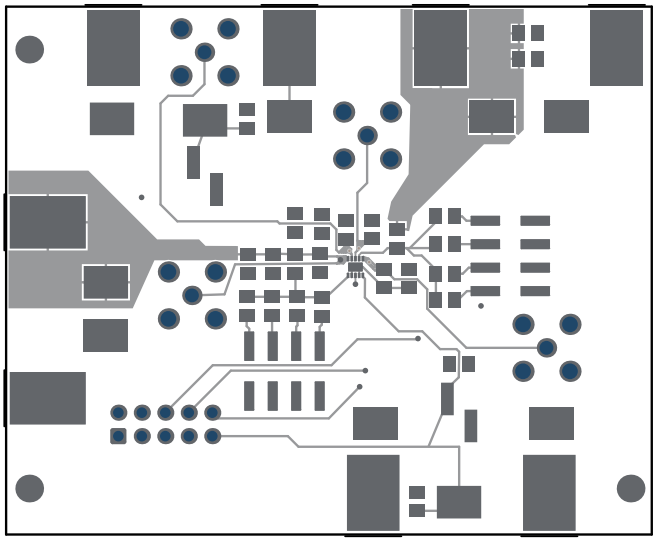


Figure 3. Top Layer Routing

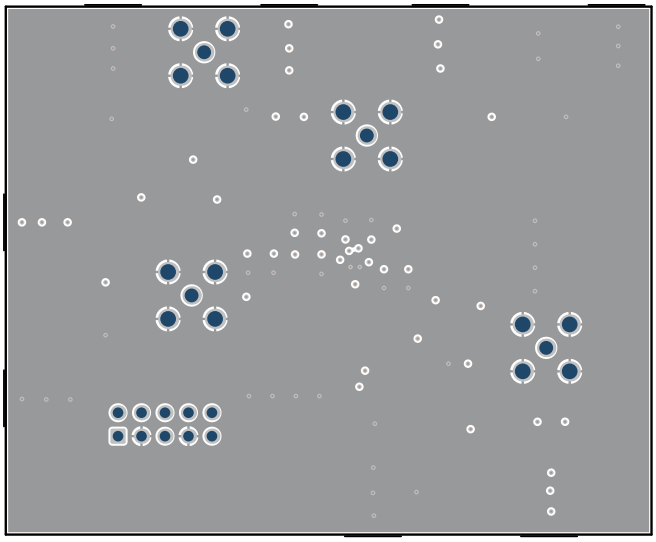


Figure 4. First Middle Layer

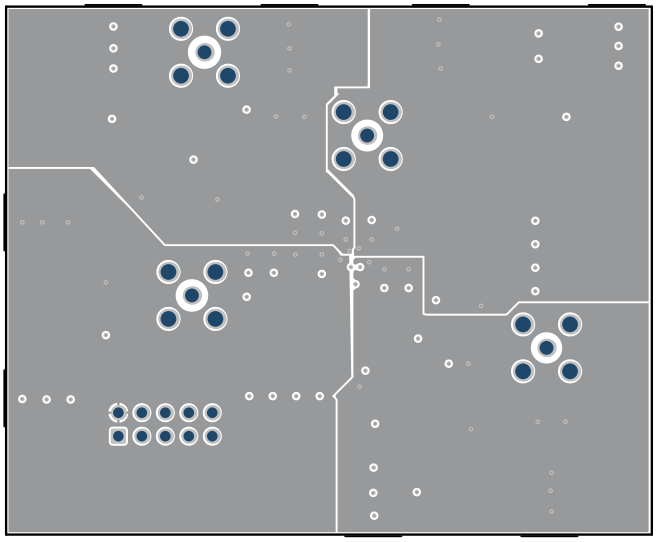


Figure 5. Second Middle Layer

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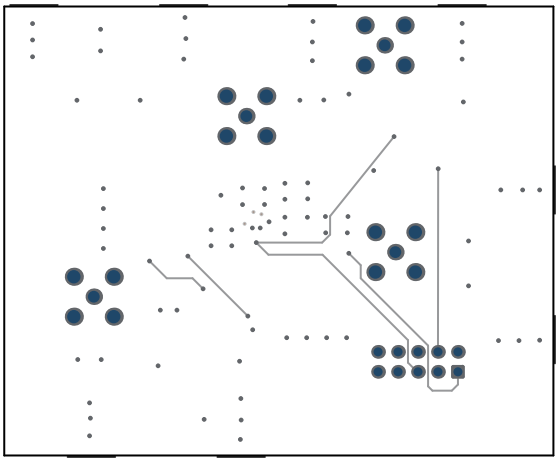


Figure 6. Bottom Layer Routing

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5 Schematic

Figure 7 shows this schematic for the EVM.

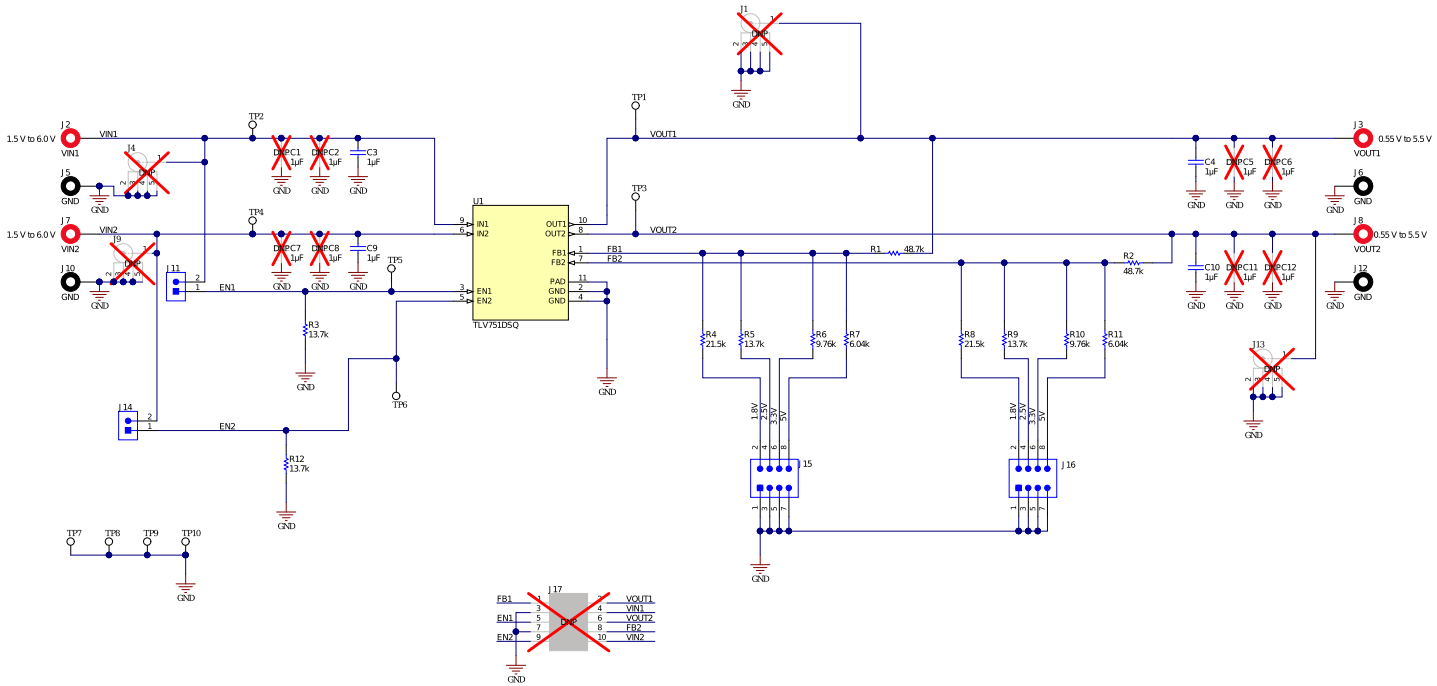


Figure 7. TLV751-EVM Schematic

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6 Bill of Materials

Table 2 shows the BOM for this EVM.

Table 2. TLV751-EVM BOM(1)(2)(3)(4)

DESIGNATOR	QTY	VALUE	DESCRIPTION	PACKAGE REFERENCE	PART NUMBER	MANUFACTURER	ALTERNATE PART NUMBER	ALTERNATE MANUFACTURER
!PCB1	1		Printed Circuit Board		LP032	Any		
C3, C4, C9, C10	4	1 µF	CAP, CERM, 1 µF, 50 V, ±10%, X7R, 0805	0805	08055C105KAT2A	AVX		
J2, J3, J7, J8	4		Standard Banana Jack, Insulated, Red	6091	6091	Keystone		
J5, J6, J10, J12	4		Standard Banana Jack, Insulated, Black	6092	6092	Keystone		
J11, J14	2		Header, 2.54 mm, 2x1, Tin, SMT	Header, 2.54 mm, 2x1, SMT	TSM-102-03-T-SV	Samtec		
J15, J16	2		Header, 2.54 mm, 4x2, Gold, SMT	Header, 2.54 mm, 4x2, SMT	61000821121	Wurth Elektronik		
LBL1	1		Thermal Transfer Printable Labels, 0.650" W x 0.200" H - 10 000 per roll	PCB Label 0.650 x 0.200 inch	THT-14-423-10	Brady		
R1, R2	2	48.7 k	RES, 48.7 k, 1%, 0.125 W, AEC-Q200 Grade 0, 0805	0805	CRCW080548K7FKEA	Vishay-Dale		
R3, R5, R9, R12	4	13.7 k	RES, 13.7 k, 1%, 0.125 W, AEC-Q200 Grade 0, 0805	0805	CRCW080513K7FKEA	Vishay-Dale		
R4, R8	2	21.5 k	RES, 21.5 k, 1%, 0.125 W, AEC-Q200 Grade 0, 0805	0805	CRCW080521K5FKEA	Vishay-Dale		
R6, R10	2	9.76 k	RES, 9.76 k, 1%, 0.125 W, AEC-Q200 Grade 0, 0805	0805	CRCW08059K76FKEA	Vishay-Dale		
R7, R11	2	6.04 k	RES, 6.04 k, 1%, 0.125 W, AEC-Q200 Grade 0, 0805	0805	CRCW08056K04FKEA	Vishay-Dale		
SH-J1, SH-J2, SH-J3, SH-J4	4	1x2	Shunt, 100 mil, Gold plated, Black	Shunt	SNT-100-BK-G	Samtec	969102-0000-DA	3M
TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8, TP9, TP10	10		Test Point, Compact, SMT	Testpoint_Keystone_Compact	5016	Keystone		
U1	1		Dual, 500-mA, high-accuracy, adjustable-LDO in a small size package, DSQ0010A (WSON-10)	DSQ0010A	TLV751DSQ	Texas Instruments		Texas Instruments
C1, C2, C5, C6, C7, C8, C11, C12	0	1 µF	CAP, CERM, 1 µF, 50 V, ±10%, X7R, 0805	0805	08055C105KAT2A	AVX		
FID1, FID2, FID3	0		Fiducial mark. There is nothing to buy or mount.	N/A	N/A	N/A		
J1, J4, J9, J13	0		SMA Straight Jack, Gold, 50 Ω, TH	SMA Straight Jack, TH	901-144-8RFX	Amphenol RF		
J17	0		Header, 2.54 mm, 5x2, Gold, TH	Header, 2.54 mm, 5x2, TH	61301021121	Wurth Elektronik		

(1) These assemblies are ESD sensitive, observe ESD precautions.
(2) These assemblies must be clean and free from flux and all contaminants. Use of no-clean flux is not acceptable.
(3) These assemblies must comply with workmanship standards IPC-A-610 Class 2.
(4) Unless otherwise noted in the *Alternate Part Number* or *Alternate Manufacturer* columns, all parts can be substituted with equivalents.

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1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an “EVM” or “EVMs”) to the User (“User”) in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.

   1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM (“Software”) shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms that accompany such Software

   1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.

2 *Limited Warranty and Related Remedies/Disclaimers*:

   2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.

   2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.

   2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

WARNING

Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI’s recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI’s recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI’s instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGREDATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

3 *Regulatory Notices:*

3.1 *United States*

3.1.1 *Notice applicable to EVMs not FCC-Approved:*

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 *Canada*

3.2.1 *For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247*

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 *Japan*

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

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3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page
電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page

3.4 *European Union*

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive)*:

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

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