ISDN S-Interface Transformers

Dual - Transmit / Receive

Surface Mount Package

Meets the Pulse Waveform Template of CCITT I.430when recommended Transformer & Chip pair are used.

Excellent Longitudinal Balance

2000V & 4000V RMS min. Isolation Versions

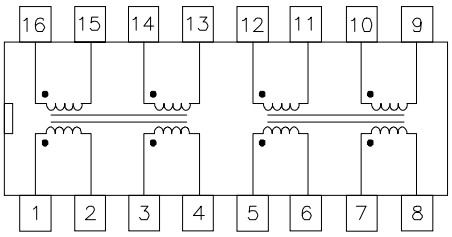
Surface Mount Replacements for T-10500 / T-10550 families

Electrical Specifications [3,4] at 25°C

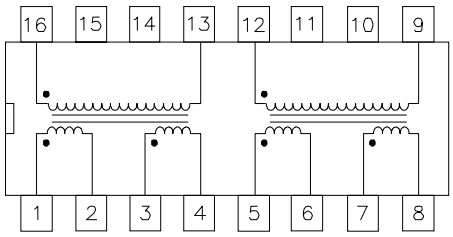
4000V_{RMS} Isolation *SMD* P/N	2000V_{RMS} Isolation *SMD* P/N	Transmit Receive	Ratio (±2%)	OCL [1,2] Pri. min. (mH)	L_L Sec. max. (µH)	Cw/w max. (pF)	CD pri max. (pF)	DCR pri ±15% (Ω)	DCR sec ±15% (Ω)
T-13550	T-13500	T	1:2	22	15	100	80	2.3	4.0
		R	1:1	22	5	100	40	2.4	2.4
T-13551	T-13501	T	1:1	22	5	100	40	2.4	2.4
		R	1:1	22	5	100	40	2.4	2.4
T-13552	T-13502	T	1:1.8	22	15	100	140	2.5	4.2
		R	1:2.5	22	30	100	150	2.5	5.8
T-13553	T-13503	T	1:2	22	15	100	80	2.3	4.0
		R	1:2	22	15	100	80	2.3	4.0
T-13554	T-13504	T	1:2.5	22	15 - 40	100	110	2.5	5.8
		R	1:2.5	22	15 - 40	100	110	2.5	5.8
T-13555	T-13505	T	1:2	22	11	100	180	2.5	5.0
		R	1:2	22	11	100	180	2.5	5.0
T-13556	T-13506 (2400V_{RMS} Isolation)	T	1:2.5	22	4	100	100	2.3 max.	5.8 max.
		R	1:2.5	22	4	100	100	2.3 max.	5.8 max.

1) Primary is Line Side
2) OCL @10 KHz and 700 mV, except T-13506, T-13556 voltage is 500mV
3) Unbalanced current at TE: Δ Idc = 1 mA max.
4) Longitudinal Conversion Loss: 10 kHz to 300 kHz is 60dB min.

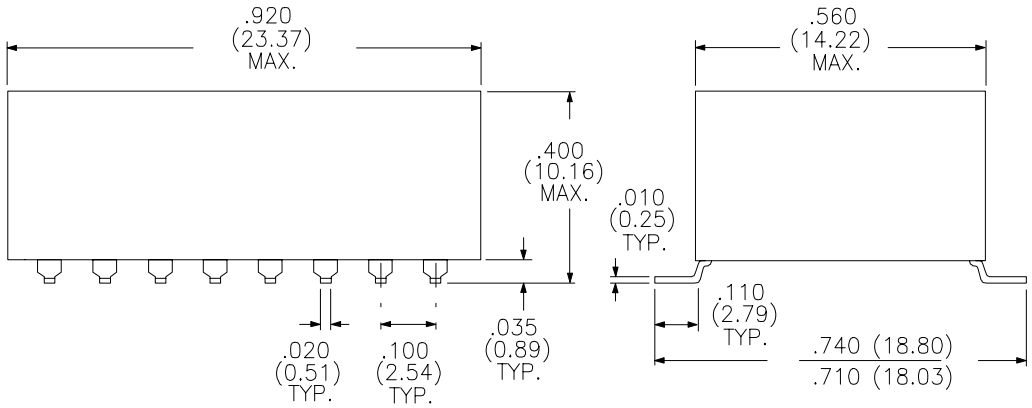
Schematic T-135*0* thru T-135*0*5 and T-135*5*0 thru T-135*5*5



Schematic T-13506 & T-13556



Package Dimensions in Inches (mm)



Specifications subject to change without notice.

For other values & Custom Designs, contact factory.

ISDNDULG - 4/99