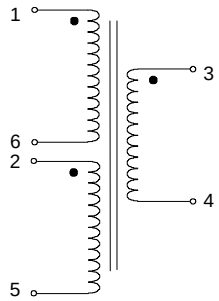
ADSL Line Interface Transformer

Designed for use with Analog Devices # AD20msp910

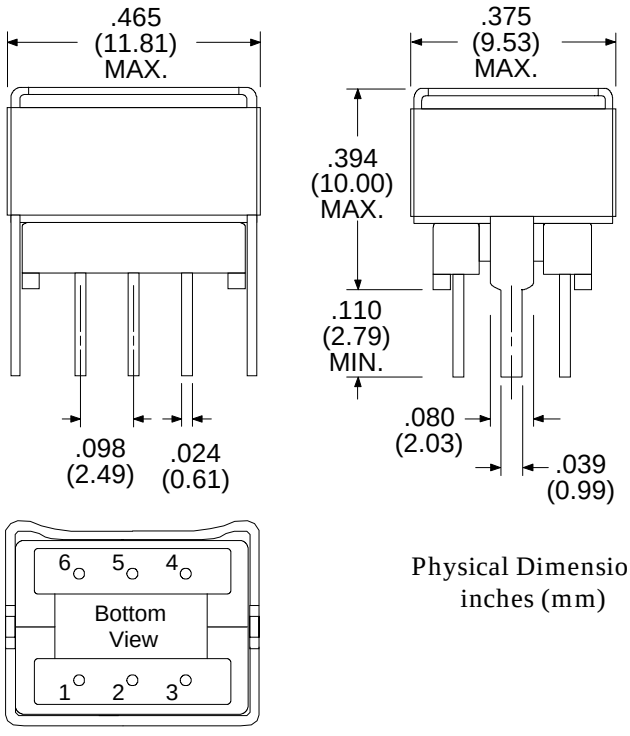
PARAMETER		UNITS
TURNS RATIO 1 : 1 ±1% Pins (3-4) : (1-5)		
Inductance [1] Min.	25.4	mH
DCR Pins 1 - 6	1.30 ±10%	Ω
DCR Pins 2 - 5	1.30 ±10%	Ω
DCR Pins 3 - 4	2.25 ±10%	Ω
Longitudinal Balance at 20 kHz - 1.1 MHz	40 Min.	dB
Return Loss at 30 kHz - 1.1 MHz	10	dB
Isolation (Any winding to any winding)	1250	V_{RMS}
Frequency Response 30 - 700 kHz	±0.5	dB

1. Tested @ 10 kHz and 100 mVAC (Pins 3-4)
Tested on a Wayne Kerr 3245

Schematic Diagram

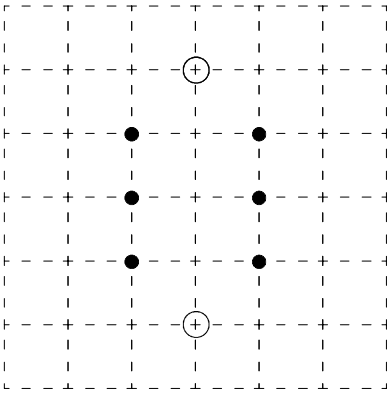


Primary = Line Side
Secondary = Device Side



Physical Dimensions
inches (mm)

PIN POSITION GRID 0.098 (2.5)
○ CLIP SET TERMINATIONS
● BOBBIN TERMINATIONS



Rhombus P/N: **T-1099**	
Cust. P/N:	Name:
Date: **6/25/98**	Sheet: 1 of 1

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