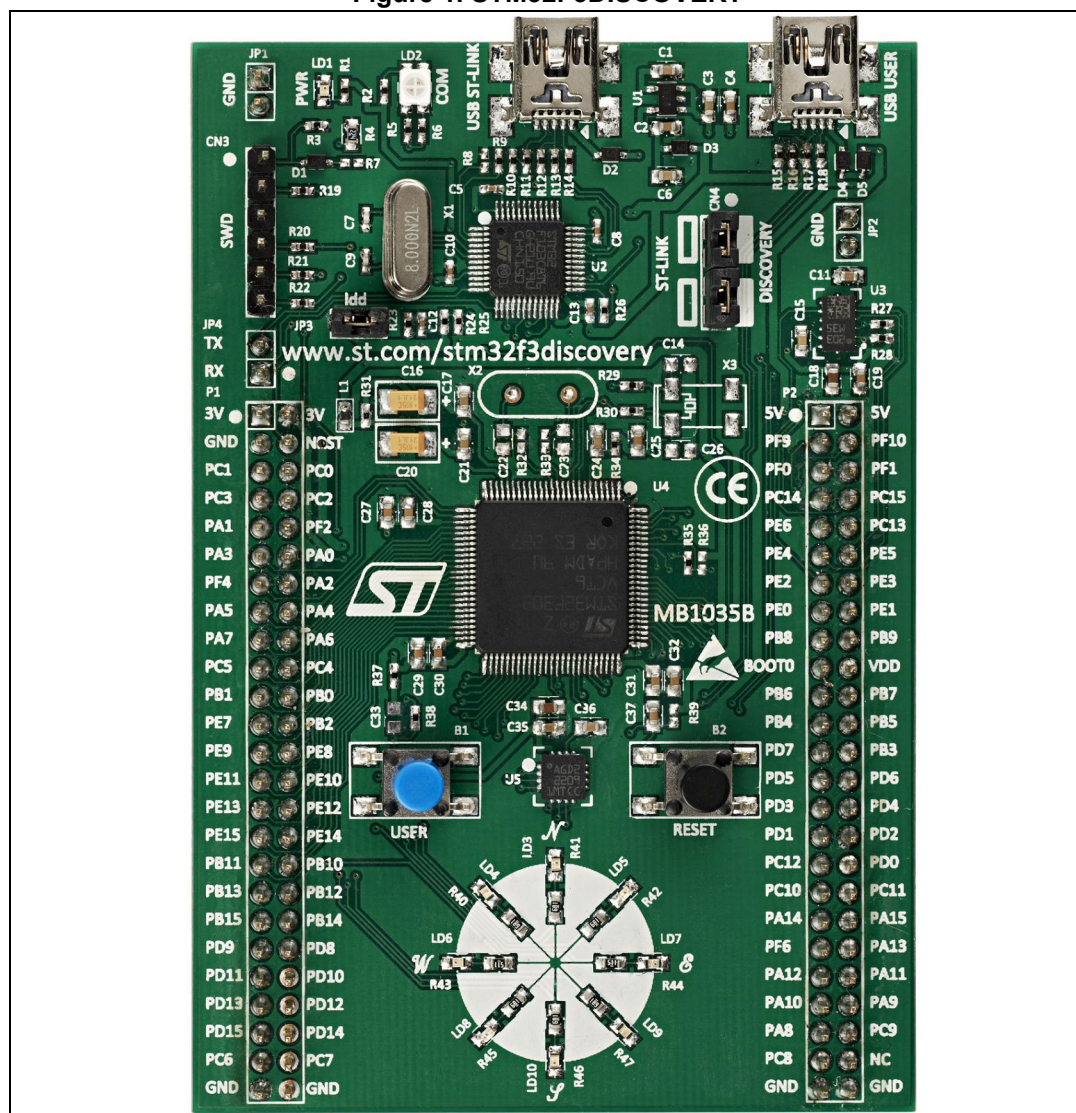


## Introduction

The STM32F3DISCOVERY Discovery kit is a complete demonstration and development platform for STMicroelectronics Arm® Cortex®-M4 core-based STM32F303VCT6 microcontroller. It includes an ST-LINK/V2 (or V2-B) embedded debug tool interface, an ST MEMS gyroscope, an ST MEMS E-compass, LEDs, push-buttons, and a USB Mini-B connector.

Figure 1. STM32F3DISCOVERY



Picture is not contractual.

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# 1 Features

The STM32F3DISCOVERY board offers the following features.

- STM32F303VCT6 Arm<sup>®(a)</sup>-based microcontroller featuring 256 Kbytes of Flash memory, 48 Kbytes of RAM in an LQFP100 package
- On-board ST-LINK/V2 for PCB Version A or B or ST-LINK/V2-B for PCB Version C and newer
- USB ST-LINK functions:
  - Debug port
  - Virtual COM port with ST-LINK/V2-B only
  - Mass storage with ST-LINK/V2-B only
- Board power supply: through the USB bus or from an external 3 V or 5 V supply voltage
- External application power supply: 3 V and 5 V
- L3GD20, ST MEMS motion sensor, 3-axis digital output gyroscope
- LSM303DLHC, ST MEMS system-in-package featuring a 3D digital linear acceleration sensor and a 3D digital magnetic sensor
- Ten LEDs:
  - LD1 (red) for 3.3 V power-on.
  - LD2 (red/green) for USB communication.
  - Eight user LEDs, LD3/10 (red), LD4/9 (blue), LD5/8 (orange) and LD6/7 (green).
- Two push buttons (user and reset)
- USB USER with Mini-B connector
- Extension header for LQFP100 I/Os for quick connection to prototyping board and easy probing
- Comprehensive free software including a variety of examples, part of STM32CubeF3 package or STSW-STM32118 for legacy Standard Library usage

A large number of free ready-to-run application firmware examples are available at [www.st.com/stm32f3discovery](http://www.st.com/stm32f3discovery) to support a quick evaluation and development.

**arm**

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a. Arm is a registered trademark of Arm Limited (or its subsidiaries) in the US and/or elsewhere.

## 2 Product marking

Evaluation tools marked as "ES" or "E" are not yet qualified and therefore they are not ready to be used as reference design or in production. Any consequences deriving from such usage will not be at ST charge. In no event, ST will be liable for any customer usage of these engineering sample tools as reference design or in production.

"E" or "ES" marking examples of location:

- On the targeted STM32 that is soldered on the board (for illustration of STM32 marking, refer to the section "Package information" of the STM32 datasheet available at [www.st.com](http://www.st.com)).
- Next to the evaluation tool ordering part number, that is stuck or silk-screen printed on the board.

## 3 Ordering information

To order the Discovery kit for the STM32F303 line of microcontrollers, refer to [Table 1](#).

**Table 1. List of the order codes**

| Order code       | ST-LINK version                          |
|------------------|------------------------------------------|
| STM32F3DISCOVERY | ST-LINK/V2 for PCB version A or B        |
|                  | ST-LINK/V2-B for PCB version C and newer |

## 4 Conventions

[Table 2](#) provides the definition of some conventions used in the present document.

**Table 2. ON/OFF conventions**

| Convention            | Definition                       |
|-----------------------|----------------------------------|
| Jumper JP1 ON         | Jumper fitted                    |
| Jumper JP1 OFF        | Jumper not fitted                |
| Solder bridge SBx ON  | SBx connections closed by solder |
| Solder bridge SBx OFF | SBx connections left open        |

## 5 Quick start

The STM32F3DISCOVERY board is a low-cost and easy-to-use development kit to quickly evaluate and start development with an STM32 F3 Series microcontroller. Before installing and using the product, accept the Evaluation Product License Agreement from the [www.st.com/stm32f3discovery](http://www.st.com/stm32f3discovery) webpage. For more information on the STM32F3DISCOVERY board and for demonstration software, visit the [www.st.com/stm32f3discovery](http://www.st.com/stm32f3discovery) webpage.

### 5.1 Getting started

Follow the sequence below to configure the STM32F3DISCOVERY board and launch the DISCOVER application:

1. Check the jumper positions on the board, JP3 ON, CN4 ON (DISCOVERY selected).
2. Connect the STM32F3DISCOVERY board to a PC with a USB cable Type-A to Mini-B through the USB ST-LINK or USB USER connector to power the board. The red LEDs LD1 (PWR) and LD2 (COM) light up.
3. The eight LED indicators blink sequentially.
4. Press the USER button to enable the ST MEMS gyroscope sensor.
5. Observe how the blinking of the LEDs indicates the gyroscope movements.
6. Press the USER button again to enable the ST MEMS E-compass sensor.
7. Keep the board in a horizontal position. One of the eight LEDs lights up to indicate the direction to North.
8. Tilt the board and the eight LEDs blink again
9. To examine or modify the DISCOVER project related to this demo, visit the [www.st.com/stm32f3discovery](http://www.st.com/stm32f3discovery) webpage and follow the tutorial. Discover the STM32F303xx MCU features, download and execute programs proposed in the list of projects.
10. Develop applications using the available examples.

## 5.2 System requirements

- Windows® PC (XP, 7, 8)
- USB Type-A to Mini-B cable

## 5.3 Development toolchains supporting the STM32F3DISCOVERY

- IAR™ EWARM (IAR Embedded Workbench®)
- Keil® MDK-ARM™
- GCC-based IDEs (free AC6: SW4STM32, Atollic® TrueSTUDIO®,...)

## 6 Hardware layout and configuration

The STM32F3DISCOVERY board is designed around the STM32F303VCT6 microcontroller in a 100-pin LQFP package.

[Figure 2](#) illustrates the connections between the STM32F303VCT6 and its peripherals (ST-LINK/V2, push-buttons, LEDs, USB, ST MEMS Gyroscope, ST MEMS E-compass, and connectors).

[Figure 2](#) and [Figure 3](#) help the user to locate these features on the STM32F3DISCOVERY board.

**Figure 2. Hardware block diagram**

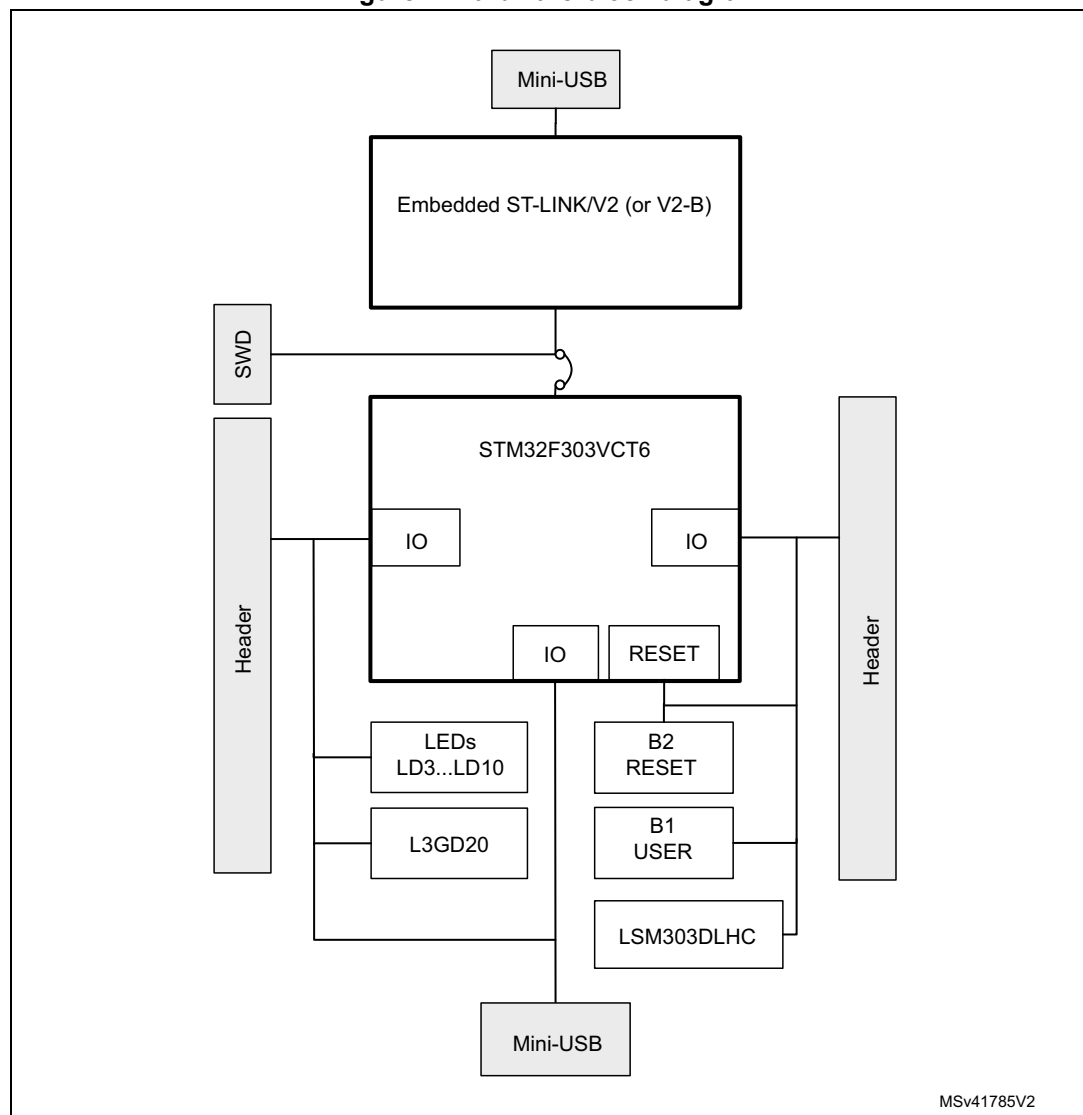
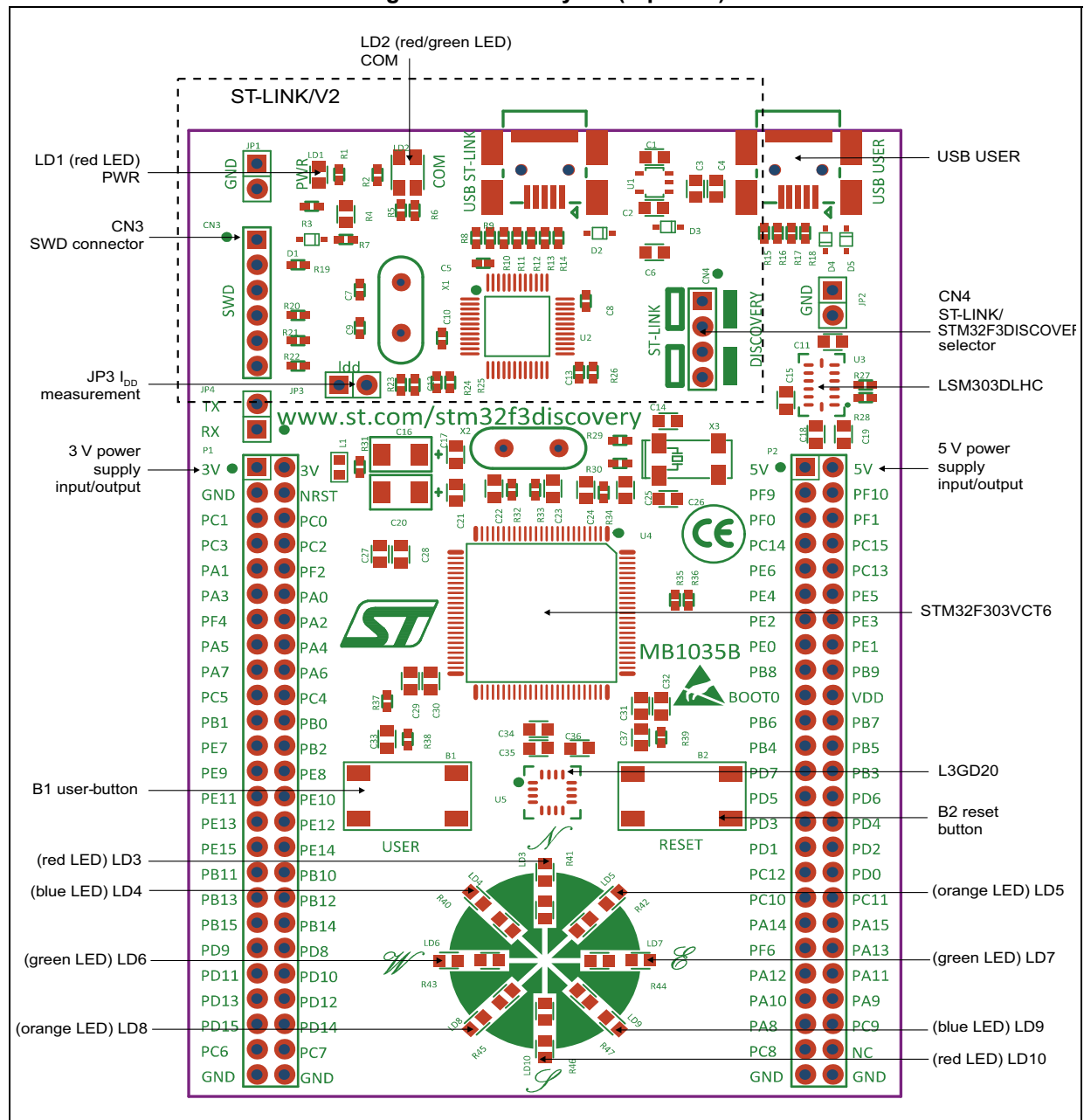
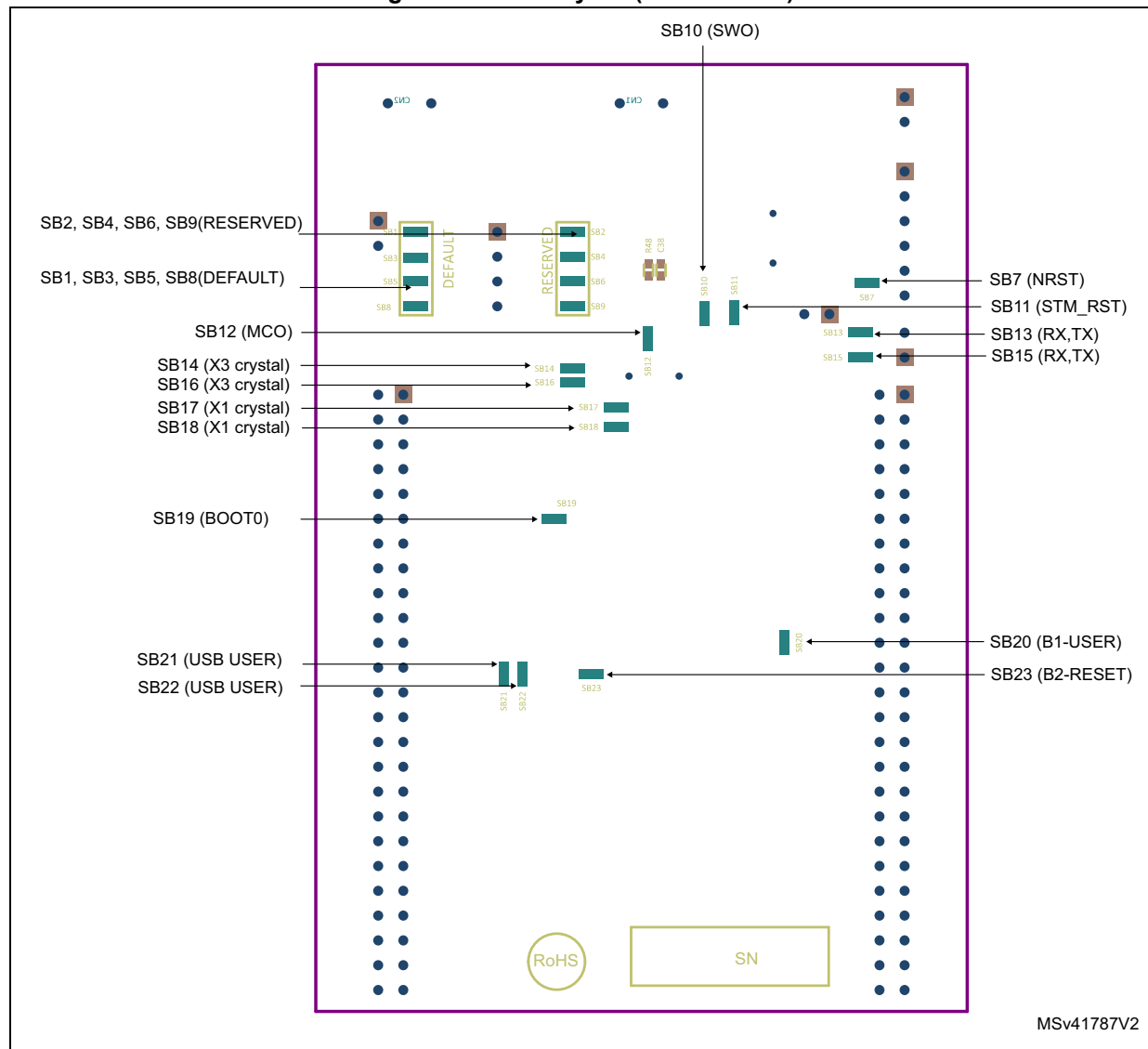


Figure 3. Board layout (top view)



Note: Pin 1 of CN3, CN4, JP3, JP4, P1 and P2 connectors are identified by a square.

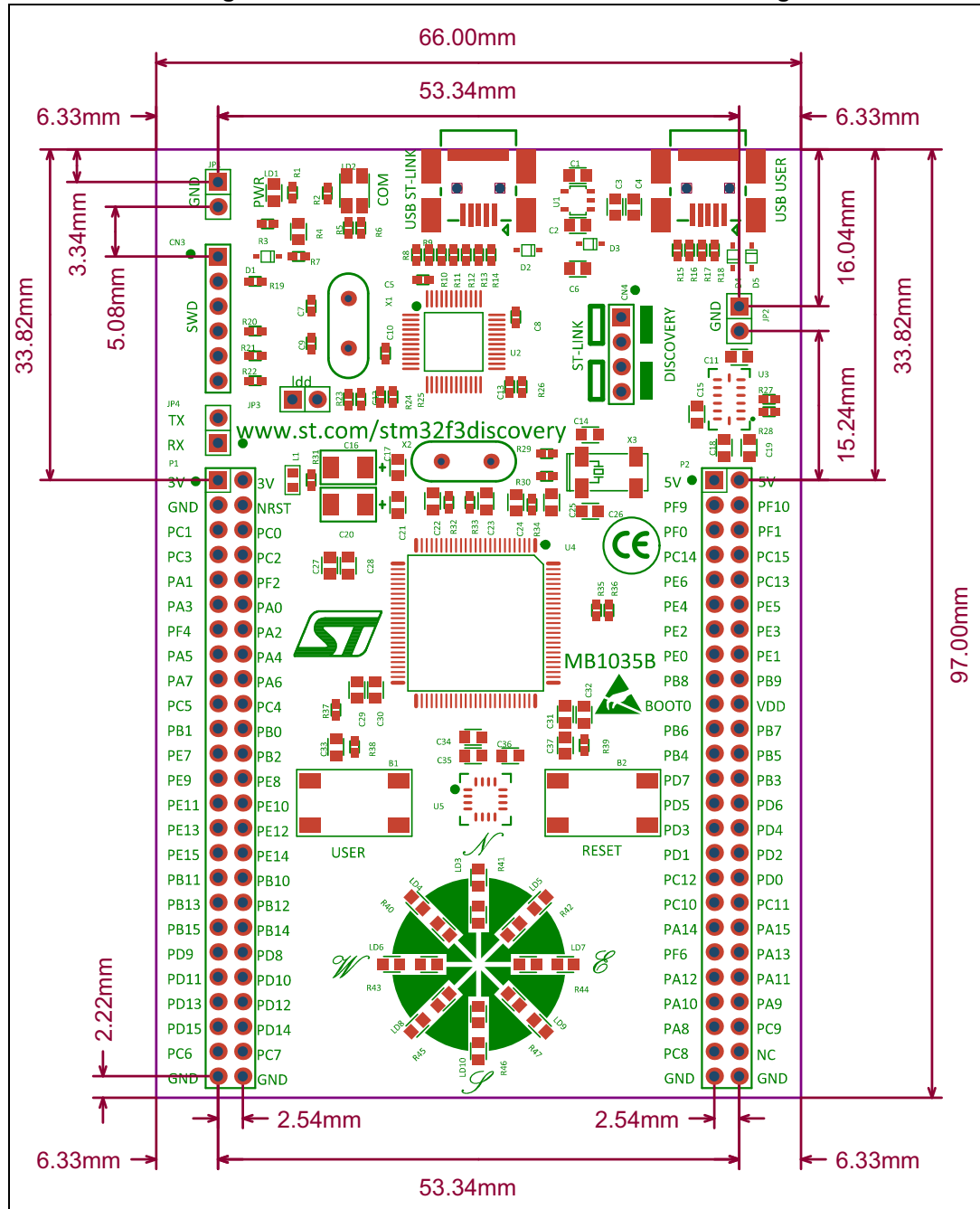
Figure 4. Board layout (bottom view)



**Note:** If SB12 is ON, SB17 must be OFF and R33 removed to provide the clock source from MCO correctly.

## 6.1 The STM32F3DISCOVERY Discovery board mechanical drawing

Figure 5. STM32F3DISCOVERY mechanical drawing



## 6.2 Embedded ST-LINK/V2 (or V2-B)

The ST-LINK/V2 on PCB version A or B or the ST-LINK/V2-B on PCB version C is embedded as programming and debugging tool. Virtual COM port and USB mass storage features are supported by the ST-LINK/V2-B only.

The embedded ST-LINK/V2 (or V2-B) supports only SWD for STM32 devices. For information about debugging and programming features refer to *ST-LINK/V2 in-circuit debugger/programmer for STM8 and STM32*, User manual (UM1075), which describes in detail all the ST-LINK/V2 features.

The changes on ST-LINK/V2-B versus ST-LINK/V2 version are listed below. New features supported on ST-LINK/V2-B are:

- Virtual Com port interface on USB
- Mass storage interface on USB

The embedded-on-board features not supported either on ST-LINK/V2 or on ST-LINK/V2-B are:

- SWIM interface
- Minimum supported application voltage limited to 3 V
- USB power management request for more than 100 mA power on USB

Known limitation:

activating the readout protection on the STM32 target, prevents the target application from running afterwards. The readout protection must be kept disabled on the STM32 target.

There are two different ways to use the embedded ST-LINK/V2 (or V2-B), depending on the jumper states (see [Table 3](#)):

- Programming/debugging the STM32 on board (refer to [Section 6.2.4: Using ST-LINK/V2 \(or V2-B\) to program/debug the STM32F303VCT6 on board](#))
- Programming/debugging the STM32 in an external application board, using a cable connected to SWD connector CN2 (refer to [Section 6.2.5: Using ST-LINK/V2 \(or V2-B\) to program/debug an external STM32 application](#))

**Table 3. Jumper states**

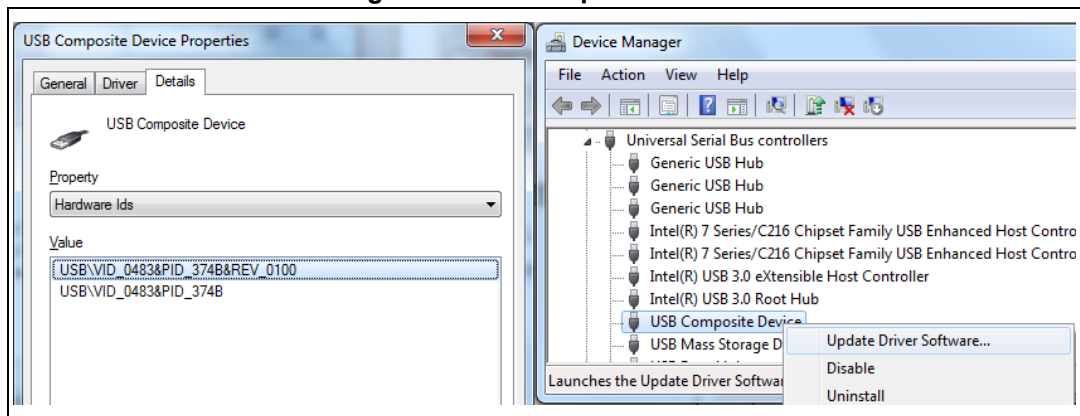
| Jumper state         | Description                                                                                           |
|----------------------|-------------------------------------------------------------------------------------------------------|
| Both CN4 jumpers ON  | ST-LINK/V2 (or V2-B) functions enabled for on-board programming (default)                             |
| Both CN4 jumpers OFF | ST-LINK/V2 (or V2-B) functions enabled for application through external CN3 connector (SWD supported) |

### 6.2.1 Drivers

The ST-LINK/V2 (or V2-B) requires a dedicated USB driver, which, for Windows® XP, 7, 8, can be found at the [www.st.com](http://www.st.com) website. In case the STM32 Discovery is connected to the PC before the driver is installed, some Discovery interfaces may be declared as "Unknown" in the PC device manager. In this case the user must install the driver files, and from the device manager he must update the driver of the connected device (see [Figure 6](#)).

*Note:* Prefer using the "USB Composite Device" handle for a full recovery.

Figure 6. USB composite device



### 6.2.2 ST-LINK/V2 (or V2-B) firmware upgrade

The ST-LINK/V2 (or V2-B) embeds a firmware upgrade mechanism for in-situ upgrade through the USB port. As the firmware may evolve during the life time of the ST-LINK/V2 (or V2-B) product (for example new functionalities, bug fixes, support for new microcontroller families), it is recommended to visit the [www.st.com](http://www.st.com) website, before starting to use the Discovery board and periodically, to stay up-to-date with the latest firmware version.

### 6.2.3 VCP configuration

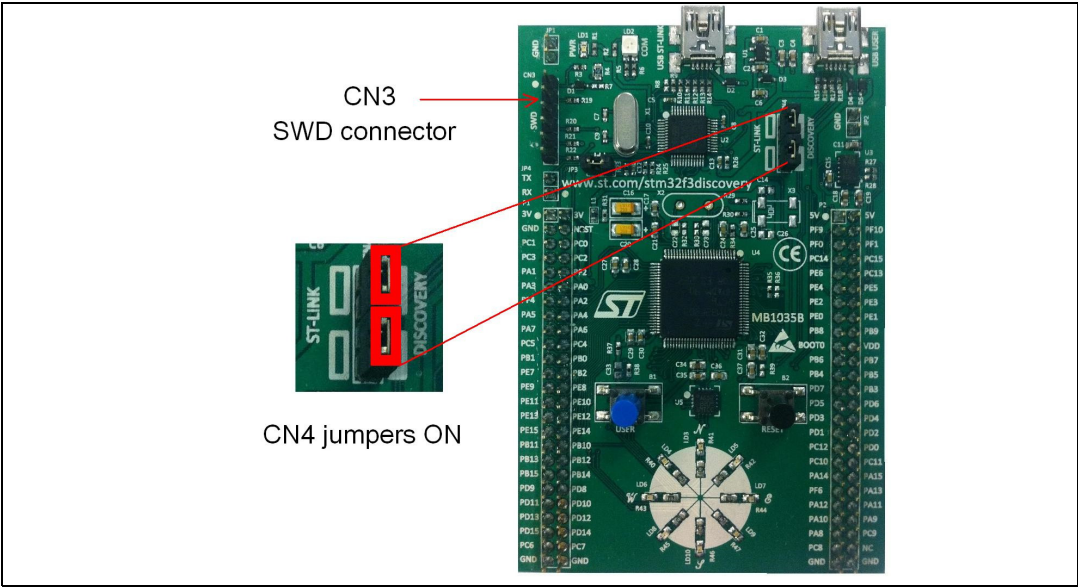
The ST-LINK/V2-B on STM32F3DISCOVERY supports virtual Com port (VCP) on U2 pin 12 (ST-LINK\_TX) and U2 pin 13 (ST-LINK\_RX), which are connected to the STM32F303 MCU target STM32 USART1 (PC4, PC5), thanks to SB13 and SB15 solder bridges.

The SB13 (PC4) and SB15 (PC5) default configurations for STM32F3DISCOVERY are given in [Table 5: Solder bridges](#).

### 6.2.4 Using ST-LINK/V2 (or V2-B) to program/debug the STM32F303VCT6 on board

To program the STM32F303VCT6 MCU on the board, simply plug in the two jumpers marked in red on CN4, as shown in [Figure 7: STM32F3DISCOVERY connections](#), but do not use the CN3 connector as that could disturb the communication with the STM32F303VCT6 of the STM32F3DISCOVERY.

Figure 7. STM32F3DISCOVERY connections



### 6.2.5 Using ST-LINK/V2 (or V2-B) to program/debug an external STM32 application

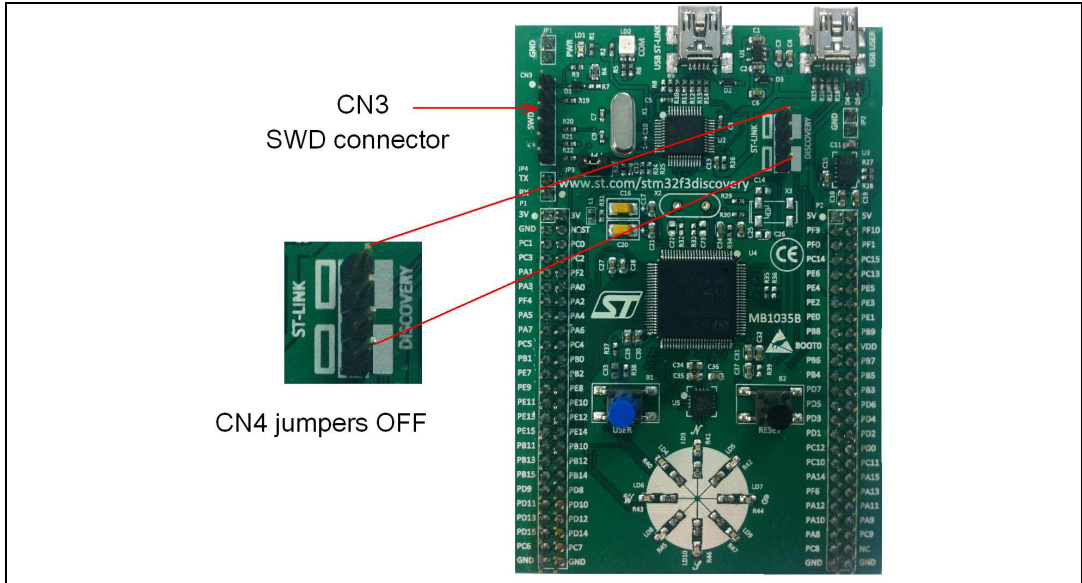
It is very easy to use the ST-LINK/V2 (or V2-B) to program the STM32 on an external application. Simply remove the two jumpers from CN4 as shown in [Figure 8: ST-LINK/V2 connections](#), and connect the application to the CN3 debug connector according to [Table 4](#).

*Note:* SB7 must be OFF if the CN3 pin 5 is used in an external application.

Table 4. Debug connector CN3 (SWD)

| Pin | CN3        | Designation           |
|-----|------------|-----------------------|
| 1   | VDD_TARGET | VDD from application  |
| 2   | SWCLK      | SWD clock             |
| 3   | GND        | Ground                |
| 4   | SWDIO      | SWD data input/output |
| 5   | NRST       | RESET of target MCU   |
| 6   | SWO        | Reserved              |

### Figure 8. ST-LINK/V2 connections



## 6.3 Power supply and power selection

The power supply is provided either by the host PC through the USB cable, or by an external 5 V power supply.

The D1 and D2 diodes protect the 5 V and 3 V pins from external power supplies.

- 5 V and 3 V can be used as output power supplies when another application board is connected to pins P1 and P2. In this case, the 5 V and 3 V pins deliver a 5 V or 3 V power supply and power consumption must be lower than 100 mA.
- 5 V and 3 V can also be used as input power supplies, for example when the USB connectors are not connected to the PC. In this case, the STM32F3DISCOVERY board must be powered by a power supply unit or by auxiliary equipment complying with the standard EN-60950-1: 2006+A11/2009, and must be Safety Extra Low Voltage (SELV) with limited power capability.

*Note:* The board can be also powered through the USB USER connector and it is protected by D4 and D5 diodes when both USBs are connected (in which case, the 5 V power is around 4.4 volts).

## 6.4 LEDs

- LD1 PWR: red LED indicates that the board is powered.
- LD2 COM: LD2 default status is red. LD2 turns to green to indicate that communications are in progress between the PC and the ST-LINK/V2.
- User LD3: red LED is a user LED connected to the I/O PE9 of the STM32F303VCT6.
- User LD4: blue LED is a user LED connected to the I/O PE8 of the STM32F303VCT6.
- User LD5: orange LED is a user LED connected to the I/O PE10 of the STM32F303VCT6.
- User LD6: green LED is a user LED connected to the I/O PE15 of the STM32F303VCT6.
- User LD7: green LED is a user LED connected to the I/O PE11 of the STM32F303VCT6.
- User LD8: orange LED is a user LED connected to the I/O PE14 of the STM32F303VCT6.
- User LD9: blue LED is a user LED connected to the I/O PE12 of the STM32F303VCT6.
- User LD10: red LED is a user LED connected to the I/O PE13 of the STM32F303VCT6.

## 6.5 Push-buttons

- B1 USER: user and wake-up button connected to the I/O PA0 of the STM32F303VCT6.
- B2 RESET: push-button connected to NRST is used to RESET the STM32F303VCT6.

## 6.6 USB device supported

The STM32F303VCT6 MCU is also used to drive the second USB Mini-B connector (USB USER), which allows the board to be used as a USB Device. The STM32F3DISCOVERY can then act as a USB joystick, mouse, or other similar device. If both USBs are connected, diodes D4 and D5 protect the board and use the power from the USB ST-LINK.

The board can be powered through this USB USER connector, then LED1 PWR lights, LED2 COM blinks and it can run an application in standalone mode.

## 6.7 ST MEMS E-compass (ST MEMS LSM303DLHC)

The LSM303DLHC is an ultra-compact low-power system-in-package featuring a 3D digital linear acceleration sensor and a 3D digital magnetic sensor. It includes a sensing element and an IC interface able to provide the measured acceleration to the external world through an I<sup>2</sup>C serial interface.

The LSM303DLHC has dynamically user-selectable full scales of  $\pm 2g/\pm 8g$  and is capable of measuring acceleration, and a magnetic field full scale from  $\pm 1.3g$  to  $8.1g$  with an output data rate of 100 Hz or 400 Hz.

The STM32F303VCT6 MCU controls this motion sensor through the I<sup>2</sup>C interface.

## 6.8 ST MEMS Gyroscope (ST MEMS L3GD20)

The L3GD20 is an ultra-compact, low-power, three-axis-angular-rate sensor. It includes a sensing element and an IC interface able to provide the measured angular rate to the external world through the I<sup>2</sup>C/SPI serial interface.

The L3GD20 has dynamically user-selectable full scales of  $\pm 250$  dps/ $\pm 500$  dps/ $\pm 2000$  dps and is capable of measuring rates.

The STM32F303VCT6 MCU controls this motion sensor through the SPI interface.

## 6.9 JP3 (Idd)

Jumper JP3, labeled Idd, allows the measurement of the consumption of the STM32F303VCT6 by removing the jumper and connecting an ammeter.

- Jumper on: STM32F303VCT6 is powered (default).
- Jumper off: an ammeter must be connected to measure the STM32F303VCT6 current. If there is no ammeter, the STM32F303VCT6 is not powered.

## 6.10 OSC clock

### 6.10.1 OSC clock supply

The following information indicates all configurations for clock supply selection:

- **MCO from ST-LINK.** From MCO of the STM32F103C8T6. This frequency cannot be changed, it is fixed at 8 MHz and connected to PF0-OSC\_IN of the STM32F303VCT6. Configuration needed:
  - SB12 closed, SB17 open, R33 removed
  - SB18, R32, C22, C23, X2 = do not care
- **Oscillator on board.** From X2 crystal (not provided). For typical frequencies and its capacitors and resistors, refer to the STM32F303VCT6 Datasheet. Configuration needed:
  - SB12, SB17, SB18 open
  - R32, R33, C22, C23, X2 soldered
- **Oscillator from external PF0.** From external oscillator through pin 5 of the P2 connector. Configuration needed:
  - SB12 open, SB17 closed, R33 removed
  - SB18, R32, C22, C23, X2 = do not care
- **No external oscillator.** From Internal oscillator HSI only. PF0 and PF1 can be used as GPIO. Configuration needed:
  - SB12 open, SB17 closed, SB18 closed, R32 removed, R33 removed
  - C22, C23, X2 = do not care

### 6.10.2 OSC 32 kHz clock supply

The following information indicates all the configurations for the 32 kHz clock supply selection:

- **Oscillator on board.** From X3 Crystal (not provided). Configuration needed:
  - SB14 open, SB16 open.
  - R29, R30, C14, C26, X3 soldered.
- **Oscillator from external PC14.** From external oscillator through the pin 7 of P2 connector. Configuration needed:
  - SB14 open, R29 removed
  - SB16, R30, C14, C26, X3 = do not care
- **No external oscillator.** PC14 and PC15 can be used as GPIO. Configuration needed:
  - SB14 closed, SB16 closed, R29 removed, R30 removed.
  - C14, C26, X3 = do not care.

## 6.11 Solder bridges

**Table 5. Solder bridges**

| Bridge                                 | State <sup>(1)</sup> | Description                                                                                                         |
|----------------------------------------|----------------------|---------------------------------------------------------------------------------------------------------------------|
| SB17,18<br>(X1 crystal) <sup>(2)</sup> | <b>OFF</b>           | X1, C22, C23, R32 and R33 provide a clock. PF0, PF1 are disconnected from P2.                                       |
|                                        | ON                   | PF0, PF1 are connected to P2 (R32 and R33 must not be fitted).                                                      |
| SB1,3,5,8 (default)                    | <b>ON</b>            | Reserved, do not modify                                                                                             |
| SB2,4,6,9 (reserved)                   | <b>OFF</b>           | Reserved, do not modify                                                                                             |
| SB14,16<br>(X3 crystal)                | <b>OFF</b>           | X3, C14, C26, R29 and R30 deliver a 32 KHz clock. PC14, PC15 are not connected to P2.                               |
|                                        | ON                   | PC14, PC15 are only connected to P2. Remove only R26, R29                                                           |
| SB23<br>(B2-RESET)                     | <b>ON</b>            | B2 push-button is connected to NRST of STM32F303VCT6                                                                |
|                                        | OFF                  | B2 push-button is not connected to NRST of STM32F303VCT6                                                            |
| SB20<br>(B1-USER)                      | <b>ON</b>            | B1 push-button is connected to PA0                                                                                  |
|                                        | OFF                  | B1 push-button is not connected to PA0                                                                              |
| SB13,15<br>(RX,TX)                     | OFF                  | STM32F303VCT6 USART1 is not connected to ST-LINK, so VCP is disabled (Default configuration on PCB version A or B). |
|                                        | <b>ON</b>            | STM32F303VCT6 USART1 is connected to ST-LINK, so VCP is enabled (default configuration on PCB version C and newer). |
| SB7<br>(NRST)                          | <b>ON</b>            | NRST signal of connector CN3 is connected to NRST of STM32F303VCT6                                                  |
|                                        | OFF                  | NRST signal is not connected                                                                                        |
| SB10<br>(SWO)                          | <b>ON</b>            | SWO signal of connector CN3 is connected to PB3                                                                     |
|                                        | OFF                  | SWO signal is not connected                                                                                         |
| SB11<br>(STM_RST)                      | <b>OFF</b>           | No incidence on NRST signal of STM32F103C8T6                                                                        |
|                                        | ON                   | NRST signal of STM32F103C8T6 is connected to GND                                                                    |
| SB19<br>(BOOT0)                        | <b>ON</b>            | BOOT0 signal of STM32F303VCT6 is at level "0" through 510 ohm pull-down                                             |
|                                        | OFF                  | BOOT0 signal of STM32F303VCT6 is at level "1" through 10K ohm pull-up (not provided).                               |
| SB21,22<br>(USB USER)                  | <b>OFF</b>           | PA11 and PA12 are only used for USB USER and not connected to P2 to avoid noise.                                    |
|                                        | ON                   | PA11 and PA12 are connected to P2.                                                                                  |
| SB12<br>(MCO) <sup>(2)</sup>           | <b>OFF</b>           | MCO signal of STM32F103C8T6 is not used.                                                                            |
|                                        | ON                   | MCO clock signal from STM32F103C8T6 is connected to OSC_IN of STM32F303VCT6.                                        |

1. Default state is shown in bold.

2. If SB12 is ON, SB17 must be OFF and R33 removed to provide the clock source from the MCO correctly.

## 6.12 Extension connectors

The male headers P1 and P2 can connect the STM32F3DISCOVERY to a standard prototyping/wrapping board. STM32F303VCT6 GPIOs are available on these connectors. P1 and P2 can also be probed by an oscilloscope, logical analyzer or voltmeter.

**Table 6. STM32F303VCT6 MCU pin description versus board function <sup>(1)</sup>**

| MCU pin       |                                                                 |                    | Board function |            |         |             |     |      |     |     |          |              |     |    |    |
|---------------|-----------------------------------------------------------------|--------------------|----------------|------------|---------|-------------|-----|------|-----|-----|----------|--------------|-----|----|----|
| Main function | Alternate function                                              | LQFP100 pin number | VCP            | LSM303DLHC | L3GD20  | Push-button | LED | SWD  | USB | OSC | Free I/O | Power supply | CN3 | P1 | P2 |
| BOOT0         | -                                                               | 94                 | -              | -          | -       | -           | -   | -    | -   | -   | -        | -            | -   | -  | 19 |
| NRST          | -                                                               | 14                 | -              | -          | -       | RESET       | -   | NRST | -   | -   | -        | -            | 5   | 4  | -  |
| PA0           | TIM2_CH1_ETR, G1_IO1, USART2_CTS, COMP1_OUT, TIM8_BKIN, TM8_ETR | 23                 | -              | -          | -       | USER        | -   | -    | -   | -   | -        | -            | -   | 12 | -  |
| PA1           | TIM2_CH2, G1_IO2, USART2_RTS, TIM15_CH1N                        | 24                 | -              | -          | -       | -           | -   | -    | -   | -   | -        | -            | -   | 9  | -  |
| PA2           | TIM2_CH3, G1_IO3, USART2_TX, COMP2_OUT, TIM15_CH1, AOP1_OUT     | 25                 | -              | -          | -       | -           | -   | -    | -   | -   | -        | -            | -   | 14 | -  |
| PA3           | TIM2_CH4, G1_IO4, USART2_RX, TIM15_CH2                          | 26                 | -              | -          | -       | -           | -   | -    | -   | -   | -        | -            | -   | 11 | -  |
| PA4           | TIM3_CH2, G2_IO1, SPI1_NSS, SPI3_NSS/I2S3_WS, USART2_CK         | 29                 | -              | -          | -       | -           | -   | -    | -   | -   | -        | -            | -   | 16 | -  |
| PA5           | TIM2_CH1_ETR, G2_IO2, SPI1_SCK                                  | 30                 | -              | -          | SCL/SPC | -           | -   | -    | -   | -   | -        | -            | -   | 15 | -  |

Table 6. STM32F303VCT6 MCU pin description versus board function (continued)<sup>(1)</sup>

| MCU pin       |                                                                                                     |                    | Board function |            |             |             |     |     |     |     |          |              |     |    |    |
|---------------|-----------------------------------------------------------------------------------------------------|--------------------|----------------|------------|-------------|-------------|-----|-----|-----|-----|----------|--------------|-----|----|----|
| Main function | Alternate function                                                                                  | LQFP100 pin number | VCP            | LSM303DLHC | L3GD20      | Push-button | LED | SWD | USB | OSC | Free I/O | Power supply | CN3 | P1 | P2 |
| PA6           | TIM16_CH1,<br>TIM3_CH1, G2_IO3,<br>TIM8_BKIN,<br>SPI1_MISO,<br>TIM1_BKIN,<br>AOP2_OUT,<br>COMP1_OUT | 31                 | -              | -          | SAO/SDO     | -           | -   | -   | -   | -   | -        | -            | -   | 18 | -  |
| PA7           | TIM17_CH1,<br>TIM3_CH2, G2_IO4,<br>TIM8_CH1N,<br>SPI1_MOSI,<br>TIM1_CH1N,<br>COMP2_OUT              | 32                 | -              | -          | SDA/SDI/SDO | -           | -   | -   | -   | -   | -        | -            | -   | 17 | -  |
| PA8           | MCO, I2C2_SMBAL,<br>I2S2_MCK,<br>TIM1_CH1,<br>USART1_CK,<br>COMP3_OUT,<br>TIM4_ETR                  | 67                 | -              | -          | -           | -           | -   | -   | -   | -   | -        | -            | -   | -  | 45 |
| PA9           | G4_IO1, I2C2_SCL,<br>I2S3_MCK,<br>TIM1_CH2,<br>USART1_TX,<br>COMP5_OUT,<br>TIM15_BKIN,<br>TIM2_CH3  | 68                 | -              | -          | -           | -           | -   | -   | -   | -   | -        | -            | -   | -  | 44 |
| PA10          | TIM17_BKIN,<br>G4_IO2, I2C2_SDA,<br>TIM1_CH3,<br>USART1_RX,<br>COMP6_OUT,<br>TIM2_CH4,<br>TIM8_BKIN | 69                 | -              | -          | -           | -           | -   | -   | -   | -   | -        | -            | -   | -  | 43 |

Table 6. STM32F303VCT6 MCU pin description versus board function (continued)<sup>(1)</sup>

| MCU pin       |                                                                                                               |                    | Board function |            |        |             |     |       |     |     |          |              |     |    |    |
|---------------|---------------------------------------------------------------------------------------------------------------|--------------------|----------------|------------|--------|-------------|-----|-------|-----|-----|----------|--------------|-----|----|----|
| Main function | Alternate function                                                                                            | LQFP100 pin number | VCP            | LSM303DLHC | L3GD20 | Push-button | LED | SWD   | USB | OSC | Free I/O | Power supply | CN3 | P1 | P2 |
| PA11          | TIM1_CH1N,<br>USART1_CTS,<br>COMP1_OUT,<br>CAN_RX,<br>TIM4_CH1,<br>TIM1_CH4_BKIN2,<br>USBDM                   | 70                 | -              | -          | -      | -           | -   | -     | DM  | -   | -        | -            | -   | -  | 42 |
| PA12          | TIM16_CH1,<br>TIM1_CH2N,<br>USART1_RTS,<br>COMP2_OUT,<br>CAN_TX,TIM4_CH2,<br>TIM1_ETR, USBDP                  | 71                 | -              | -          | -      | -           | -   | -     | DP  | -   | -        | -            | -   | -  | 41 |
| PA13          | JTMS-SWDAT,<br>TIM16_CH1N,<br>G4_IO3, IR-Out,<br>USART3_CTS,<br>TIM4_CH3                                      | 72                 | -              | -          | -      | -           | -   | SWDIO | -   | -   | -        | -            | 4   | -  | 40 |
| PA14          | JTCK-SWCLK,<br>G4_IO4, I2C1_SDA,<br>TIM8_CH2,<br>TIM1_BKIN,<br>USART2_TX                                      | 76                 | -              | -          | -      | -           | -   | SWCLK | -   | -   | -        | -            | 2   | -  | 37 |
| PA15          | JTDI,<br>TIM2_CH1_ETR,<br>TIM8_CH1,<br>I2C1_SCL,<br>SPI1_NSS,<br>SPI3_NSS/I2S3_WS,<br>USART2_RX,<br>TIM1_BKIN | 77                 | -              | -          | -      | -           | -   | -     | -   | -   | -        | -            | -   | -  | 38 |
| PB0           | TIM3_CH3, G3_IO2,<br>TIM8_CH2N,<br>TIM1_CH2N                                                                  | 35                 | -              | -          | -      | -           | -   | -     | -   | -   | -        | -            | -   | 22 | -  |

Table 6. STM32F303VCT6 MCU pin description versus board function (continued)<sup>(1)</sup>

| MCU pin       |                                                                                                       |                    | Board function |            |        |             |     |     |     |     |          |              |     |    |    |
|---------------|-------------------------------------------------------------------------------------------------------|--------------------|----------------|------------|--------|-------------|-----|-----|-----|-----|----------|--------------|-----|----|----|
| Main function | Alternate function                                                                                    | LQFP100 pin number | VCP            | LSM303DLHC | L3GD20 | Push-button | LED | SWD | USB | OSC | Free I/O | Power supply | CN3 | P1 | P2 |
| PB1           | TIM3_CH4, G3_IO3, TIM8_CH3N, TIM1_CH3N, COMP4_OUT, AOP3_OUT                                           | 36                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 21 | -  |
| PB2           | G3_IO4                                                                                                | 37                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 24 | -  |
| PB3           | JTDO/TRACESWO, TIM2_CH2, TIM4_ETR, G5_IO1, TIM8_CH1N, SPI1_SCK, SPI3_SCK/I2S3_CK, USART2_TX, TIM3_ETR | 89                 | -              | -          | -      | -           | -   | SWO | -   | -   | -        | -            | 6   | -  | 26 |
| PB4           | NJTRST, TIM16_CH1, TIM3_CH1, G5_IO2, TIM8_CH2N, SPI1_MISO, SPI3_MISO/I2S3_DIN,USART2_RX, TIM17_BKIN   | 90                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | -  | 23 |
| PB5           | TIM16_BKIN, TIM3_CH2, TIM8_CH3N, I2C1_SMBAL, SPI1_MOSI, SPI3_MOSI/I2S3_DOUT, USART2_CK, TIM17_CH1     | 91                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | -  | 24 |
| PB6           | TIM16_CH1N, TIM4_CH1, G5_IO3, I2C1_SCL, TIM8_CH1, TIM8_ETR_BKIN2, USART1_TX                           | 92                 | -              | SCL        | -      | -           | -   | -   | -   | -   | -        | -            | -   | -  | 21 |

Table 6. STM32F303VCT6 MCU pin description versus board function (continued)<sup>(1)</sup>

| MCU pin       |                                                                                                |                    | Board function |            |        |             |     |     |     |     |          |              |     |    |    |
|---------------|------------------------------------------------------------------------------------------------|--------------------|----------------|------------|--------|-------------|-----|-----|-----|-----|----------|--------------|-----|----|----|
| Main function | Alternate function                                                                             | LQFP100 pin number | VCP            | LSM303DLHC | L3GD20 | Push-button | LED | SWD | USB | OSC | Free I/O | Power supply | CN3 | P1 | P2 |
| PB7           | TIM17_CH1N,<br>TIM4_CH2, G5_IO4,<br>I2C1_SDA,<br>TIM8_BKIN,<br>USART1_RX,<br>TIM3_CH4          | 93                 | -              | SDA        | -      | -           | -   | -   | -   | -   | -        | -            | -   | -  | 22 |
| PB8           | TIM16_CH1,<br>TIM4_CH3, SYNCH,<br>I2C1_SCL,<br>COMP1_OUT,<br>CAN_RX,<br>TIM8_CH2,<br>TIM1_BKIN | 95                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | -  | 17 |
| PB9           | TIM17_CH1,<br>TIM4_CH4,<br>I2C1_SDA, IR-OUT,<br>CAN_TX, TIM8_CH3                               | 96                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | -  | 18 |
| PB10          | TIM2_CH3, SYNCH,<br>USART3_TX                                                                  | 47                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 34 | -  |
| PB11          | TIM2_CH4, G6_IO1,<br>USART3_RX,<br>COMP2_OUT                                                   | 48                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 33 | -  |
| PB12          | G6_IO2,<br>I2C2_SMBAL,<br>SPI2_NSS/I2S2_WS,<br>TIM1_BKIN,<br>USART3_CK,<br>AOP4_OUT            | 51                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 36 | -  |
| PB13          | G6_IO3,<br>SPI2_SCK/I2S2_CK,<br>TIM1_CH1N,<br>USART3_CTS                                       | 52                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 35 | -  |

Table 6. STM32F303VCT6 MCU pin description versus board function (continued)<sup>(1)</sup>

| MCU pin       |                                                                      |                    | Board function |            |        |             |     |     |     |     |          |              |     |    |    |
|---------------|----------------------------------------------------------------------|--------------------|----------------|------------|--------|-------------|-----|-----|-----|-----|----------|--------------|-----|----|----|
| Main function | Alternate function                                                   | LQFP100 pin number | VCP            | LSM303DLHC | L3GD20 | Push-button | LED | SWD | USB | OSC | Free I/O | Power supply | CN3 | P1 | P2 |
| PB14          | TIM15_CH1,<br>G6_IO4,<br>SPI2_MISO/I2S2_DIN,TIM1_CH2N,<br>USART3_RTS | 53                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 38 | -  |
| PB15          | TIM15_CH2,<br>TIM15_CH1N,<br>TIM1_CH3N,<br>SPI2_MOSI/<br>I2S2_DOUT   | 54                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 37 | -  |
| PC0           | -                                                                    | 15                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 6  | -  |
| PC1           | -                                                                    | 16                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 5  | -  |
| PC2           | COMP7_OUT                                                            | 17                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 8  | -  |
| PC3           | TIM1_BKIN2                                                           | 18                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 7  | -  |
| PC4           | USART1_TX                                                            | 33                 | USART1_TX      | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 20 | -  |
| PC5           | G3_IO1,<br>USART1_RX                                                 | 34                 | USART1_RX      | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 19 | -  |
| PC6           | TIM3_CH1,<br>TIM8_CH1,<br>I2S2_MCK,<br>COMP6_OUT                     | 63                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 47 | -  |
| PC7           | TIM3_CH2,<br>TIM8_CH2,<br>I2S3_MCK,<br>COMP5_OUT                     | 64                 | -              | -          | -      | -           | -   | -   | -   | -   | -        | -            | -   | 48 | -  |

Table 6. STM32F303VCT6 MCU pin description versus board function (continued)<sup>(1)</sup>

| MCU pin       |                                                              |                    | Board function |            |        |             |     |     |     |           |          |              |     |    |    |
|---------------|--------------------------------------------------------------|--------------------|----------------|------------|--------|-------------|-----|-----|-----|-----------|----------|--------------|-----|----|----|
| Main function | Alternate function                                           | LQFP100 pin number | VCP            | LSM303DLHC | L3GD20 | Push-button | LED | SWD | USB | OSC       | Free I/O | Power supply | CN3 | P1 | P2 |
| PC8           | TIM3_CH3,<br>TIM8_CH3,<br>COMP3_OUT                          | 65                 | -              | -          | -      | -           | -   | -   | -   | -         | -        | -            | -   | -  | 47 |
| PC9           | TIM3_CH4,<br>TIM8_CH4_BKIN2,<br>COMP3_OUT                    | 66                 | -              | -          | -      | -           | -   | -   | -   | -         | -        | -            | -   | -  | 46 |
| PC10          | TIM8_CH1N,<br>UART4_TX,<br>SPI3_SCK/I2S3_CK,<br>USART3_TX    | 78                 | -              | -          | -      | -           | -   | -   | -   | -         | -        | -            | -   | -  | 35 |
| PC11          | TIM8_CH2N,<br>UART4_RX,<br>SPI3_MISO/I2S3_DIN,USART3_RX      | 79                 | -              | -          | -      | -           | -   | -   | -   | -         | -        | -            | -   | -  | 36 |
| PC12          | TIM8_CH3N,<br>UART5_TX,<br>SPI3_MOSI/I2S3_DOUT,<br>USART3_CK | 80                 | -              | -          | -      | -           | -   | -   | -   | -         | -        | -            | -   | -  | 33 |
| PC13          | TIM1_CH1N                                                    | 7                  | -              | -          | -      | -           | -   | -   | -   | -         | -        | -            | -   | -  | 10 |
| PC14          | OSC32_IN                                                     | 8                  | -              | -          | -      | -           | -   | -   | -   | OSC32_IN  | -        | -            | -   | -  | 7  |
| PC15          | OSC32_OUT                                                    | 9                  | -              | -          | -      | -           | -   | -   | -   | OSC32_OUT | -        | -            | -   | -  | 8  |
| PD0           | CAN_RX                                                       | 81                 | -              | -          | -      | -           | -   | -   | -   | -         | -        | -            | -   | -  | 34 |
| PD1           | TIM8_CH4_BKIN2,<br>CAN_TX                                    | 82                 | -              | -          | -      | -           | -   | -   | -   | -         | -        | -            | -   | -  | 31 |

Table 6. STM32F303VCT6 MCU pin description versus board function (continued)<sup>(1)</sup>

| MCU pin       |                                      |                    | Board function |            |           |             |     |     |     |     |          |              |     |    |    |
|---------------|--------------------------------------|--------------------|----------------|------------|-----------|-------------|-----|-----|-----|-----|----------|--------------|-----|----|----|
| Main function | Alternate function                   | LQFP100 pin number | VCP            | LSM303DLHC | L3GD20    | Push-button | LED | SWD | USB | OSC | Free I/O | Power supply | CN3 | P1 | P2 |
| PD2           | TIM3_ETR,<br>TIM8_BKIN,<br>UART5_RX  | 83                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | -  | 32 |
| PD3           | TIM2_CH1_ETR,<br>USART2_CTS          | 84                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | -  | 29 |
| PD4           | TIM2_CH2,<br>USART2_RTS              | 85                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | -  | 30 |
| PD5           | USRT2_TX                             | 86                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | -  | 27 |
| PD6           | TIM2_CH4,<br>USART2_RX               | 87                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | -  | 28 |
| PD7           | TIM2_CH3,<br>USART2_CK               | 88                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | -  | 25 |
| PD8           | USART3_TX                            | 55                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | 40 | -  |
| PD9           | USART3_RX                            | 56                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | 39 | -  |
| PD10          | USART3_CK                            | 57                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | 42 | -  |
| PD11          | USART3_CTS                           | 58                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | 41 | -  |
| PD12          | TIM4_CH1, G8_IO1,<br>USART3_RTS      | 59                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | 44 | -  |
| PD13          | TIM4_CH2, G8_IO2                     | 60                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | 43 | -  |
| PD14          | TIM4_CH3, G8_IO3                     | 61                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | 46 | -  |
| PD15          | TIM4_CH4, G8_IO4,<br>SPI2_NSS        | 62                 | -              | -          | -         | -           | -   | -   | -   | -   | -        | -            | -   | 45 | -  |
| PE0           | TIM4_ETR,<br>TIM16_CH1,<br>USART1_TX | 97                 | -              | -          | INT1      | -           | -   | -   | -   | -   | -        | -            | -   | -  | 15 |
| PE1           | TIM17_CH1,<br>USART1_RX              | 98                 | -              | -          | DRDY/INT2 | -           | -   | -   | -   | -   | -        | -            | -   | -  | 16 |

Table 6. STM32F303VCT6 MCU pin description versus board function (continued)<sup>(1)</sup>

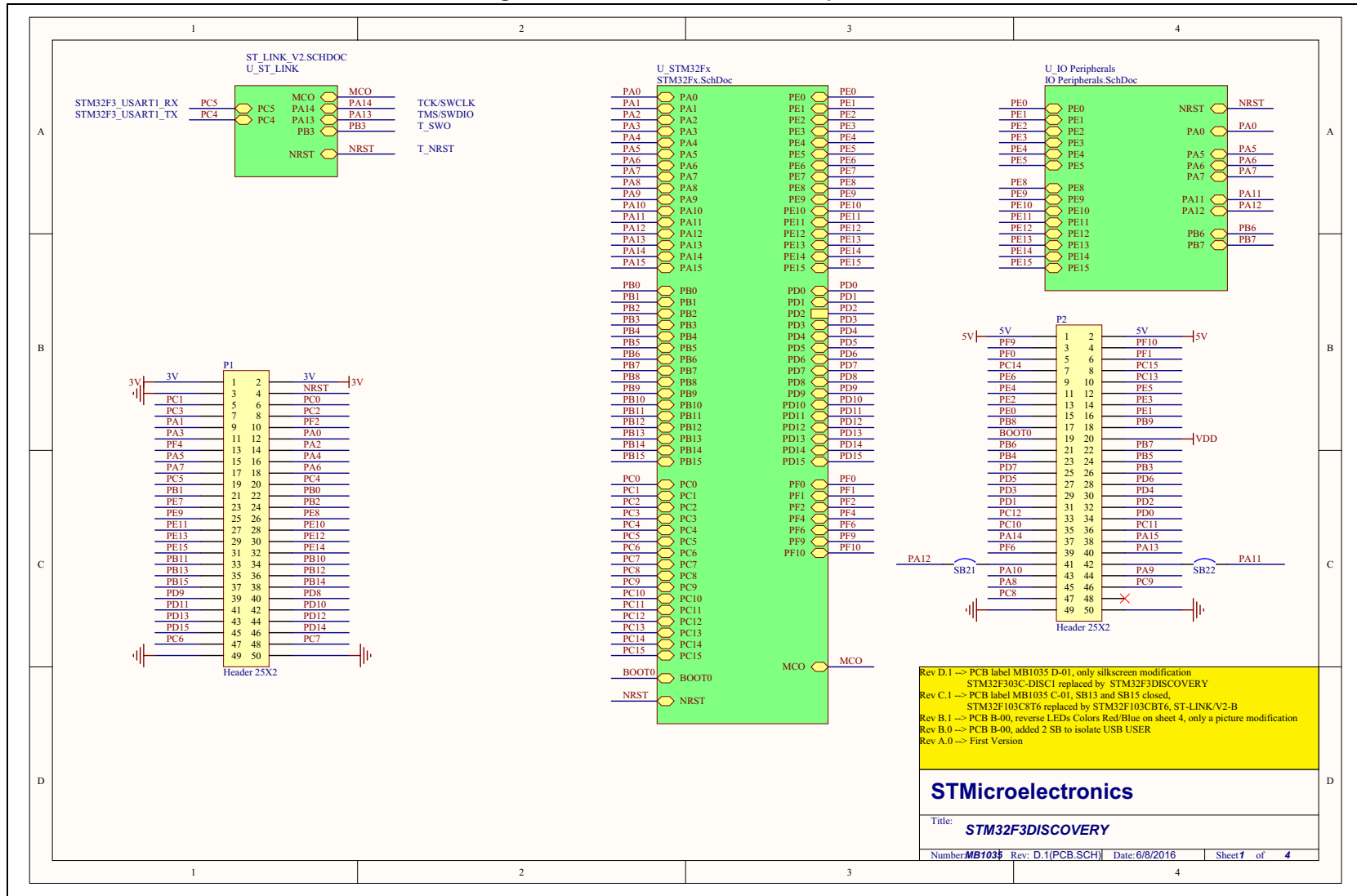
| MCU pin       |                           |                    | Board function |            |            |             |                |     |     |     |          |              |     |    |    |
|---------------|---------------------------|--------------------|----------------|------------|------------|-------------|----------------|-----|-----|-----|----------|--------------|-----|----|----|
| Main function | Alternate function        | LQFP100 pin number | VCP            | LSM303DLHC | L3GD20     | Push-button | LED            | SWD | USB | OSC | Free I/O | Power supply | CN3 | P1 | P2 |
| PE2           | TRACECK, TIM3_CH1, G7_IO1 | 1                  | -              | DRDY       | -          | -           | -              | -   | -   | -   | -        | -            | -   | -  | 13 |
| PE3           | TRACED0, TIM3_CH2, G7_IO2 | 2                  | -              | -          | CS_I2C/SPI | -           | -              | -   | -   | -   | -        | -            | -   | -  | 14 |
| PE4           | TRACED1, TIM3_CH3, G7_IO3 | 3                  | -              | INT1       | -          | -           | -              | -   | -   | -   | -        | -            | -   | -  | 11 |
| PE5           | TRACED2, TIM3_CH4, G7_IO4 | 4                  | -              | INT2       | -          | -           | -              | -   | -   | -   | -        | -            | -   | -  | 12 |
| PE6           | TRACED3                   | 5                  | -              | -          | -          | -           | -              | -   | -   | -   | -        | -            | -   | -  | 9  |
| PE7           | TIM1_ETR                  | 38                 | -              | -          | -          | -           | -              | -   | -   | -   | -        | -            | -   | 23 | -  |
| PE8           | TIM1_CH1N                 | 39                 | -              | -          | -          | -           | LD4/<br>BLUE   | -   | -   | -   | -        | -            | -   | 26 | -  |
| PE9           | TIM1_CH1                  | 40                 | -              | -          | -          | -           | LD3/<br>RED    | -   | -   | -   | -        | -            | -   | 25 | -  |
| PE10          | TIM1_CH2N                 | 41                 | -              | -          | -          | -           | LD5/<br>ORANGE | -   | -   | -   | -        | -            | -   | 28 | -  |
| PE11          | TIM1_CH2                  | 42                 | -              | -          | -          | -           | LD7/<br>GREEN  | -   | -   | -   | -        | -            | -   | 27 | -  |
| PE12          | TIM1_CH3N                 | 43                 | -              | -          | -          | -           | LD9/<br>BLUE   | -   | -   | -   | -        | -            | -   | 30 | -  |
| PE13          | TIM1_CH3                  | 44                 | -              | -          | -          | -           | LD10/<br>RED   | -   | -   | -   | -        | -            | -   | 29 | -  |
| PE14          | TIM1_CH4_BKIN2            | 45                 | -              | -          | -          | -           | LD8/<br>ORANGE | -   | -   | -   | -        | -            | -   | 32 | -  |

Table 6. STM32F303VCT6 MCU pin description versus board function (continued)<sup>(1)</sup>

| MCU pin       |                                |                    | Board function |            |        |             |               |     |     |         |          |              |     |    |    |
|---------------|--------------------------------|--------------------|----------------|------------|--------|-------------|---------------|-----|-----|---------|----------|--------------|-----|----|----|
| Main function | Alternate function             | LQFP100 pin number | VCP            | LSM303DLHC | L3GD20 | Push-button | LED           | SWD | USB | OSC     | Free I/O | Power supply | CN3 | P1 | P2 |
| PE15          | TIM1_BKIN, USART3_RX           | 46                 | -              | -          | -      | -           | LD6/<br>GREEN | -   | -   | -       | -        | -            | -   | 31 | -  |
| PF0           | OSC_IN, I2C2_SDA, TIM1_CH3N    | 12                 | -              | -          | -      | -           | -             | -   | -   | OSC_IN  | -        | -            | -   | -  | 5  |
| PF1           | OSC_OUT, I2C2_SCL              | 13                 | -              | -          | -      | -           | -             | -   | -   | OSC_OUT | -        | -            | -   | -  | 6  |
| PF2           | -                              | 19                 | -              | -          | -      | -           | -             | -   | -   | -       | -        | -            | -   | 10 | -  |
| PF4           | COMP1_OUT                      | 27                 | -              | -          | -      | -           | -             | -   | -   | -       | -        | -            | -   | 13 | -  |
| PF6           | TIM4_CH4, I2C2_SCL, USART3_RTS | 73                 | -              | -          | -      | -           | -             | -   | -   | -       | -        | -            | -   | -  | 39 |
| PF9           | TIM15_CH1, SPI2_SCK            | 10                 | -              | -          | -      | -           | -             | -   | -   | -       | -        | -            | -   | -  | 3  |
| PF10          | TIM15_CH2, SPI2_SCK            | 11                 | -              | -          | -      | -           | -             | -   | -   | -       | -        | -            | -   | -  | 4  |
| -             | -                              | -                  | -              | -          | -      | -           | -             | -   | -   | -       | -        | 5V           | -   | -  | 1  |
| -             | -                              | -                  | -              | -          | -      | -           | -             | -   | -   | -       | -        | 5V           | -   | -  | 2  |
| -             | -                              | -                  | -              | -          | -      | -           | -             | -   | -   | -       | -        | 3V           | -   | 1  | -  |
| -             | -                              | -                  | -              | -          | -      | -           | -             | -   | -   | -       | -        | 3V           | -   | 2  | -  |
| -             | -                              | -                  | -              | -          | -      | -           | -             | -   | -   | -       | -        | VDD          | -   | -  | 20 |
| -             | -                              | -                  | -              | -          | -      | -           | -             | -   | -   | -       | -        | GND          | 3   | -  | -  |
| -             | -                              | -                  | -              | -          | -      | -           | -             | -   | -   | -       | -        | GND          | -   | 49 | 49 |
| -             | -                              | -                  | -              | -          | -      | -           | -             | -   | -   | -       | -        | GND          | -   | 50 | 50 |

1. The cells marked in grey identify connections between MCU pin function and the board function.

Figure 9. STM32F3DISCOVERY top level



### Figure 10. STM32F3DISCOVERY ST-LINK/V2 (SWD only)

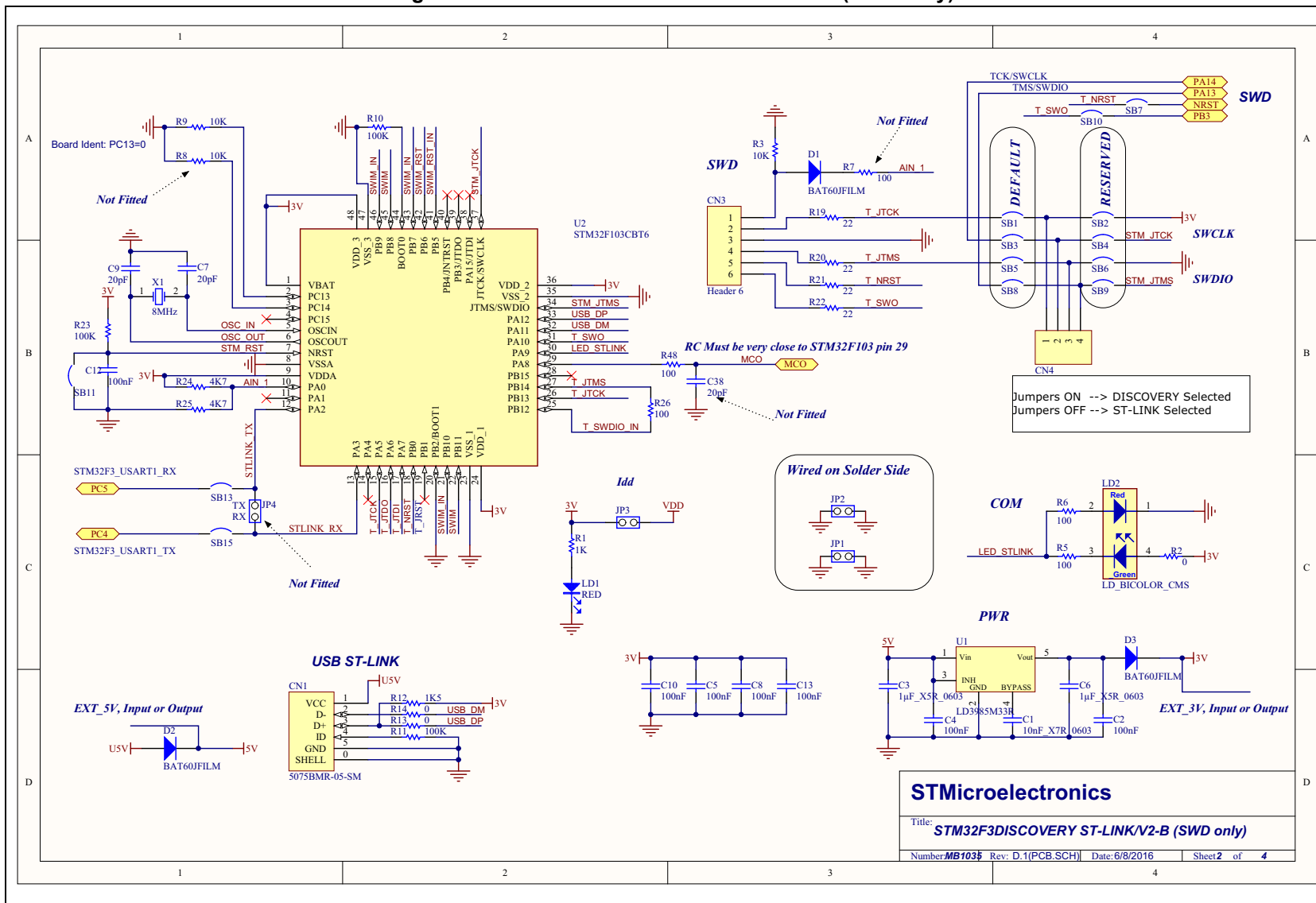




Figure 11. STM32F3DISCOVERY STM32F303VCT6 MCU

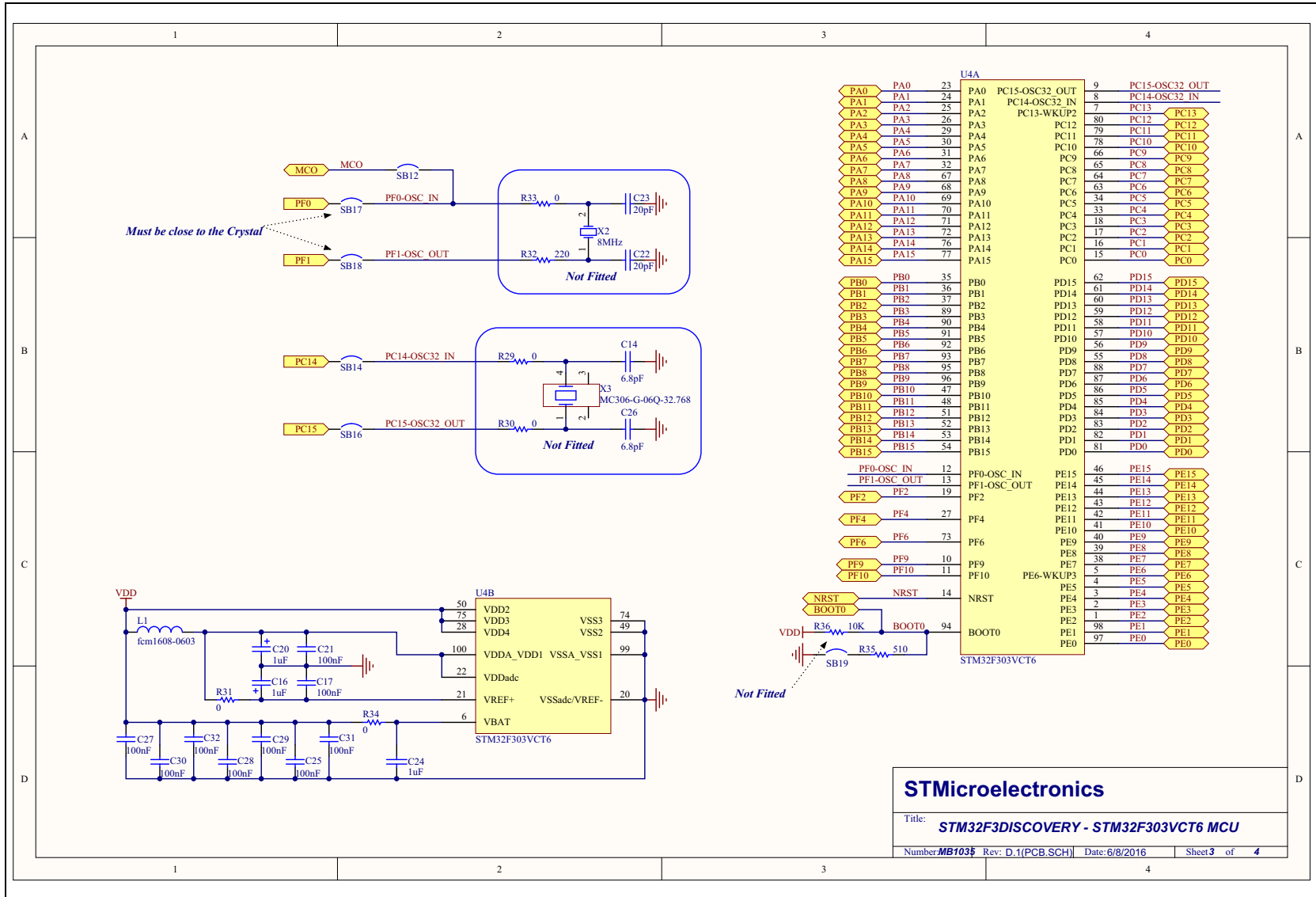
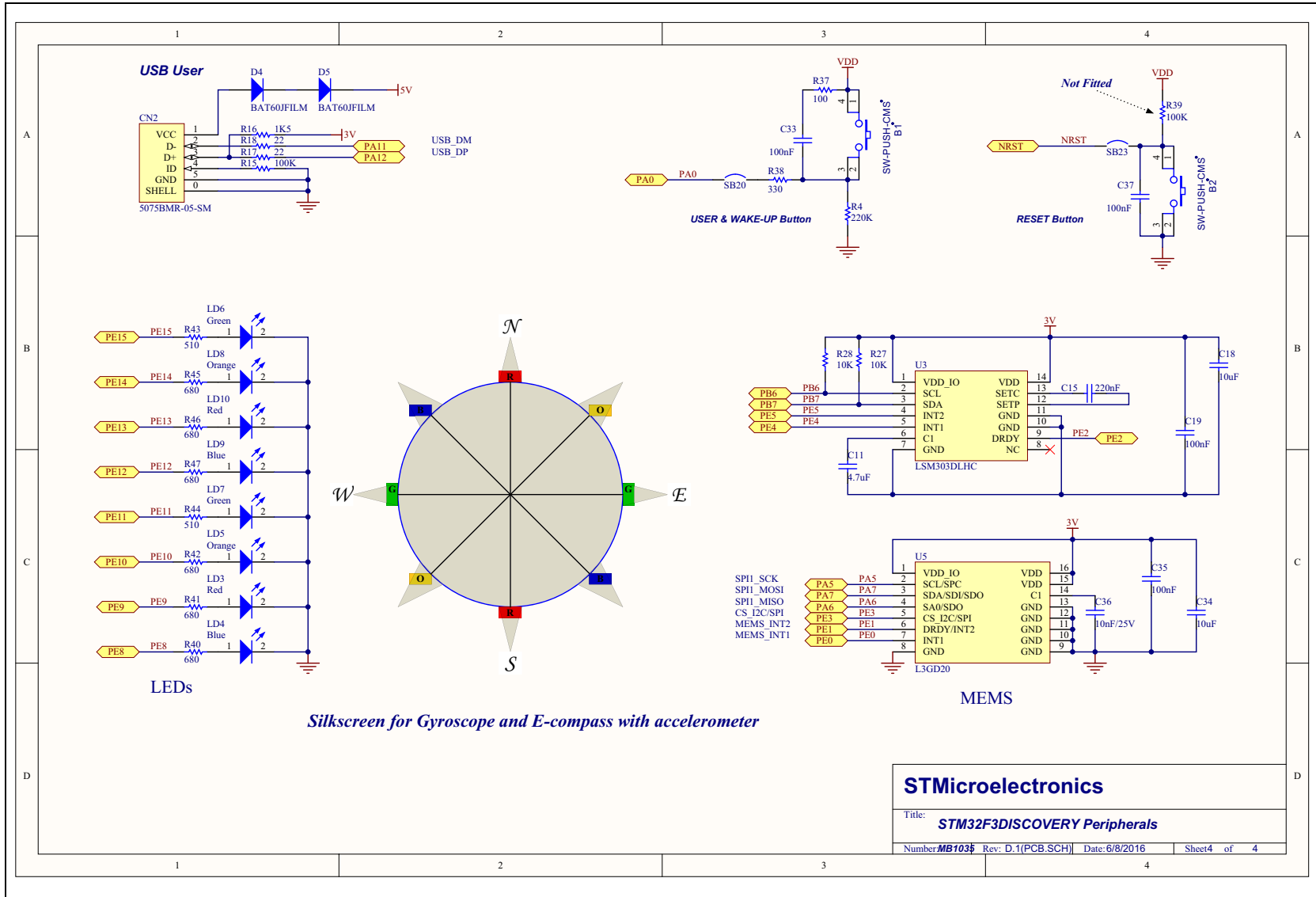


Figure 12. STM32F3DISCOVERY Peripherals



## 8 Revision history

**Table 7. Document revision history**

| Date        | Revision | Changes                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
|-------------|----------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 10-Sep-2012 | 1        | Initial release.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| 20-Sep-2012 | 2        | Added <a href="#">Figure 7 on page 11</a> and updated <a href="#">Figure 12 on page 35</a>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| 07-Feb-2013 | 3        | Updated <a href="#">Section 6.2: Embedded ST-LINK/V2 (or V2-B)</a> : removed WLCSP package, Sigma Delta ADC, CEC, updated timers and SPI/I <sup>2</sup> Ss.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| 21-Jul-2016 | 4        | Updated to introduce the information that boards come with ST-LINK/V2 or ST-LINK/V2-B.<br>Updated <a href="#">Section 1: Features</a> , <a href="#">Section 3: Ordering information</a> , <a href="#">Section 6.2.4: Using ST-LINK/V2 (or V2-B) to program/debug the STM32F303VCT6 on board</a> , <a href="#">Section 6.2.5: Using ST-LINK/V2 (or V2-B) to program/debug an external STM32 application</a> , <a href="#">Table 5: Solder bridges</a> , <a href="#">Table 6: STM32F303VCT6 MCU pin description versus board function</a> .<br>Added <a href="#">Section 2: Product marking</a> , <a href="#">Section 6.2.1: Drivers</a> , <a href="#">Section 6.2.2: ST-LINK/V2 (or V2-B) firmware upgrade</a> , <a href="#">Section 6.2.3: VCP configuration</a> . |
| 13-Apr-2018 | 5        | Corrected the VCP pin connection of the MCU from incorrect PA9&PA10 to PC4&PC5 in <a href="#">Section 6.2.3: VCP configuration</a> and <a href="#">Table 6: STM32F303VCT6 MCU pin description versus board function</a> .                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |

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