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RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SH7-A883A/E	Rev.	1.00
Title	Correction of errors regarding the SRAM Interface on Internal Bus of the Bus State Controller (BSC)		Information Category	Technical Notification		
Applicable Product	• SH7214 Group • SH7216 Group	Lot No. All lots	Reference Document	• SH7214 Group, SH7216 Group User's Manual: Hardware Rev.4.00 (R01UH0230EJ0400)		

We would like to inform you of the correction of errors in the above listed user's manuals. Please refer to the following for details.

<Corrections>

9.5.8 SRAM Interface with Byte Selection

Erroneous:

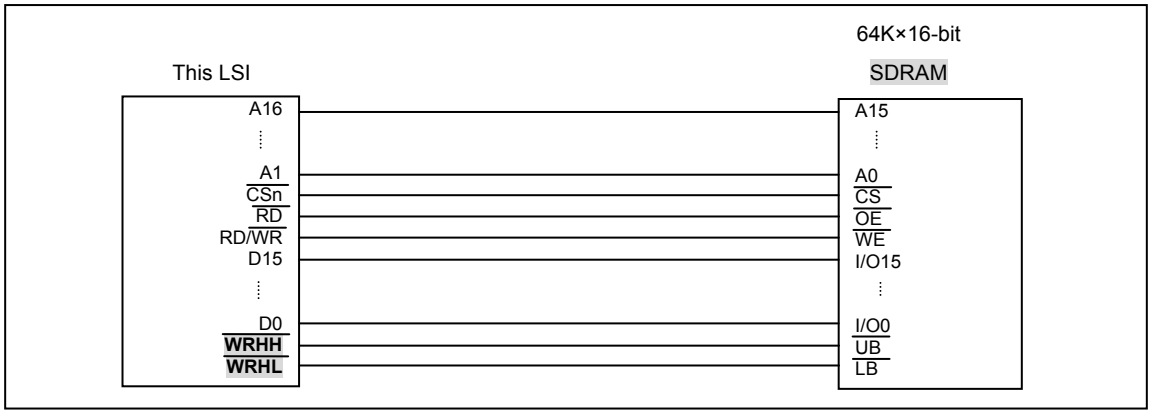

Figure 9.41 Example of Connection with 16-Bit Data-Width SRAM with Byte Selection

Corrected:

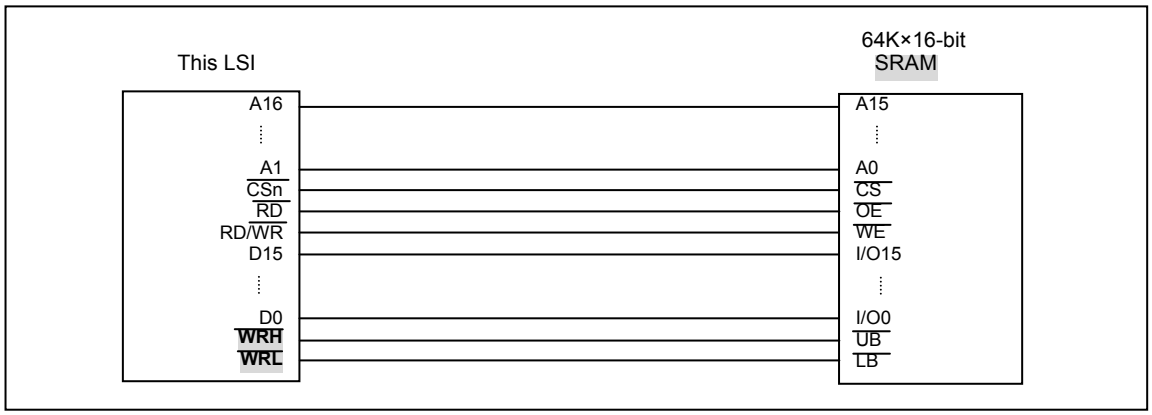

Figure 9.41 Example of Connection with 16-Bit Data-Width SRAM with Byte Selection

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