Sealed Choke Coil SDED20161T type

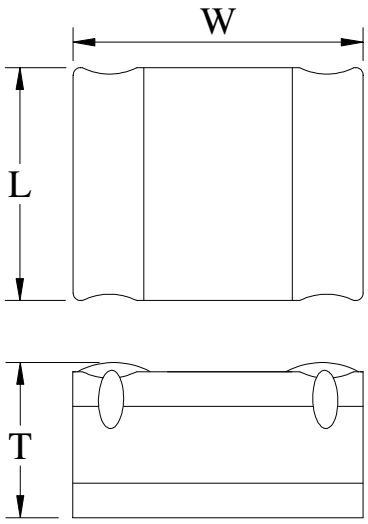
Features

Low profile：2.0 mm x 1.6 mm x 1.0 mm
Low coil resistance with large currents.
High magnetic shield construction should actualize high resolution for EMC protection.
100% lead (Pb) free meet RoHS standard

Application

Cellular phones, LCD displays, HDDs, DVCs, DSCs, PDAs etc..

Outline Dimensions

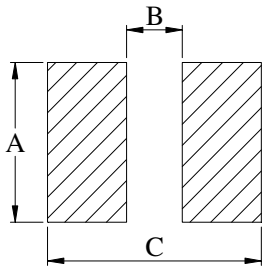


Code	Dimensions(mm)
L	1.6 ± 0.1
W	2.0 ± 0.1
T	1.0 Max.

Note ： This graph is in regard to outline dimensions spec. For outer appearance, please refer to actual product.

Recommend Land Pattern Dimensions

The customer shall determine the land dimensions shown below after confirming and safety.



A	1.7
B	0.6
C	2.3

Unit : mm

Specifications

Part Number	L0 Inductance (μH) @ (0A)	R_{dc} (mΩ)		Heat Rating Current DC Amps. Idc (A)		Saturation Current DC Amps. Isat (A)	
		Typical	Maximum	Typical	Maximum	Typical	Maximum
SDED20161T-R24MS	0.24	26.5	34.5	4.00	3.60	3.83	3.42
SDED20161T-R47MS	0.47	49	59	2.60	2.34	3.00	2.70
SDED20161T-R68MS	0.68	67	81	2.30	2.05	2.30	2.05
SDED20161T-1R0MS	1.0	87	107	1.70	1.50	2.00	1.80
SDED20161T-1R5MS	1.5	137	164	1.60	1.44	1.65	1.50
SDED20161T-2R2MS	2.2	192	230	1.35	1.22	1.45	1.31
SDED20161T-3R3MS	3.3	243	292	1.05	0.95	1.05	0.95
SDED20161T-4R7MS	4.7	322	387	0.95	0.85	0.95	0.80
SDED20161T-6R8MS	6.8	610	732	0.62	0.56	0.80	0.72
SDED20161T-100MS	10.0	932	1,119	0.47	0.42	0.62	0.55
SDED20161T-150MS	15.0	1,580	1,895	0.42	0.38	0.50	0.45
SDED20161T-220MS	22.0	2,365	2,838	0.37	0.33	0.45	0.40

*：If you require another part number please contact with us.

**：Inductance Tolerance ± 20%

Note 1.： All test data is referenced to 25℃ ambient.

Note 2.： Test Condition:1MHz, 1.0Vrms

Note 3.： Idc : DC current (A) that will cause an approximate △T of 40℃

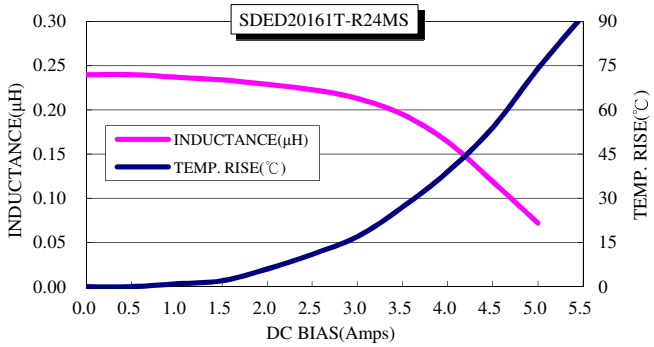
Note 4.： Isat : DC current (A) that will cause L0 to drop approximately 30%

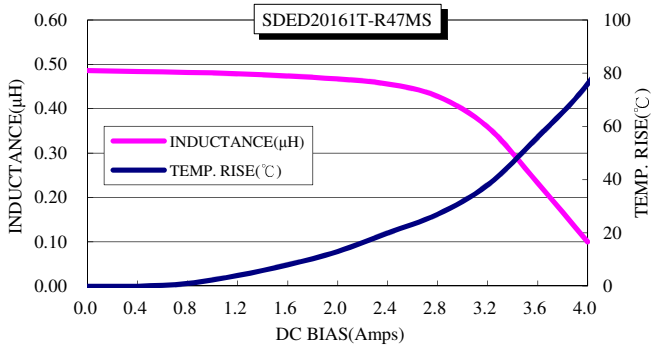
Note 5.： Operating Temperature Range -55℃ to + 125℃

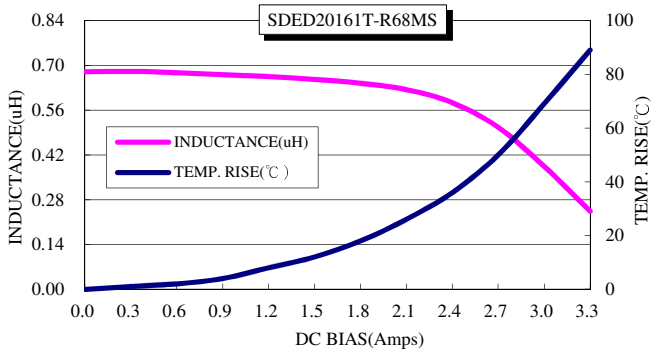
Note 6.： The part temperature (ambient + temp rise) should not exceed 125℃ under the worst case operating conditions. Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provision all affect the part temperature. Part temperature should be verified in the end application.

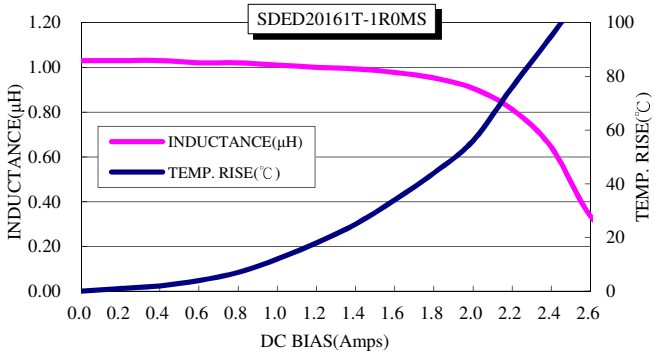
Note 7.： The rated current as listed is either the saturation current or the heating current depending on which value is lower.

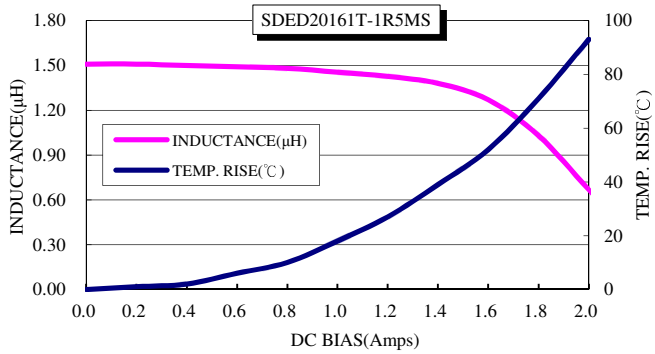
Current Characteristic

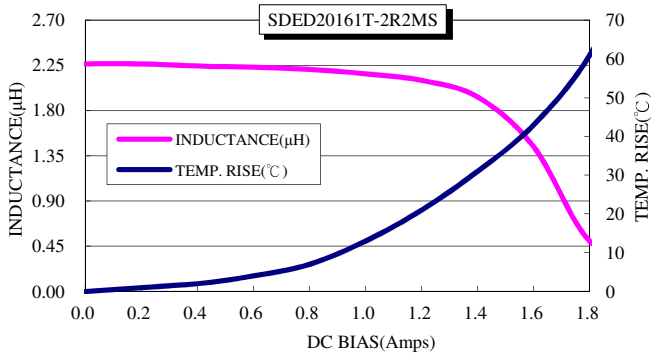


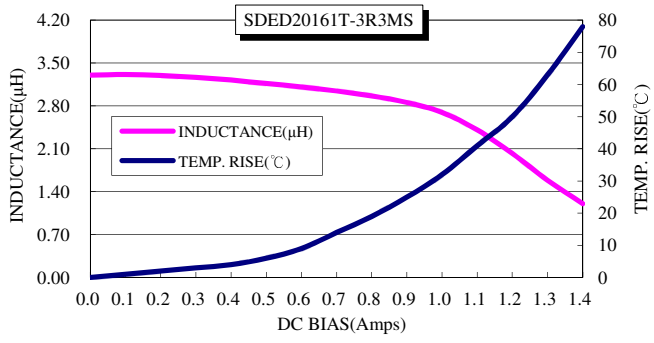


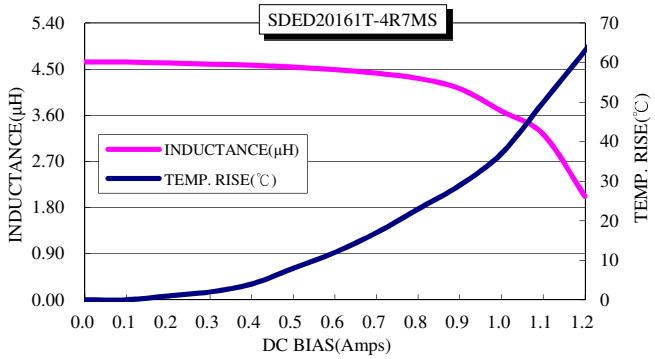


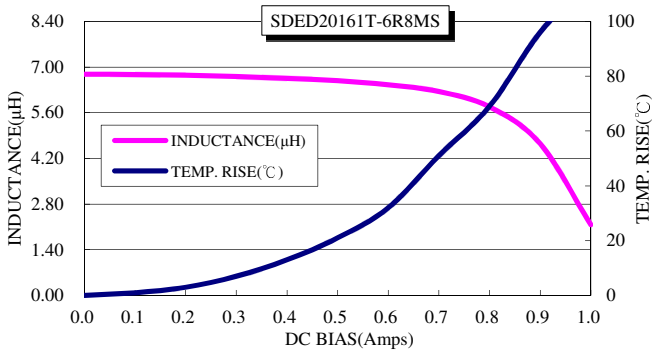


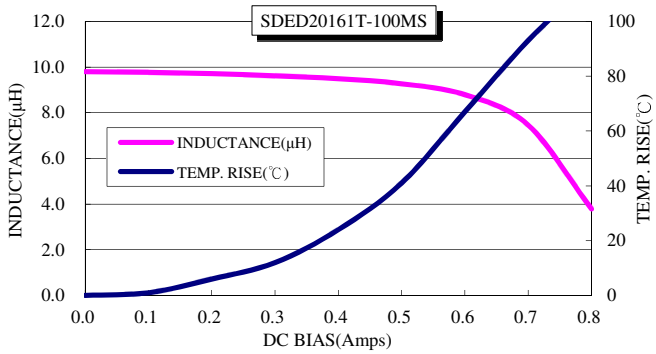


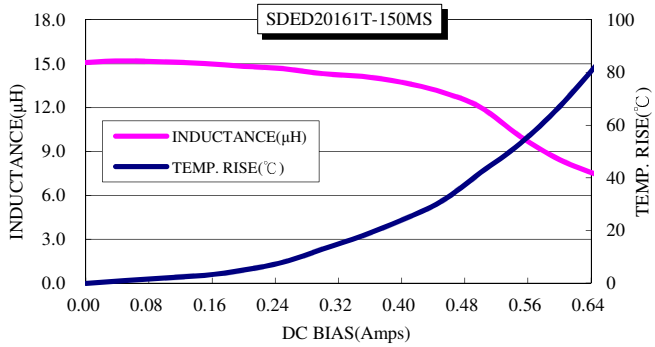


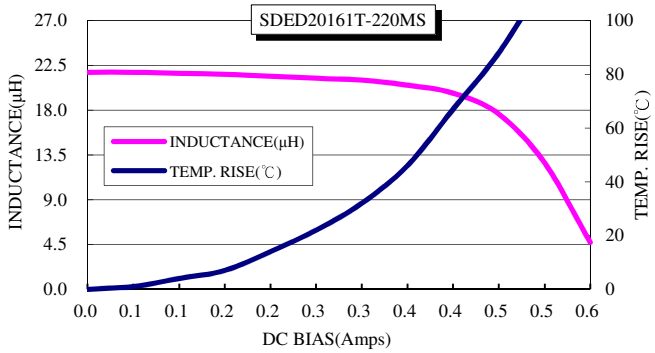





SDED20161T-6R8MS
INDUCTANCE(μH)
TEMP. RISE(℃)
INDUCTANCE(μH)
TEMP. RISE(℃)
DC BIAS(Amps)
8.40
7.00
5.60
4.20
2.80
1.40
0.00
100
80
60
40
20
0
0.0 0.1 0.2 0.3 0.4 0.5 0.6 0.7 0.8 0.9 1.0


SDED20161T-100MS
INDUCTANCE(μH)
TEMP. RISE(℃)
INDUCTANCE(μH)
TEMP. RISE(℃)
DC BIAS(Amps)
12.0
10.0
8.0
6.0
4.0
2.0
0.0
100
80
60
40
20
0
0.0 0.1 0.2 0.3 0.4 0.5 0.6 0.7 0.8


SDED20161T-150MS
INDUCTANCE(μH)
TEMP. RISE(℃)
INDUCTANCE(μH)
TEMP. RISE(℃)
DC BIAS(Amps)
18.0
15.0
12.0
9.0
6.0
3.0
0.0
100
80
60
40
20
0
0.00 0.08 0.16 0.24 0.32 0.40 0.48 0.56 0.64


SDED20161T-220MS
INDUCTANCE(μH)
TEMP. RISE(℃)
INDUCTANCE(μH)
TEMP. RISE(℃)
DC BIAS(Amps)
27.0
22.5
18.0
13.5
9.0
4.5
0.0
100
80
60
40
20
0
0.0 0.1 0.1 0.2 0.2 0.3 0.3 0.4 0.4 0.5 0.5 0.6