WIREWOUND CHIP INDUCTORS

SCI1008LP SERIES

1. PART NO. EXPRESSION :

S C I 1 0 0 8 L P - 3 N 3 K F

(a) (b) (c) (d) (e)(f)

(a) Series code

(b) Dimension code

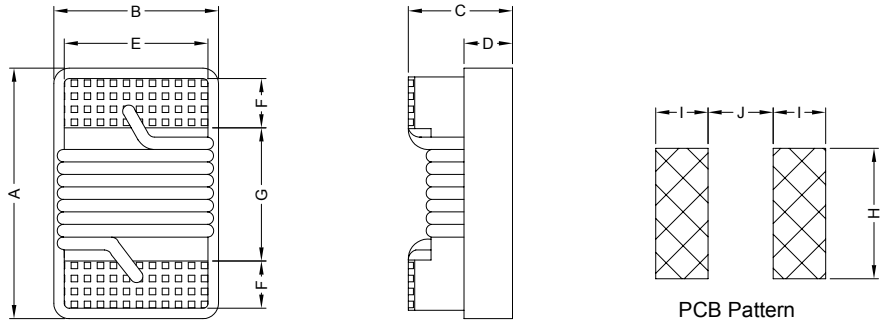
(c) Type code : LP (Low Profile)

(d) Inductance code : 3N3 = 3.3nH

(e) Tolerance code : G = ±2%, J = ±5%, K = ±10%

(f) F : Lead Free

2. CONFIGURATION & DIMENSIONS :

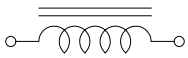


PCB Pattern

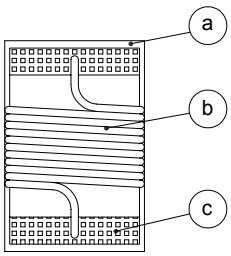
Unit:m/m

A	B	C	D	E	F	G	H	I	J
2.92 Max.	2.79 Max.	2.03 Max.	0.65 Ref.	2.03 Ref.	0.51 Ref.	1.52 Ref.	2.54 Ref.	1.02 Ref.	1.27 Ref.

3. SCHEMATIC :



4. MATERIALS :



(a) Core : Ceramic U core

(b) Wire : Enamelled Copper Wire

(c) Terminal Metallization : Ag + Ni + Au

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15.01.2008

SUPERWORLD ELECTRONICS (S) PTE LTD

5. GENERAL SPECIFICATION :

a) Storage temp. : -25°C to +80°C

b) Operating temp. : -40°C to +125°C

c) Temperature rise : 40°C Max.

d) Rated current : Base on temp. rise & ΔL/L0A=10% Max.

e) Resistance to solder heat : 260°C.10sec

6. ELECTRICAL CHARACTERISTICS :

Part No.	Tolerance Available	L (nH)	Test Freq (MHz)	Q Min.	Q Test Freq (MHz)	SRF (MHz) Min.	DCR (Ω) Max.	IDC (mA) Max.
SCI1008LP-3N3□F	K, J	3.3	50	42	1500	6000	0.030	1000
SCI1008LP-3N9□F	K, J	3.9	50	42	1500	6000	0.080	1000
SCI1008LP-4N7□F	K, J	4.7	50	42	1500	6000	0.150	600
SCI1008LP-5N6□F	K, J	5.6	50	50	1500	5800	0.160	600
SCI1008LP-6N8□F	K, J	6.8	50	50	1500	5400	0.170	600
SCI1008LP-8N2□F	K, J	8.2	50	50	1500	5000	0.220	600
SCI1008LP-15N□F	K, J	15	50	57	500	3000	0.220	600
SCI1008LP-20N□F	K, J	20	50	72	500	2400	0.330	600
SCI1008LP-30N□F	K, J	30	50	69	500	2400	0.380	600
SCI1008LP-40N□F	K, J	40	50	67	500	2000	0.430	600
SCI1008LP-50N□F	K, J, G	50	50	72	500	1900	0.480	600
SCI1008LP-56N□F	K, J, G	56	50	67	500	1850	0.490	600
SCI1008LP-60N□F	K, J, G	60	50	75	500	1800	0.520	600
SCI1008LP-68N□F	K, J, G	68	50	72	500	1750	0.530	560
SCI1008LP-70N□F	K, J, G	70	50	68	500	1700	0.550	510
SCI1008LP-80N□F	K, J, G	80	50	75	500	1400	0.560	510
SCI1008LP-90N□F	K, J, G	90	50	80	500	1400	0.610	500
SCI1008LP-R56□F	K, J, G	560	25	40	100	400	1.330	400

Inductance tolerance :

□ : G : ±2%

J : ±5%

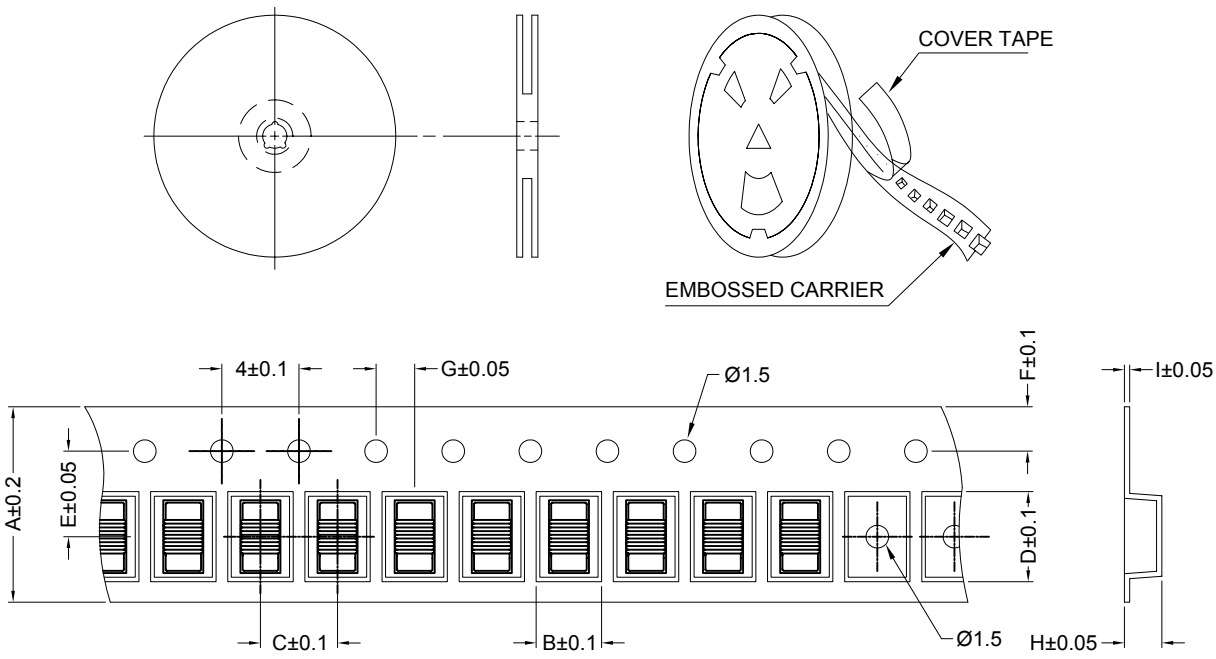
K : ±10%

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7. ELECTRICAL CHARACTERISTICS :

CONFIGURATTION :



DIMENSION (unit: mm)

SERIES	A	B	C	D	E	F	G	H	I
SCI0402	8.0	0.71	2.0	1.16	3.5	1.75	0	0.65	0.23
SCI0603	8.0	1.10	4.0	1.75	3.5	1.75	2	1.15	0.25
SCI0805	8.0	1.88	4.0	2.38	3.5	1.75	2	1.48	0.20
SCI1008	8.0	2.73	4.0	2.88	3.5	1.75	2	2.33	0.20

PACKING QUANTITY

SERIES	INNER REEL
	Q'TY (PCS)
SCI0402	4000
SCI0603	4000
SCI0805	2000
SCI1008	2000

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8. ENVIRONMENTAL CHARACTERISTICS :

Electrical Performance Test :

ITEM		SPECIFICATION	TEST CONDITIONS / TEST METHODS
1	Inductance	Refer to Electrcal Characteristics List	HP4291B
2	Q		HP4291B
3	SRF		HP8753D
4	DC Resistance Rdc		Mico-Ohmmeter (GOM-801G)
5	Rated Current IDC		The device should be REFLOW soldered (230±5°C for 10 seconds) to a tinned copper subs rate. A dynamiter
6	Over Load Test	After test, inductors shall have no evidence of electrical and mechanical damage	Applied 2 times of rated allowed DC current to inductor for a period of 5 minute.
7	Withstanding Voltage Test	After test, inductors shall have no evidence of electrical and mechanical damage	AC voltage of 500VAC applied between inductors terminal and case for 1 minute.
8	Insulation Resistance Test	1000 MOhm min.	100VDC applied between inductor terminal and case

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Mechanical Performance Test :

ITEM		SPECIFICATION	TEST CONDITIONS / TEST METHODS
1	Vibration	Appearance : No damage L change : within ±5% Q change : within ±10%	Test device shall be soldered on the substrate Oscillation Frequency : 10 to 55 to 10Hz for 1 min. Amplitude : 1.5mm Time : 2hrs for each axis (X, Y, Z), total 6hrs.
2	Resistance to Soldering Heat	Appearance : No damage L change : within ±5% Q change : within ±10%	Pre-heating : 150°C, 1 min Solder Composition : Sn/Pb=63/67 Solder Temperature : 230±5°C Immersion Time : 20±2sec Solder Temperature : 260±5°C Immersion Time : 5±2sec
3	Component Adhesion (Push Test)	1 lbs. For 0402 2 lbs. For 0603 3 lbs. For the rest	The device should be REFLOW soldered (230±5°C for 10 seconds) to a tinned copper subs rate. A dynamiter force gauge should be applied to the side of the conponent. The device must withstand a minimum force of 2 or 4 pounds without a failure of the termination attached to component.
3	Component Adhesion (Push Test)	The electrodes shall be at least 90% covered with new solder coating.	Pre-heating : 150°C, 1min Solder Composition : Sn/Pb=63/67 Solder Temperature : 230±5°C Immersion Time : 4±1sec
4	Drop Test	After test, the chip inductor don't fell of broke on the P.C.Board	Drop 1 time for each face and 1 time for each corner. Total drop 10 times. Drop Height : 100cm Drop Weight : 125g
5	Solderability Test	The terminal should at least be 90% covered with solder	after fluxing (alpha 100 or equiv), inductor shall be dipped in a melted solder bath at 232±5°C for 5 seconds.
6	Resistance to solvent test	There shall be no case of deformation change in appearance of obliteration of marking	MIL-STD202F, METHOD 215D

Pb

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Climatic Test :

<table>
<tr><th colspan="2">ITEM</th><th>SPECIFICATION</th><th>TEST CONDITIONS / TEST METHODS</th></tr>
<tr><td>1</td><td>Temperature Characteristics</td><td rowspan="7">Appearance : No damage
L change : within ±10%
Q change : within ±20%

There should be no evidence of short of open circuit.</td><td>-40°C ~ +125°C</td></tr>
<tr><td>2</td><td>Humidity Resistance</td><td>Temperature : 40±2°C
Relative Humidity : 90~95%
Time : 96hrs ±2hrs
Measured after exposure in the room condition for 2hrs</td></tr>
<tr><td>3</td><td>Low Temperature Storage Test</td><td>Temperature : -40±2°C
Time : 48±2hrs
Inductors are to be tested after 1 hour at room temperature.</td></tr>
<tr><td>4</td><td>Thermal Shock Test</td><td>One cycle :
<table>
<tr><th>Step</th><th>Temperature (°C)</th><th>Time (min)</th></tr>
<tr><td>1</td><td>-25±3</td><td>30</td></tr>
<tr><td>2</td><td>25±2</td><td>15</td></tr>
<tr><td>3</td><td>85±3</td><td>30</td></tr>
<tr><td>4</td><td>25±2</td><td>15</td></tr>
</table>
Total : 5 cycles</td></tr>
<tr><td>5</td><td>High Temperature Storage Test</td><td>Temperature : 125±2°C
Time : 48±2hrs
Load : Allowed DC current</td></tr>
<tr><td>6</td><td>High Temperature Load Life Test</td><td>Temperature : 85±2°C
Time : 1000±12hrs
Load : Allowed DC current</td></tr>
<tr><td>7</td><td>Humidity Load Life</td><td>Temperature : 40±2°C
Relative Humidity : 90~95%
Time : 1000±12hrs
Load : Allowed DC current</td></tr>
</table>

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