SBF-5089
SBF-5089Z

RoHS Compliant & Green Package

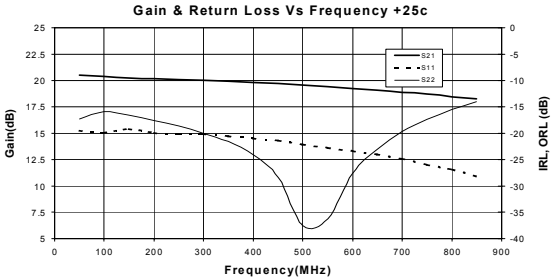
DC-500 MHz, Cascadable InGaP/GaAs HBT MMIC Amplifier



Product Description

Sirenza Microdevices' SBF-5089 is a high performance InGaP/GaAs Heterojunction Bipolar Transistor MMIC Amplifier. A Darlington configuration designed with InGaP process technology provides broadband performance up to 0.5 GHz with excellent thermal perfomance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only a single positive supply voltage, DC-blocking capacitors, a bias resistor, and an optional RF choke are required for operation.

The matte tin finish on Sirenza's lead-free package utilizes a post annealing process to mitigate tin whisker formation and is RoHS compliant per EU Directive 2002/95. This package is also manufactured with green molding compounds that contain no antimony trioxide nor halogenated fire retardants.



Product Features

- **Available in Lead Free, RoHS compliant, & Green packaging**
- **IP3 = 41dBm @ 240MHz**
- **Stable Gain Over Temperature**
- **Robust 1000V ESD, Class 1C**
- **Operates From Single Supply**
- **Low Thermal Resistance**

Applications

- **Receiver IF Applications**
- **Cellular, PCS, GSM, UMTS**
- **IF Amplifier**
- **Wireless Data, Satellite Terminals**

Symbol	Parameter	Units	Frequency	Min.	Typ.	Max.
G	Small Signal Gain	dB	70 MHz 240 MHz 500 MHz	 18.5 18.0	20.5 20.0 19.5	 21.5 21.0
P_{1dB}	Output Power at 1dB Compression	dBm	70 MHz 240 MHz 400 MHz	 19.2	21 21 20.7	
OIP_3	Output Third Order Intercept Point	dBm	70 MHz 240 MHz 400 MHz	 37.5	39.0 41.0 39.5	
IRL	Input Return Loss	dB	500 MHz	14	18	
ORL	Output Return Loss	dB	500 MHz	12	16	
NF	Noise Figure	dB	500 MHz		2.8	3.8
V_D	Device Operating Voltage	V		4.5	4.9	5.3
I_D	Device Operating Current	mA		82	90	98
R_{TH}, j-l	Thermal Resistance (junction to lead)	°C/W			43	

Test Conditions: V_S = 8 V, I_D = 90 mA Typ., OIP_3 Tone Spacing = 1 MHz, Pout per tone = 0 dBm
R_{BIAS} = 33 Ohms, T_L = 25ºC, Z_S = Z_L = 50 Ohms, App circuit page 4

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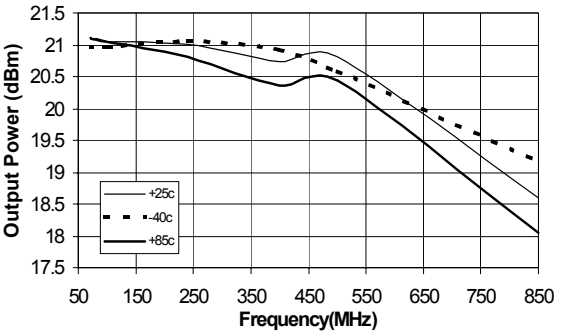
303 South Technology Court, Broomfield, CO 80021 Phone: (800) SMI-MMIC http://www.sirenza.com

Typical RF Performance at Key Operating Frequencies

Symbol	Parameter	Unit	Frequency (MHz) 70	100	240	400	500	850
G	Small Signal Gain	dB	20.5	20.4	20.1	19.8	19.5	18.2
OIP_3	Output Third Order Intercept Point	dBm	39	39	41	39.5	39	34
P_{1dB}	Output Power at 1dB Compression	dBm	21.0	21.0	21.0	20.7	20.8	18.6
IRL	Input Return Loss	dB	19.4	19.9	20.1	20.9	22.0	26.8
ORL	Output Return Loss	dB	17.2	15.8	18.6	24.0	37.5	15.5
S_{12}	Reverse Isolation	dB	25.2	22.4	22.3	22.3	22.3	22.4
NF	Noise Figure	dB	2.7	2.8	2.7	2.8	2.8	2.8

Test Conditions: V_S = 8 V, I_D = 90 mA Typ., OIP_3 Tone Spacing = 1 MHz, Pout per tone = 0 dBm
R_{BIAS} = 33 Ohms, T_L = 25ºC, $Z_S = Z_L$ = 50 Ohms using app circuit see page 4

P1dB vs Temp



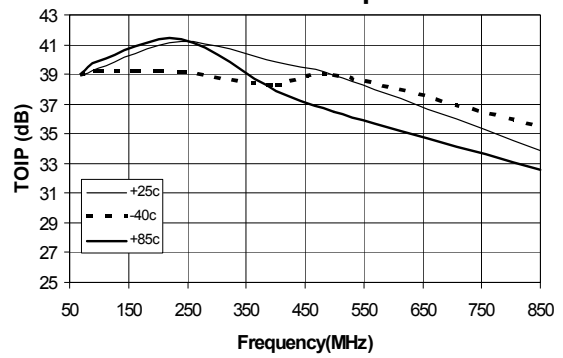
Absolute Maximum Ratings

Parameter	Absolute Limit
Max. Device Current (I_D)	150 mA
Max. Device Voltage (V_D)	6 V
Max. RF Input Power	+19 dBm
Max Operating Dissipated Power	0.8 W
Max. Junction Temp. (T_J)	+150°C
Operating Temp. Range (T_L)	-40°C to +85°C
Max. Storage Temp.	+150°C

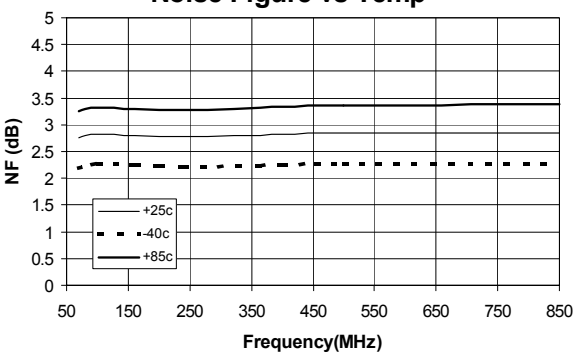
Operation of this device beyond any one of these limits may cause permanent damage. For reliable continous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.

Bias Conditions should also satisfy the following expression:
$I_D V_D < (T_J - T_L) / R_{TH}$, j-l $T_L = T_{LEAD}$

TOIP vs Temp

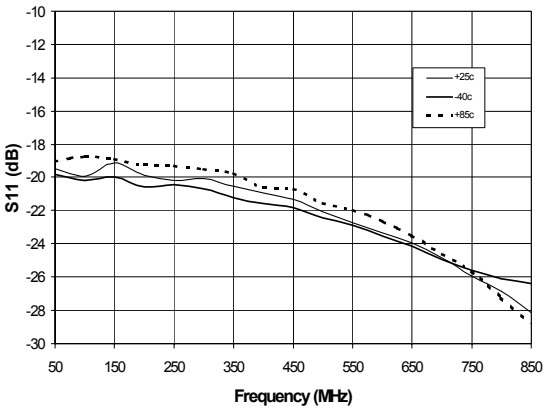


Noise Figure vs Temp

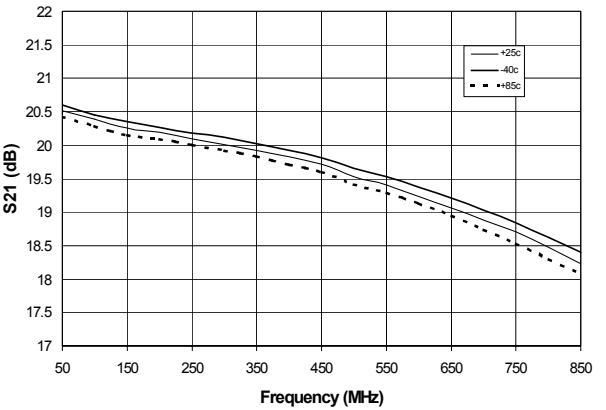


Test Conditions : Vs = 8v, R-bias = 33ohm, Id = 90mA, Temp = +25c

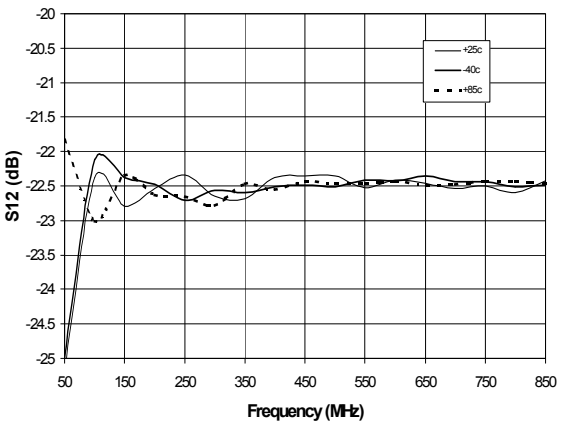
$|S_{11}|$ vs. Frequency



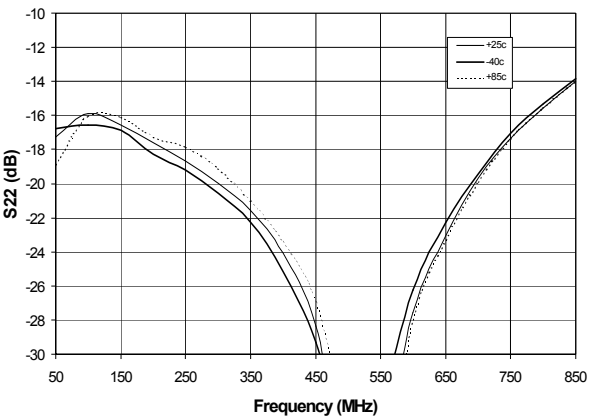
$|S_{21}|$ vs. Frequency



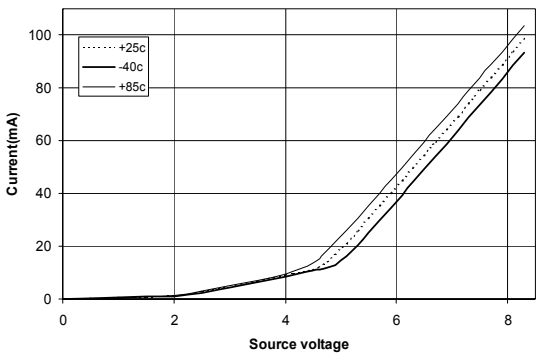
$|S_{12}|$ vs. Frequency



$|S_{22}|$ vs. Frequency

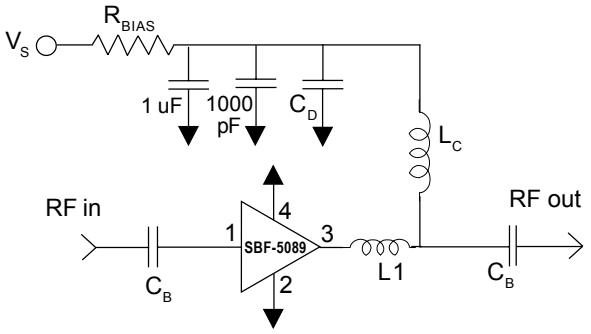


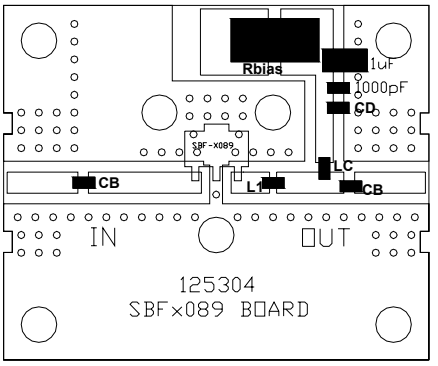
Bias Sweep vs. Temperature



NOTE: Output Return Loss Can be improved at low end of band with L1 selection, see Page 4 app circuit.

Basic Application Circuit





Application Circuit Element Values

Reference Designator	Frequency (Mhz)				
	70	100	240	500	850
C_B	1uF	1000 pF	1000 pF	220 pF	100pF
C_D	1 uF	100 pF	100 pF	100 pF	68 pF
L_C	6.8uH	1.2 uH	1.2uH	68 nH	33 nH
L1	6.8nH	6.8nH	6.8nH	6.8nH	6.8nH

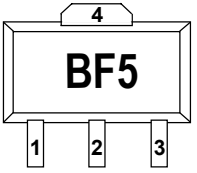
Recommended Bias Resistor Values for I_D=90mA R_{BIAS}=(V_S-V_D) / I_D				
Supply Voltage(V_S)	7.5 V	8 V	10 V	12 V
R_{BIAS}	27 Ω	33 Ω	55 Ω	77 Ω
Note: R_{BIAS} provides DC bias stability over temperature.				

Mounting Instructions

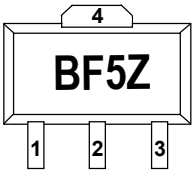
1. NOTE: For broadband RF unconditional stability do not put GND vias under the exposed backside GND paddle.
2. Solder the copper pad on the backside of the device package to the ground plane.
3. Use a large ground pad area with many plated through-holes as shown.
4. We recommend 1 or 2 ounce copper. Measurement for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

Pin #	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
2, 4	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.
3	RF OUT/ BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.

Part Identification Marking



Tin-Lead



Lead Free

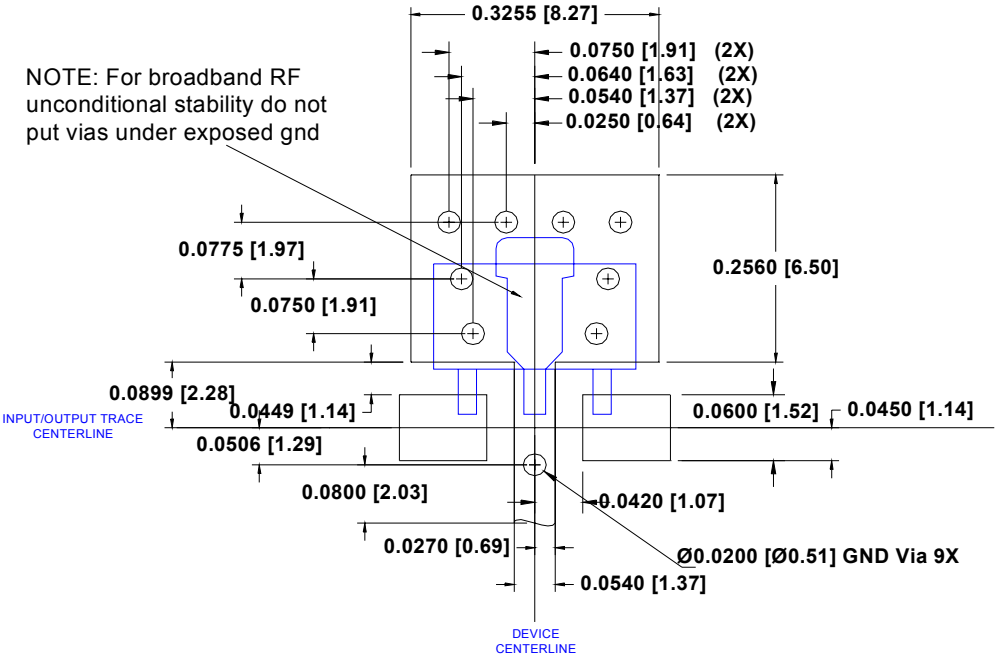
Caution: ESD sensitive
Appropriate precautions in handling, packaging and testing devices must be observed.

Part Number Ordering Information

Part Number	Reel Size	Devices/Reel
SBF-5089	7"	1000
SBF-5089Z	7"	1000

PCB Pad Layout

Dimensions in inches [millimeters]



See Application Note AN-075
For Package Outline Drawing