

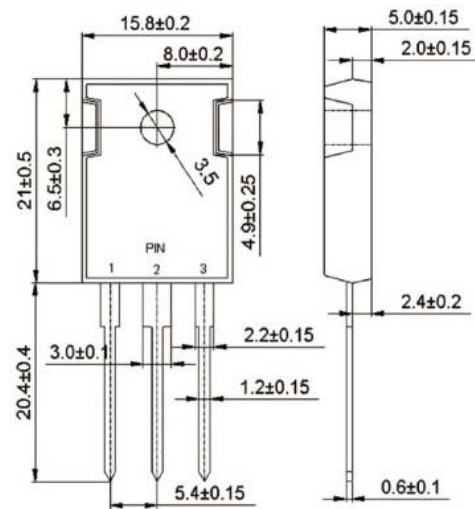
Features

- Schottky Barrier Chip
- Ideally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- For Use in Low Voltage Application
- Guard Ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94V-O

Mechanical Data

- Case: TO-247AD/TO-3P, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Mounting Position: Any
- **Lead Free: For RoHS / Lead Free Version**

TO-247AD/TO-3P



Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	MBR 3040 PT	MBR 3045 PT	MBR 3050 PT	MBR 3060 PT	MBR 30100 PT	MBR 30150 PT	MBR 30200 PT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	40	45	50	60	100	150	200	V
RMS Reverse Voltage	V _{R(RMS)}	28	31	35	42	70	105	140	V
Average Rectified Output Current @T _L = 75°C (Note 1)	I _O	30							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	275							A
Forward Voltage @I _F = 15A	V _{FM}	0.70		0.75		0.80	0.90		V
Peak Reverse Current @T _A = 25°C At Rated DC Blocking Voltage @T _A = 100°C	I _{RM}	0.2 20							mA
Typical Junction Capacitance (Note 2)	C _j	350		280		200			pF
Typical Thermal Resistance (Note 1)	R _{θJA}	3.0				2.0			°C/W
Operating and Storage Temperature Range	T _j , T _{STG}	-55 to +150							°C

Note: 1. Valid provided that leads are kept at ambient temperature at a distance of 9.5mm from the case.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

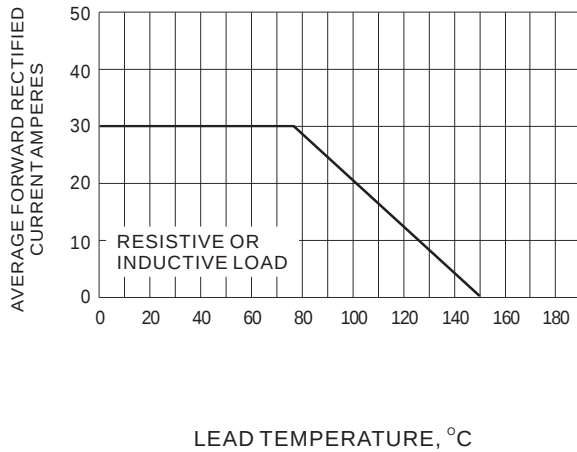


Fig.1- FORWARD CURRENT DERATING CURVE

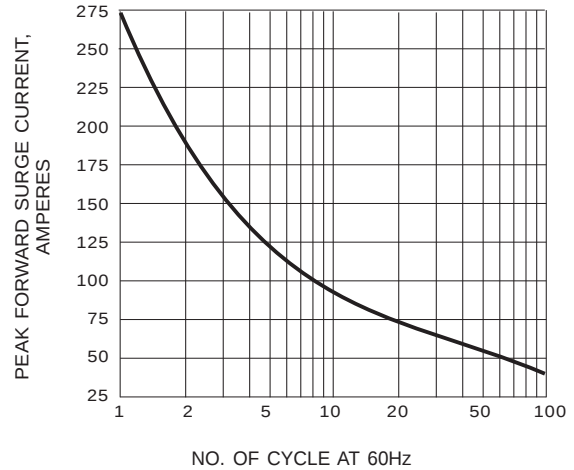


Fig.2- MAXIMUM NON-REPETITIVE SURGE CURRENT

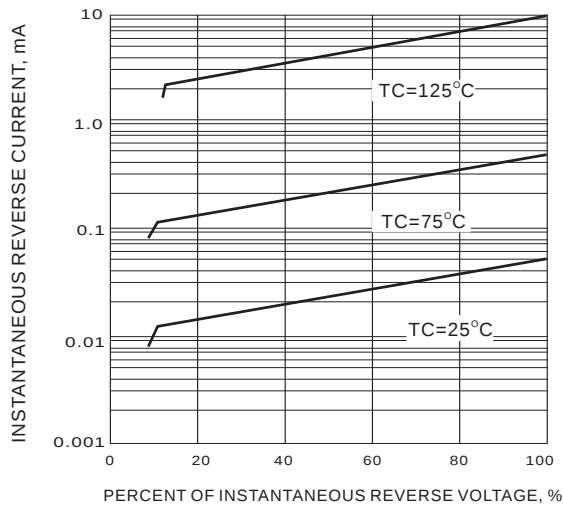


Fig.3- TYPICAL REVERSE CHARACTERISTIC

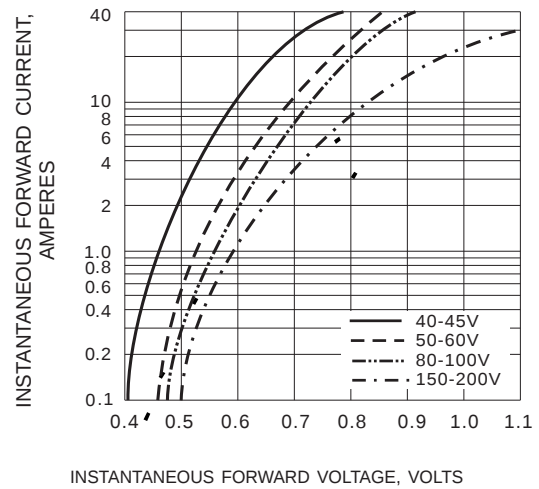


Fig.4- TYPICAL INSTANTANEOUS FORWARD CHARACTERISTIC