

ADSL Magnetics

TM02392

- Designed for use with Texas Instruments' AC5 TNETD 5800 ADSL chipset
- Approved by UL and BABT per the requirements of 60950 safety documents
- UL file number E169987
BABT certificate number NC/011450
- Minimum interwinding breakdown voltage of 1500 Vrms
- Operating and storage temperature range: -40° to +85° C
- Meets IEC 695, 2-2 flammability requirements
- PWB Process Capability: standard printed wiring board assembly techniques, total-immersion cleaning
- Reliability testing: shock, vibration, temperature cycling, temperature - humidity - bias

ELECTRICAL SPECIFICATIONS AT 25° C

Part Number	Turns Ratio ±2%	Inductance ¹ μH ±5%	Leakage Inductance μH max	Interwinding Capacitance pF max	DCR Ω max		Longitudinal Balance ² dB min	Insertion Loss ² dB max	Frequency Response ² dB max	Harmonic Distortion ² dB max
	Chip : Line	Line Side	Line Side	Between 1 & 8	Chip Side	Line Side	30 kHz - 1.1 MHz	300 kHz	30 kHz - 1.1 MHz	15 Vpp 100 kHz
S560-6600-BR	1 : 1.9	400	7	40	1.2	1.8	50	0.4	±0.35	-80

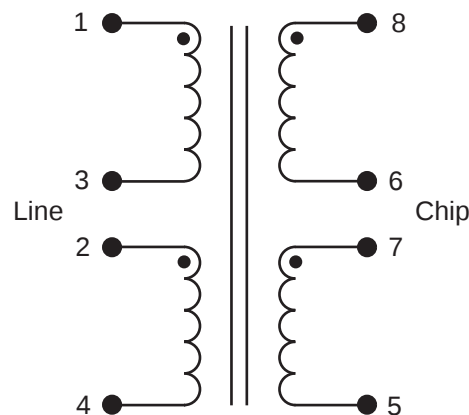
All measurements are made with pins 2 and 3 connected and pins 6 and 7 connected

1. Inductance measured at 10 kHz, 0.1 Vac

2. Measured with line impedance of 100 Ω

SCHEMATIC

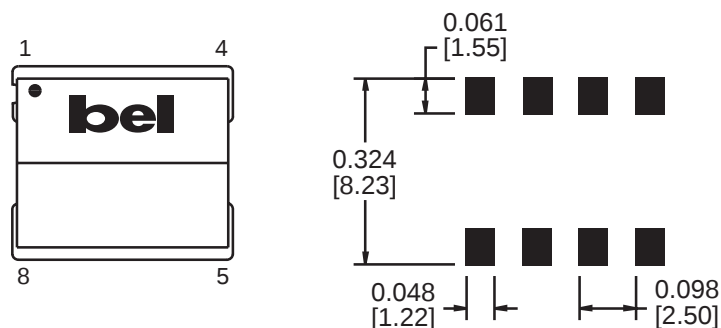
S560-6600-BR



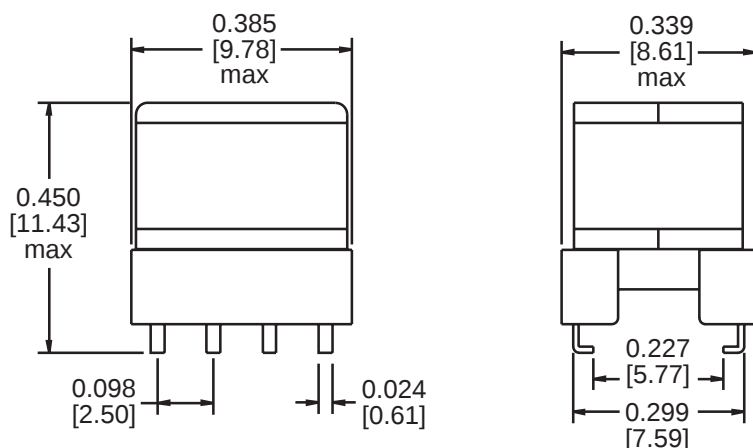
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MECHANICAL

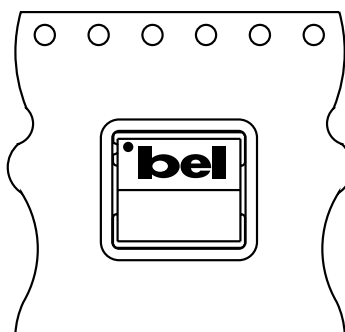
S560-6600-BR



Suggested PCB Pad Layout



Dimensions are in inches [millimeters].
Standard dimension tolerance is ± 0.005 [0.13] unless otherwise noted.



➔ User Feed Direction

Orientation of Component Inside Pocket

Tape and Reel Packaging

24 mm wide by 16 mm pitch; 13 inch OD, 4 inch ID
300 components per reel, maximum
Meets ANSI/EIA 481-2 Carrier Tape Standards

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