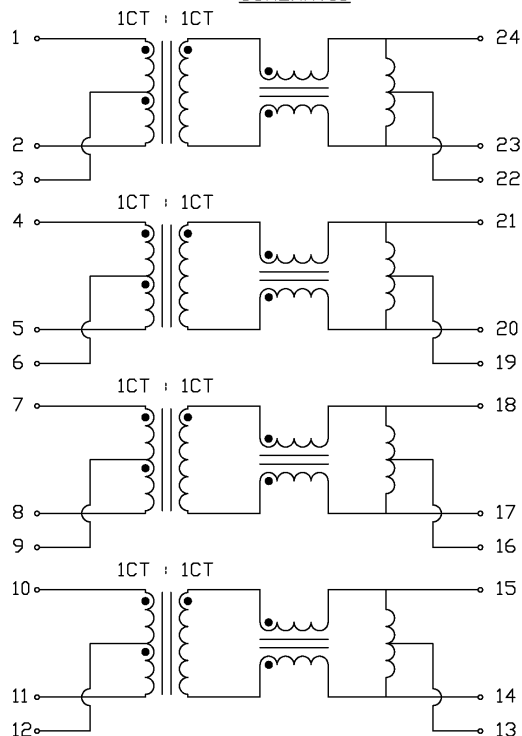


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SCHEMATIC



ELECTRICAL SPECEIFICATIONS @25°C

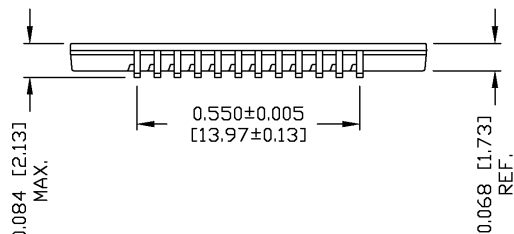
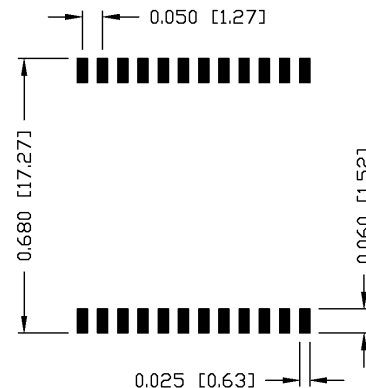
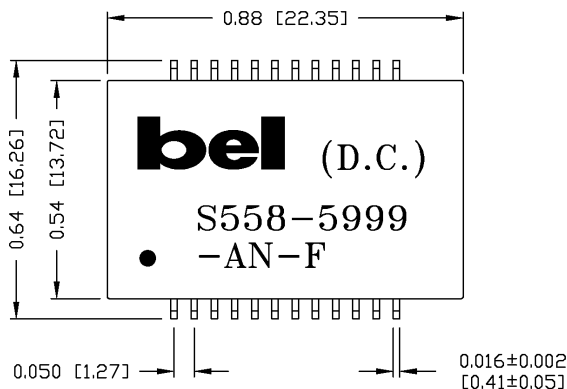
URNS RATIO	1CT : 1CT
PHASE	1 TO 24, 4 TO 21, 7 TO 18, 10 TO 15 IN PHASE
OCL :	350uH MIN. AT 100kHz, 100mV (1-2), (4-5), (7-8), (10-11) WITH 8mA DC BIAS, FROM 0 -70°C
INSERTION LOSS	1.0 dB MAX. @ 1 TO 100MHz 1.5 dB MAX. @ 125MHz
RETURN LOSS	18dB MIN. @ 1-30MHz MEASURED UNDER A 100 Ω I/O 15dB MIN. @ 60MHz 12dB MIN. @ 80MHz 10dB MIN. @ 100MHz
CROSSTALK	10-100 MHz 35 dB MIN.
COMMON TO COMMON REJECTION	30 dB MIN. AT 100kHz TO 155MHz
HIPOT	PER HAND-WORK-03

ORIGINATED BY	DATE	TITLE ELECTRICAL SPECIFICATION S558-5999-AN-F	PART NO. / DRAWING NO.	STANDARD DIM.		[] METRIC DIM. AS REFERENCE		 COMPONENTS FOR A CONNECTED PLANET
HQ	12-12-05		X5585999AN-F	TOL. IN INCH		UNIT : INCH [mm]	REV. : A	
DRAWN BY	DATE		FILE NAME	.X		SCALE : N/A	SIZE : A4	
DQ Chen	12-12-05		X5585999ANFA.DWG	.XX		PAGE : 2		
			.XXX					

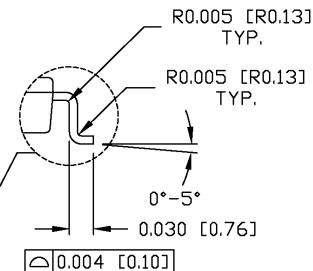
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SUGGESTED PCB PAD LAYOUT



BETWEEN
COMPONENTS
AND INTERNAL
LEADS TO
SEATING PLANE
WITH 0.002" MIN
CLEARANCE



NOTES:

1. STANDARD MARKING REFER TO DOC. HAND-WORK-04.
2. PACKAGE CODE: "RJS001".

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM. TOL. IN INCH	[] METRIC DIM. AS REFERENCE
HO	12-12-05	MECHANICAL DRAWING S558-5999-AN-F	X5585999AN-F	.X	UNIT : INCH [mm] REV. : A
DRAWN BY	DATE		FILE NAME	.XX ±0.01	SCALE : N/A SIZE : A4
DQ Chen	12-12-05		X5585999ANFA.DWG	.XXX ±0.005	PAGE : 3

