

S33xx Model
8 Pin Dip, 3.3V, HCMOS

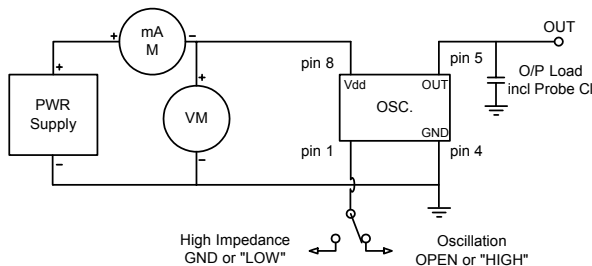
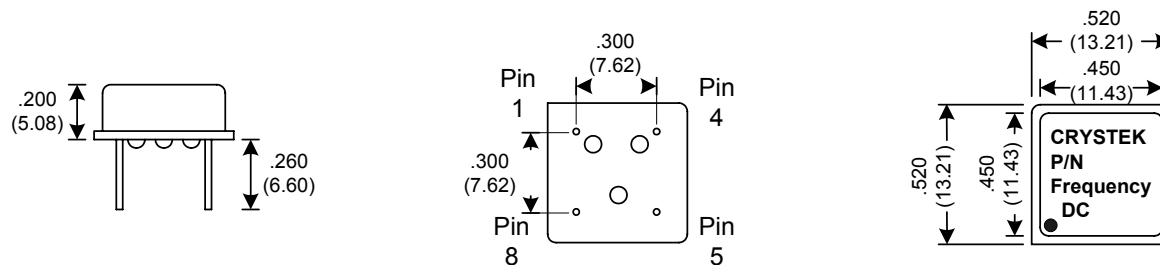
Lead Free
RoHS
Compliant

Clock Oscillator



Frequency Range: 1.544MHz to 125MHz
Frequency Stability: ± 10 ppm to ± 100 ppm
Temperature Range:
 Operating: 0°C to 70°C
 (Option) -40°C to 85°C
Storage: -55°C to 120°C
Input Voltage: 3.3V $\pm$ 0.3V
Input Current: 40mA Max
Output: HCMOS
 Symmetry: 45/55% Max @ 50% Vdd
 Rise/Fall Time: 4ns Typ, 6ns Max
 Logic: "0" = 10% Vdd Max
 "1" = 90% Vdd Min
 Load: 30pF Max
 Start-Up Time: 10mSec Max
Jitter: 12KHz~20MHz 0.5ps Typ, 1ps Max RMS
Aging: <3ppm 1st/yr, 1ppm every year thereafter

Designed to meet today's requirements for low jitter applications. The oscillator utilizes fundamental and 3rd overtone crystal technology. No multiplier is used thereby reducing output jitter.



Crystek Part Number Guide

Example: S3392-44.736
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Temperature		Frequency Stability
0/ 70°C	-40/ 85°C	
S3390	SE3390	+/- 100ppm
S3392	SE3392	+/- 50ppm
S3391	SE3391	+/- 25ppm
S3398	NA	+/- 20ppm
S3397	NA	+/- 10ppm

Tri-State Function	
Function pin 1	Output pin
Open	Active
"1" level 2.4V Min	Active
"0" level 0.4V Max	High Z

Mechanical:

Shock: MIL-STD-883, Method 2002, Condition B
 Solderability: MIL-STD-883, Method 2003
 Vibration: MIL-STD-883, Method 2007, Condition A
 Solvent Resistance: MIL-STD-202, Method 215
 Resistance to Soldering Heat: MIL-STD-202, Method 210, Condition A,B or C
Environmental:
 Thermal Shock: MIL-STD-883, Method 1011, Condition A
 Moisture Resistance: MIL-STD-883, Method 1004
 Gross Leak Test: MIL-STD-883, Method 1014, Condition A
 Fine Leak Test: MIL-STD-883, Method 1014, Condition A₂

Specifications subject to change without notice.

TD-02065 Rev.C

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