

RJK0703DPP-E0

N-Channel MOS FET
75 V, 70 A, 6.7 mΩ

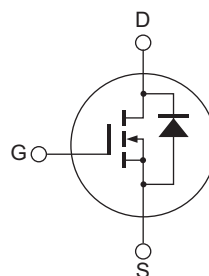
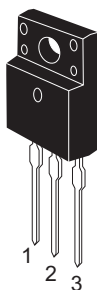
R07DS0630EJ0200
Rev.2.00
Oct 15, 2012

Features

- High speed switching
- Low drive current
- Low on-resistance $R_{DS(on)} = 5.3 \text{ m}\Omega$ typ. (at $V_{GS} = 10 \text{ V}$)
- Package TO-220FP

Outline

RENESAS Package code: PRSS0003AG-A
(Package name: TO-220FP)



1. Gate
2. Drain
3. Source

Absolute Maximum Ratings

($T_a = 25^\circ\text{C}$)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	75	V
Gate to source voltage	V_{GSS}	± 20	V
Drain current	I_D	70	A
Drain peak current	$I_{D(pulse)}$ ^{Note1}	210	A
Body-drain diode reverse drain current	I_{DR}	70	A
Avalanche current	I_{AP} ^{Note2}	35	A
Avalanche energy	E_{AS} ^{Note2}	184	mJ
Channel dissipation	P_{ch} ^{Note3}	25	W
Channel to case thermal impedance	θ_{ch-c}	5.0	$^\circ\text{C}/\text{W}$
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

Notes: 1. $PW \leq 10 \mu\text{s}$, duty cycle $\leq 1\%$
 2. Value at $L = 100 \mu\text{H}$, $T_{ch} = 25^\circ\text{C}$, $R_g \geq 50\Omega$,
 3. $T_c = 25^\circ\text{C}$

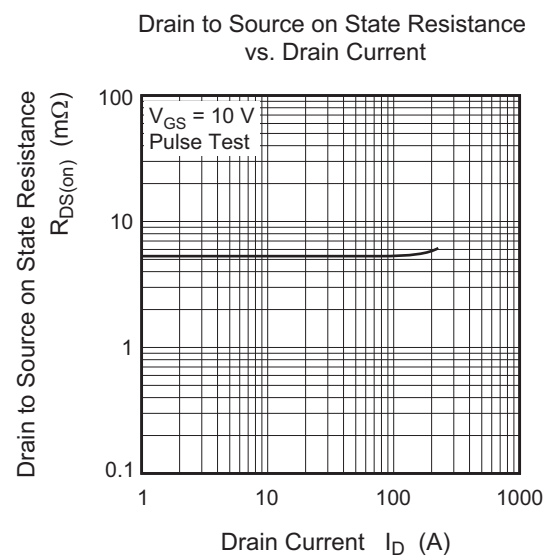
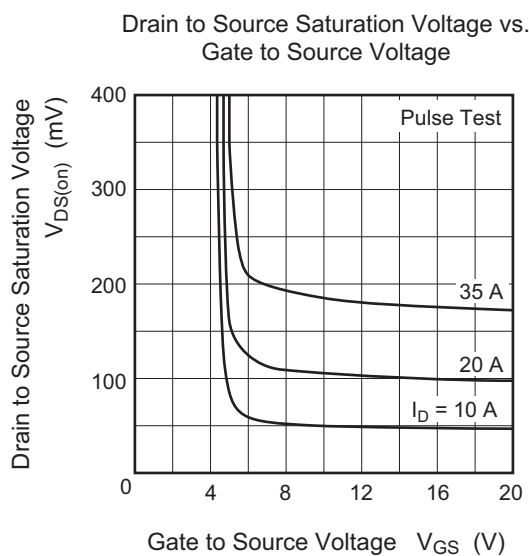
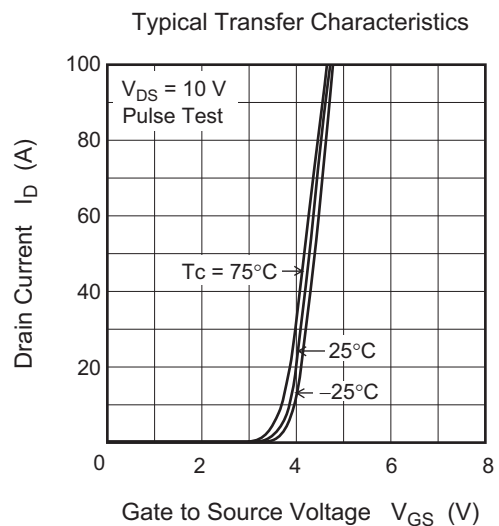
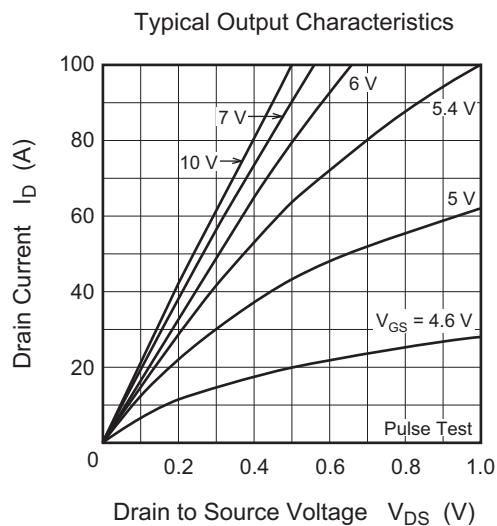
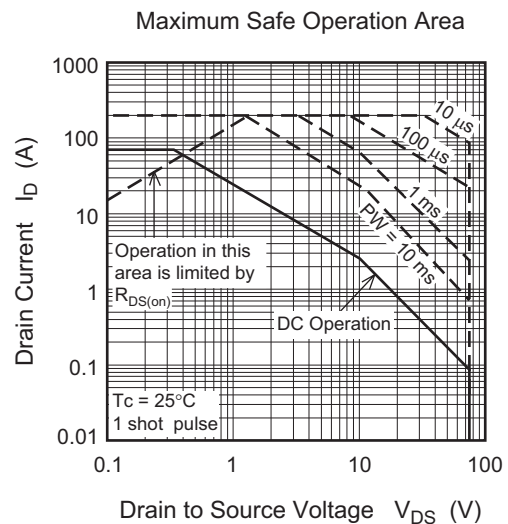
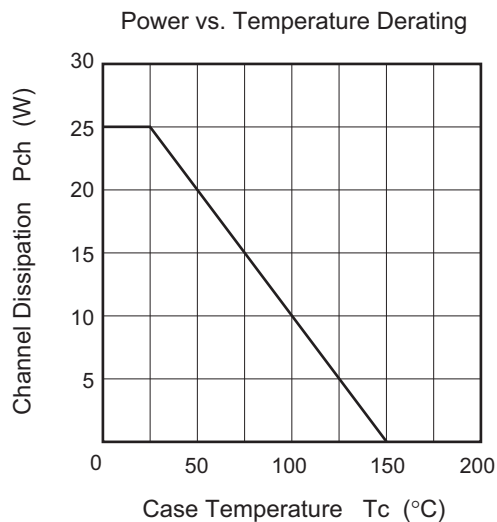
Electrical Characteristics

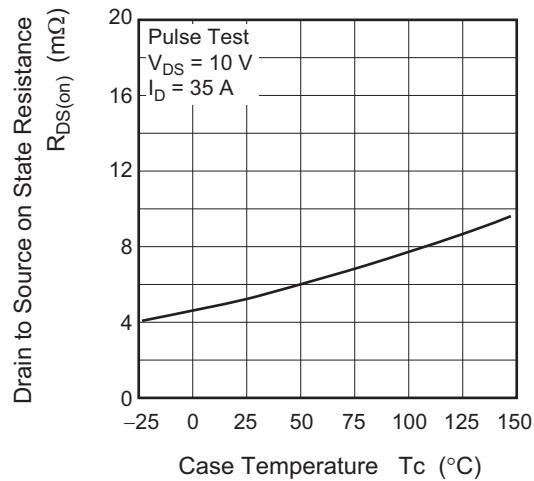
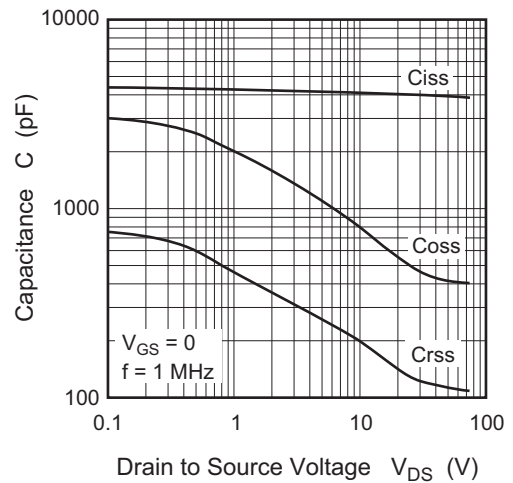
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	75	—	—	V	$I_D = 10\text{mA}$, $V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 0.1	μA	$V_{GS} = \pm 20\text{ V}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	1	μA	$V_{DS} = 75\text{ V}$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	—	4.0	V	$V_{DS} = 10\text{ V}$, $I_D = 1\text{ mA}$
Static drain to source on state resistance	$R_{DS(on)}$	—	5.3	6.7	$\text{m}\Omega$	$I_D = 35\text{ A}$, $V_{GS} = 10\text{ V}$ ^{Note4}
Forward transfer admittance	$ y_{fs} $	—	90	—	S	$I_D = 35\text{ A}$, $V_D = 10\text{ V}$ ^{Note4}
Input capacitance	C_{iss}	—	4150	—	pF	$V_{DS} = 10\text{ V}$ $V_{GS} = 0$ $f = 1\text{ MHz}$
Output capacitance	C_{oss}	—	830	—	pF	
Reverse transfer capacitance	C_{rss}	—	200	—	pF	
Gate Resistance	R_g	—	1.6	—	Ω	
Total gate charge	Q_g	—	56	—	nC	$V_{DD} = 25\text{ V}$ $V_{GS} = 10\text{ V}$, $I_D = 35\text{ A}$
Gate to source charge	Q_{gs}	—	20	—	nC	
Gate to drain charge	Q_{gd}	—	8	—	nC	
Turn-on delay time	$t_{d(on)}$	—	30	—	ns	$V_{GS} = 10\text{ V}$ $I_D = 35\text{ A}$ $V_{DD} \cong 30\text{ V}$ $R_g = 4.7\ \Omega$
Rise time	t_r	—	10	—	ns	
Turn-off delay time	$t_{d(off)}$	—	60	—	ns	
Fall time	t_f	—	11	—	ns	
Body-drain diode forward voltage	V_{DF}	—	0.85	1.5	V	$I_F = 70\text{ A}$, $V_{GS} = 0$ ^{Note4}
Body-drain diode reverse recovery time	t_{rr}	—	50	—	ns	$I_F = 70\text{ A}$, $V_{GS} = 0$ $di_F/dt = 100\text{ A}/\mu\text{s}$

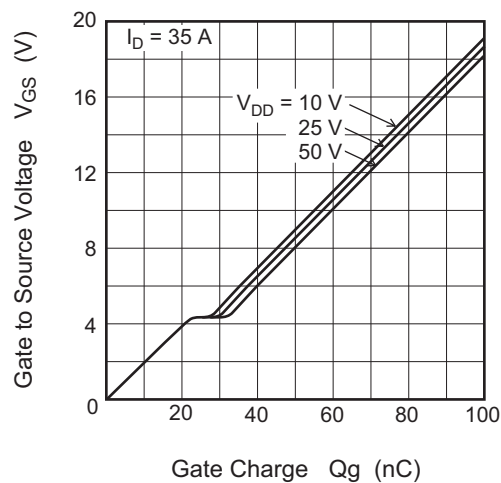
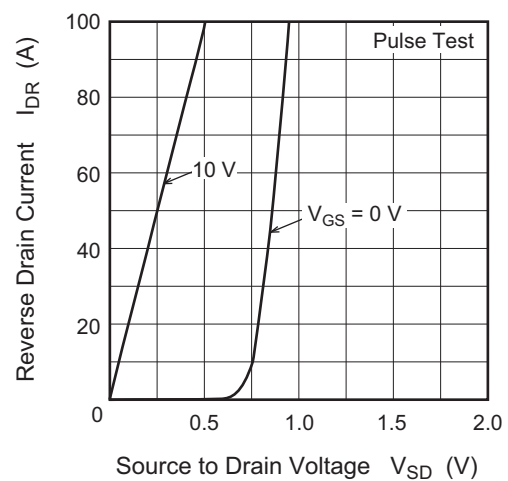
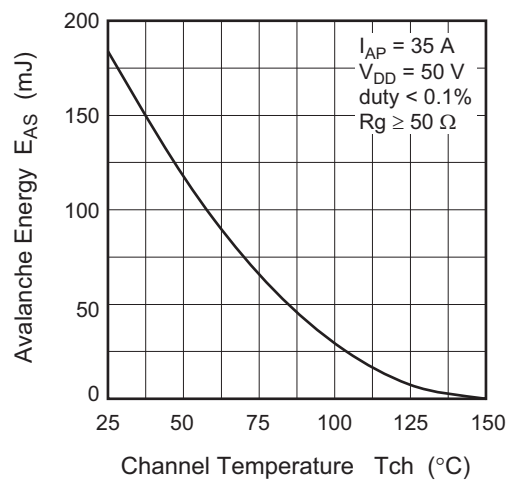
Notes: 4. Pulse test

Main Characteristics

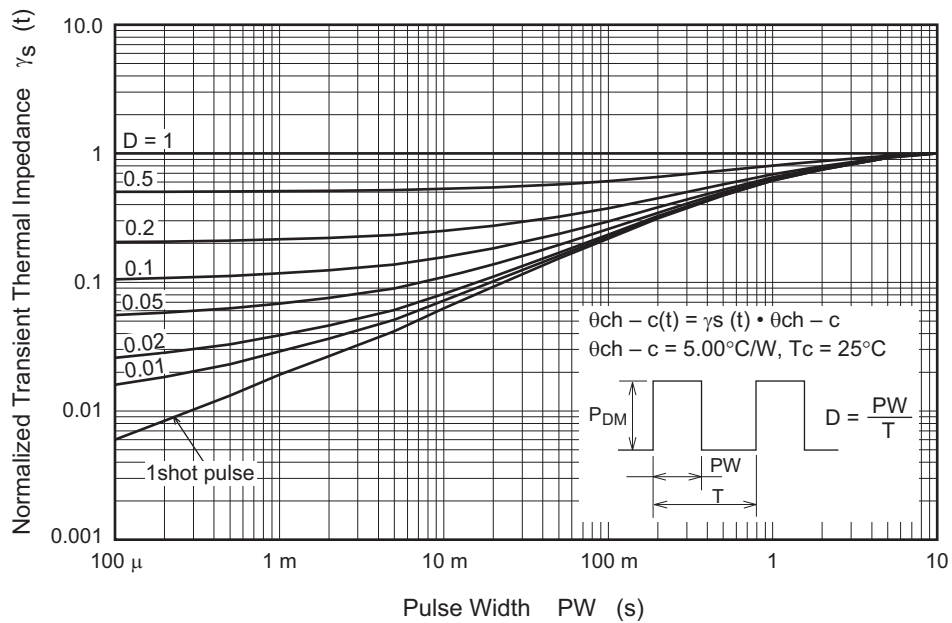


Drain to Source on State Resistance
vs. TemperatureTypical Capacitance vs.
Drain to Source Voltage

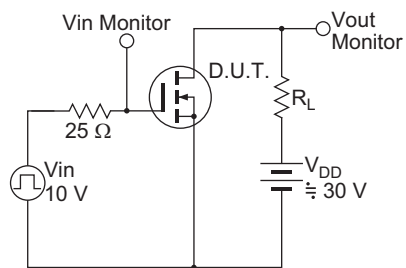
Dynamic Input Characteristics

Reverse Drain Current vs.
Source to Drain VoltageMaximum Avalanche Energy vs.
Channel Temperature Derating

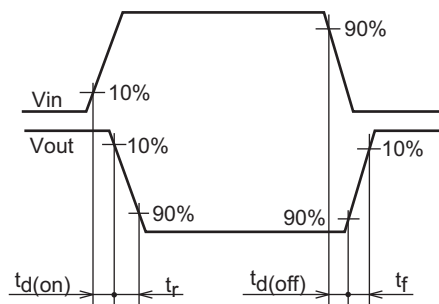
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



Waveform



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Renesas Electronics America Inc.
2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A.
Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited
1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada
Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-651-700, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH
Arcadiastrasse 10, 40472 Düsseldorf, Germany
Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China
Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2886-9318, Fax: +852 2886-9022/9044

Renesas Electronics Taiwan Co., Ltd.
13F, No. 363, Fu Shing North Road, Taipei, Taiwan
Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.
80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre Singapore 339949
Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd.
11F., Samik Laved' or Bldg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea
Tel: +82-2-558-3737, Fax: +82-2-558-5141