



## Table of Contents

|                                                |           |                                                                  |           |
|------------------------------------------------|-----------|------------------------------------------------------------------|-----------|
| <b>1 Features</b> .....                        | <b>1</b>  | 8.4 Device Functional Modes.....                                 | <b>13</b> |
| <b>2 Applications</b> .....                    | <b>1</b>  | <b>9 Application and Implementation</b> .....                    | <b>15</b> |
| <b>3 Description</b> .....                     | <b>1</b>  | 9.1 Application Information.....                                 | <b>15</b> |
| <b>4 Revision History</b> .....                | <b>2</b>  | 9.2 Typical Application .....                                    | <b>15</b> |
| <b>5 Device Comparison Table</b> .....         | <b>3</b>  | <b>10 Power Supply Recommendations</b> .....                     | <b>17</b> |
| <b>6 Pin Configuration and Functions</b> ..... | <b>3</b>  | <b>11 Layout</b> .....                                           | <b>17</b> |
| <b>7 Specifications</b> .....                  | <b>4</b>  | 11.1 Layout Guidelines .....                                     | <b>17</b> |
| 7.1 Absolute Maximum Ratings .....             | <b>4</b>  | 11.2 Layout Example .....                                        | <b>17</b> |
| 7.2 ESD Ratings.....                           | <b>4</b>  | <b>12 Device and Documentation Support</b> .....                 | <b>18</b> |
| 7.3 Recommended Operating Conditions.....      | <b>4</b>  | 12.1 Documentation Support .....                                 | <b>18</b> |
| 7.4 Thermal Information .....                  | <b>4</b>  | 12.2 Related Links .....                                         | <b>18</b> |
| 7.5 Electrical Characteristics.....            | <b>5</b>  | 12.3 Receiving Notification of Documentation Updates .....       | <b>18</b> |
| 7.6 Typical Characteristics .....              | <b>7</b>  | 12.4 Community Resources.....                                    | <b>18</b> |
| <b>8 Detailed Description</b> .....            | <b>11</b> | 12.5 Trademarks .....                                            | <b>18</b> |
| 8.1 Overview .....                             | <b>11</b> | 12.6 Electrostatic Discharge Caution.....                        | <b>18</b> |
| 8.2 Functional Block Diagram .....             | <b>11</b> | 12.7 Glossary .....                                              | <b>18</b> |
| 8.3 Feature Description.....                   | <b>11</b> | <b>13 Mechanical, Packaging, and Orderable Information</b> ..... | <b>19</b> |

## 4 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

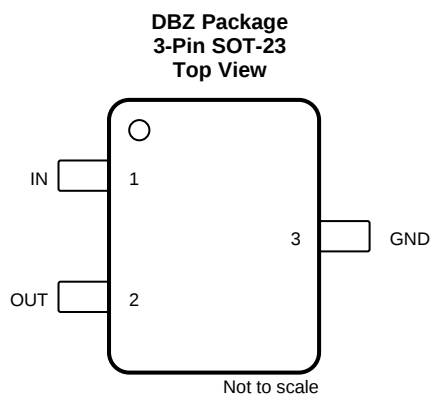
| <b>Changes from Revision G (November 2015) to Revision H</b>                                                                                                                                                             | <b>Page</b> |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------|
| • Changed section header From: REF33xx (REF3312, REF3318, REF3320, REF3325, REF3330, REF3333) To: REF30xx (REF3012, REF3020, REF3025, REF3030, REF3033, REF3040) in the <a href="#">Electrical Characteristics</a> ..... | <b>6</b>    |
| • Added turnon settling time TYP value of 120 $\mu$ s (deleted by mistake in the previous revision) .....                                                                                                                | <b>6</b>    |
| • Added NOTE to the <a href="#">Application and Implementation</a> section .....                                                                                                                                         | <b>15</b>   |

| <b>Changes from Revision F (August 2008) to Revision G</b>                                                                                                                                                                                                | <b>Page</b> |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------|
| • Added <i>Device Information</i> , <i>ESD Ratings</i> , <i>Recommended Operating Conditions</i> , and <i>Thermal Information</i> tables. ....                                                                                                            | <b>1</b>    |
| • Added <i>Detailed Description</i> , <i>Applications and Implementation</i> , <i>Power-Supply Recommendations</i> , <i>Layout</i> , <i>Device and Documentation Support</i> , and <i>Mechanical, Packaging, and Orderable Information</i> sections ..... | <b>1</b>    |
| • Changed text in <i>Description</i> section .....                                                                                                                                                                                                        | <b>1</b>    |
| • Deleted thermal resistance parameter in Electrical Characteristics; see new Thermal Information table .....                                                                                                                                             | <b>6</b>    |
| • Moved temperature parameters from Electrical Characteristics to Recommended Operating Conditions .....                                                                                                                                                  | <b>6</b>    |

## 5 Device Comparison Table

| PART NUMBER | VOLTAGE (V) |
|-------------|-------------|
| REF3012     | 1.25        |
| REF3020     | 2.048       |
| REF3025     | 2.5         |
| REF3030     | 3.0         |
| REF3033     | 3.3         |
| REF3040     | 4.096       |

## 6 Pin Configuration and Functions



### Pin Functions

| PIN |      | I/O    | DESCRIPTION              |
|-----|------|--------|--------------------------|
| NO. | NAME |        |                          |
| 1   | IN   | Input  | Input supply voltage     |
| 2   | OUT  | Output | Reference output voltage |
| 3   | GND  | —      | Ground                   |

## 7 Specifications

### 7.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)<sup>(1)</sup>

|                                             | MIN        | MAX | UNIT |
|---------------------------------------------|------------|-----|------|
| Supply voltage, V+ to V–                    |            | 7.0 | V    |
| Output short-circuit current <sup>(2)</sup> | Continuous |     |      |
| Operating temperature                       | –40        | 125 | °C   |
| Junction temperature                        |            | 150 | °C   |
| Storage temperature, T <sub>stg</sub>       | –65        | 150 | °C   |

- (1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) Short circuit to ground.

### 7.2 ESD Ratings

|                                            |                                                                                | VALUE | UNIT |
|--------------------------------------------|--------------------------------------------------------------------------------|-------|------|
| V <sub>(ESD)</sub> Electrostatic discharge | Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001 <sup>(1)</sup>              | ±4000 | V    |
|                                            | Charged-device model (CDM), per JEDEC specification JESD22-C101 <sup>(2)</sup> | ±1500 |      |

- (1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
- (2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

### 7.3 Recommended Operating Conditions

at T<sub>A</sub> = 25°C, V<sub>IN</sub> = 5 V, and I<sub>LOAD</sub> = 0 mA (unless otherwise noted)

|                                      | MIN                                    | NOM | MAX | UNIT |
|--------------------------------------|----------------------------------------|-----|-----|------|
| V <sub>IN</sub> Input voltage        | V <sub>REF</sub> + 0.05 <sup>(1)</sup> |     | 5.5 | V    |
| I <sub>LOAD</sub> Load current       |                                        |     | 25  | mA   |
| T <sub>A</sub> Operating temperature | –40                                    |     | 125 | °C   |

- (1) For I<sub>L</sub> > 0, see [Typical Characteristics](#). Minimum supply voltage for REF3012 is 1.8 V.

### 7.4 Thermal Information

| THERMAL METRIC <sup>(1)</sup> |                                              | REF30xx      | UNIT |
|-------------------------------|----------------------------------------------|--------------|------|
|                               |                                              | DBZ (SOT-23) |      |
|                               |                                              | 3 PINS       |      |
| R <sub>θJA</sub>              | Junction-to-ambient thermal resistance       | 297.3        | °C/W |
| R <sub>θJC(top)</sub>         | Junction-to-case (top) thermal resistance    | 128.5        | °C/W |
| R <sub>θJB</sub>              | Junction-to-board thermal resistance         | 91.7         | °C/W |
| ψ <sub>JT</sub>               | Junction-to-top characterization parameter   | 12.8         | °C/W |
| ψ <sub>JB</sub>               | Junction-to-board characterization parameter | 90.3         | °C/W |
| R <sub>θJC(bot)</sub>         | Junction-to-case (bottom) thermal resistance | N/A          | °C/W |

- (1) For more information about traditional and new thermal metrics, see the [Semiconductor and IC Package Thermal Metrics](#) application report.

## 7.5 Electrical Characteristics

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{ V}$ , and  $I_{LOAD} = 0\text{ mA}$  (unless otherwise noted)

| PARAMETER                             |                      | TEST CONDITIONS                                        | MIN    | TYP   | MAX    | UNIT                |
|---------------------------------------|----------------------|--------------------------------------------------------|--------|-------|--------|---------------------|
| <b>REF3012 (1.25 V)<sup>(1)</sup></b> |                      |                                                        |        |       |        |                     |
| $V_{OUT}$                             | Output voltage       |                                                        | 1.2475 | 1.25  | 1.2525 | V                   |
|                                       | Initial accuracy     |                                                        |        |       | 0.2%   |                     |
|                                       | Output voltage noise | $f = 0.1\text{ Hz to }10\text{ Hz}$                    |        | 14    |        | $\mu\text{V}_{PP}$  |
|                                       |                      | $f = 10\text{ Hz to }10\text{ kHz}$                    |        | 42    |        | $\mu\text{V}_{rms}$ |
|                                       | Line regulation      | $1.8\text{ V} \leq V_{IN} \leq 5.5\text{ V}$           |        | 60    | 190    | $\mu\text{V/V}$     |
| <b>REF3020 (2.048 V)</b>              |                      |                                                        |        |       |        |                     |
| $V_{OUT}$                             | Output voltage       |                                                        | 2.044  | 2.048 | 2.052  | V                   |
|                                       | Initial accuracy     |                                                        |        |       | 0.2%   |                     |
|                                       | Output voltage noise | $f = 0.1\text{ Hz to }10\text{ Hz}$                    |        | 23    |        | $\mu\text{V}_{PP}$  |
|                                       |                      | $f = 10\text{ Hz to }10\text{ kHz}$                    |        | 65    |        | $\mu\text{V}_{rms}$ |
|                                       | Line regulation      | $V_{REF} + 50\text{ mV} \leq V_{IN} \leq 5.5\text{ V}$ |        | 110   | 290    | $\mu\text{V/V}$     |
| <b>REF3025 (2.5 V)</b>                |                      |                                                        |        |       |        |                     |
| $V_{OUT}$                             | Output voltage       |                                                        | 2.495  | 2.50  | 2.505  | V                   |
|                                       | Initial accuracy     |                                                        |        |       | 0.2%   |                     |
|                                       | Output voltage noise | $f = 0.1\text{ Hz to }10\text{ Hz}$                    |        | 28    |        | $\mu\text{V}_{PP}$  |
|                                       |                      | $f = 10\text{ Hz to }10\text{ kHz}$                    |        | 80    |        | $\mu\text{V}_{rms}$ |
|                                       | Line regulation      | $V_{REF} + 50\text{ mV} \leq V_{IN} \leq 5.5\text{ V}$ |        | 120   | 325    | $\mu\text{V/V}$     |
| <b>REF3030 (3.0 V)</b>                |                      |                                                        |        |       |        |                     |
| $V_{OUT}$                             | Output voltage       |                                                        | 2.994  | 3.0   | 3.006  | V                   |
|                                       | Initial accuracy     |                                                        |        |       | 0.2%   |                     |
|                                       | Output voltage noise | $f = 0.1\text{ Hz to }10\text{ Hz}$                    |        | 33    |        | $\mu\text{V}_{PP}$  |
|                                       |                      | $f = 10\text{ Hz to }10\text{ kHz}$                    |        | 94    |        | $\mu\text{V}_{rms}$ |
|                                       | Line regulation      | $V_{REF} + 50\text{ mV} \leq V_{IN} \leq 5.5\text{ V}$ |        | 120   | 375    | $\mu\text{V/V}$     |
| <b>REF3033 (3.3 V)</b>                |                      |                                                        |        |       |        |                     |
| $V_{OUT}$                             | Output voltage       |                                                        | 3.294  | 3.30  | 3.306  | V                   |
|                                       | Initial accuracy     |                                                        |        |       | 0.2%   |                     |
|                                       | Output voltage noise | $f = 0.1\text{ Hz to }10\text{ Hz}$                    |        | 36    |        | $\mu\text{V}_{PP}$  |
|                                       |                      | $f = 10\text{ Hz to }10\text{ kHz}$                    |        | 105   |        | $\mu\text{V}_{rms}$ |
|                                       | Line regulation      | $V_{REF} + 50\text{ mV} \leq V_{IN} \leq 5.5\text{ V}$ |        | 130   | 400    | $\mu\text{V/V}$     |
| <b>REF3040 (4.096 V)</b>              |                      |                                                        |        |       |        |                     |
| $V_{OUT}$                             | Output voltage       |                                                        | 4.088  | 4.096 | 4.104  | V                   |
|                                       | Initial accuracy     |                                                        |        |       | 0.2%   |                     |
|                                       | Output voltage noise | $f = 0.1\text{ Hz to }10\text{ Hz}$                    |        | 45    |        | $\mu\text{V}_{PP}$  |
|                                       |                      | $f = 10\text{ Hz to }10\text{ kHz}$                    |        | 128   |        | $\mu\text{V}_{rms}$ |
|                                       | Line regulation      | $V_{REF} + 50\text{ mV} \leq V_{IN} \leq 5.5\text{ V}$ |        | 160   | 410    | $\mu\text{V/V}$     |

(1) The minimum supply voltage for the REF3012 is 1.8 V.

## Electrical Characteristics (continued)

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{ V}$ , and  $I_{LOAD} = 0\text{ mA}$  (unless otherwise noted)

| PARAMETER                                                             | TEST CONDITIONS                                                                    | MIN | TYP | MAX | UNIT                    |
|-----------------------------------------------------------------------|------------------------------------------------------------------------------------|-----|-----|-----|-------------------------|
| <b>REF30xx (REF3012, REF3020, REF3025, REF3030, REF3033, REF3040)</b> |                                                                                    |     |     |     |                         |
| $dV_{OUT}/dT$ Output voltage temperature drift <sup>(2)</sup>         | $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$                                   |     | 20  | 50  | ppm/ $^\circ\text{C}$   |
|                                                                       | $-30^\circ\text{C} \leq T_A \leq +85^\circ\text{C}$                                |     | 28  | 60  |                         |
|                                                                       | $-40^\circ\text{C} \leq T_A \leq +85^\circ\text{C}$                                |     | 30  | 65  |                         |
|                                                                       | $-40^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$                               |     | 35  | 75  |                         |
| Long-term stability                                                   | 0000h to 1000h                                                                     |     | 24  |     | ppm                     |
|                                                                       | 1000h to 2000h                                                                     |     | 15  |     |                         |
| $\Delta V_{O(\Delta IL)}$ Load regulation <sup>(3)</sup>              | $0\text{ mA} < I_{LOAD} < 25\text{ mA}$ , $V_{IN} = V_{REF} + 500\text{ mV}^{(4)}$ |     | 3   | 100 | $\mu\text{V}/\text{mA}$ |
| $dT$ Thermal hysteresis <sup>(4)</sup>                                |                                                                                    |     | 25  | 100 | ppm                     |
| $V_{IN} - V_{OUT}$ Dropout voltage                                    |                                                                                    |     | 1   | 50  | mV                      |
| $I_{SC}$ Short-circuit current                                        |                                                                                    |     | 45  |     | mA                      |
| Turnon settling time                                                  | To 0.1% with $C_L = 1\text{ }\mu\text{F}$                                          |     | 120 |     | $\mu\text{s}$           |
| <b>POWER SUPPLY</b>                                                   |                                                                                    |     |     |     |                         |
| $I_Q$ Quiescent current                                               |                                                                                    |     | 42  | 50  | $\mu\text{A}$           |
|                                                                       | $-40^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$                               |     |     | 59  |                         |

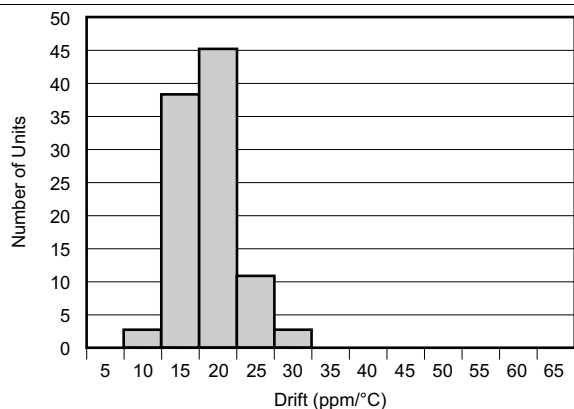
(2) Box method used to determine over temperature drift.

(3) Typical value of load regulation reflects measurements using a force and sense contacts; see [Load Regulation](#) section.

(4) Thermal hysteresis procedure explained in more detail in [Thermal Hysteresis](#) section.

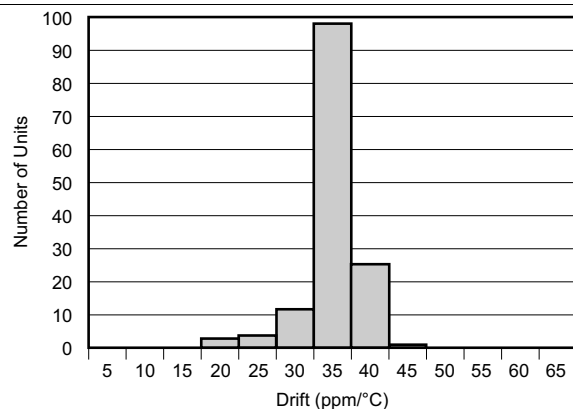
## 7.6 Typical Characteristics

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{ V}$ , and REF3025 used for typical characteristics (unless otherwise noted)



0°C to 70°C

Figure 1. Temperature Drift



-40°C to +125°C

Figure 2. Temperature Drift

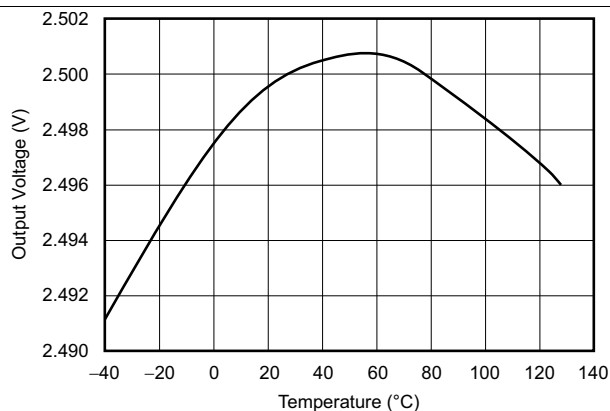


Figure 3. Output Voltage vs Temperature

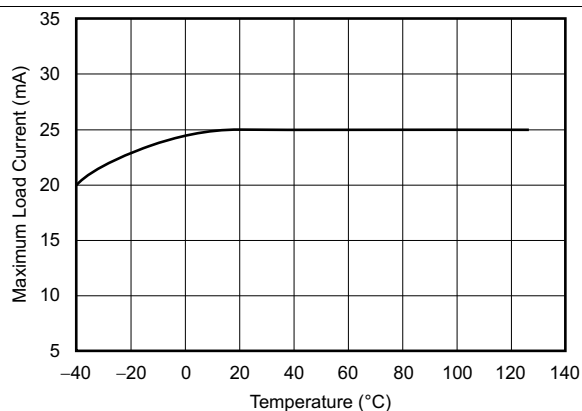


Figure 4. Maximum Load Current vs Temperature

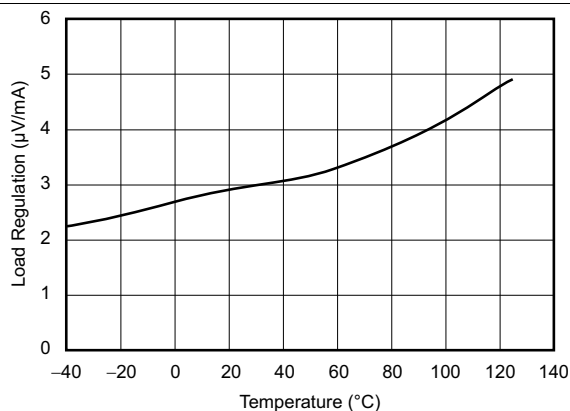


Figure 5. Load Regulation vs Temperature

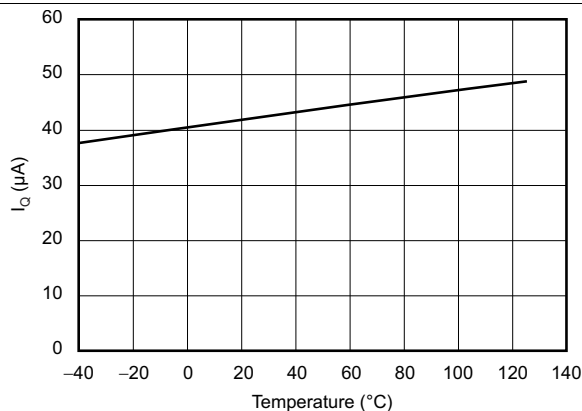


Figure 6. Quiescent Current vs Temperature

## Typical Characteristics (continued)

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{ V}$ , and REF3025 used for typical characteristics (unless otherwise noted)

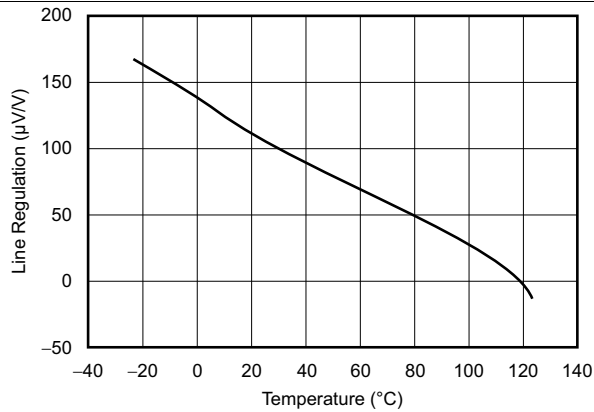


Figure 7. Line Regulation vs Temperature

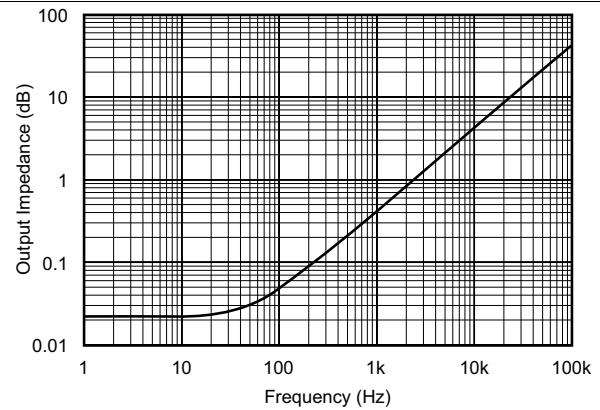


Figure 8. Output Impedance vs Frequency

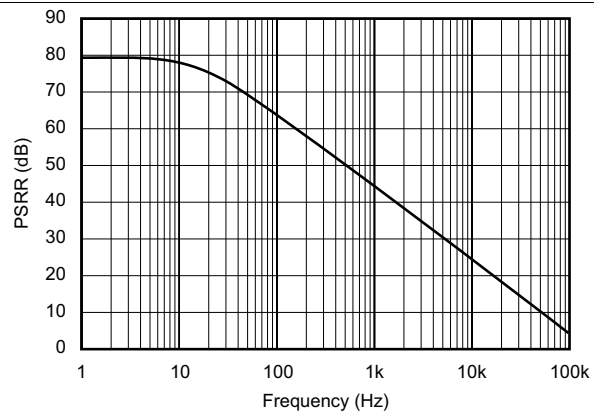
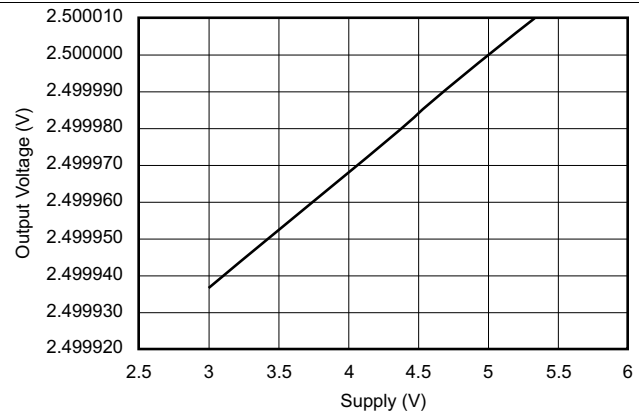
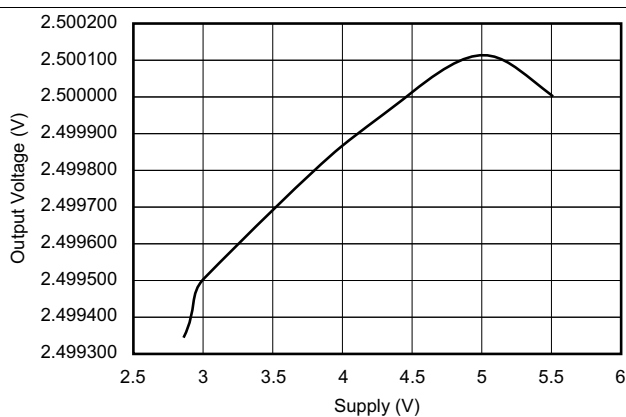


Figure 9. Power-Supply Rejection Ratio vs Frequency



No Load

Figure 10. Output Voltage vs Supply Voltage



$I_{LOAD} = 25\text{ mA}$

Figure 11. Output Voltage vs Supply Voltage

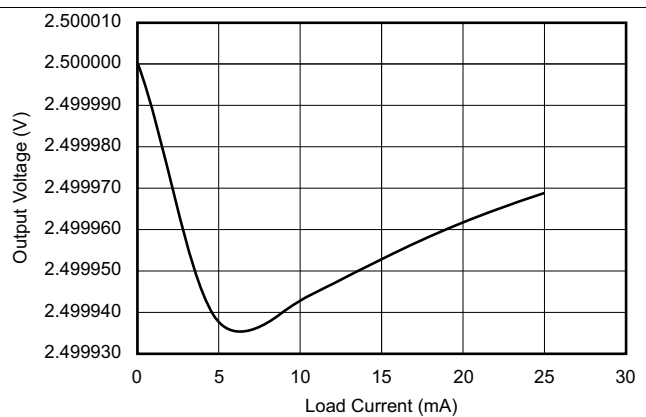
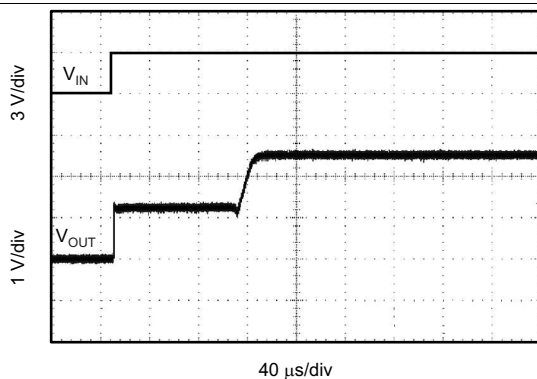


Figure 12. Output Voltage vs Load Current

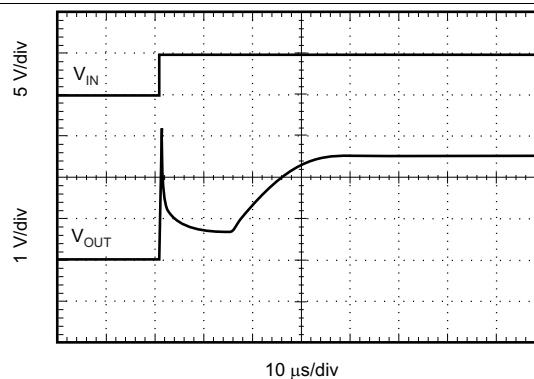
## Typical Characteristics (continued)

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{ V}$ , and REF3025 used for typical characteristics (unless otherwise noted)



$C_L = 0$ , 3-V startup

Figure 13. Step Response



$C_L = 0$ , 5-V startup

Figure 14. Step Response

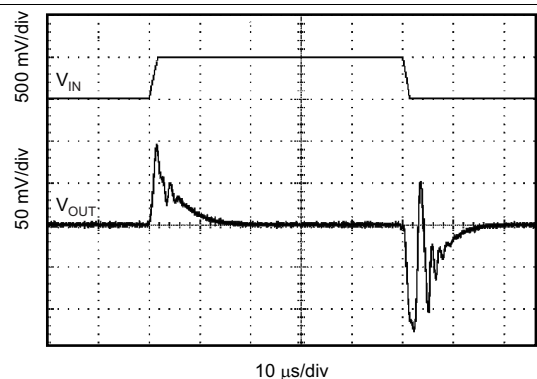
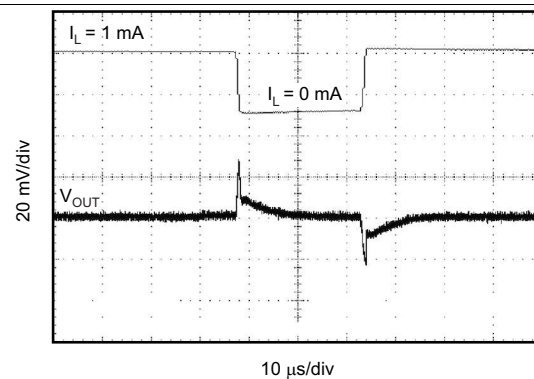
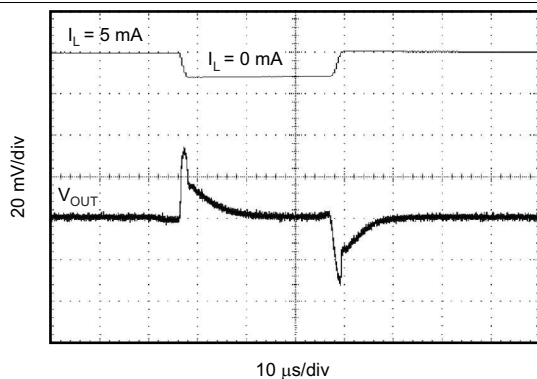


Figure 15. Line Transient Response



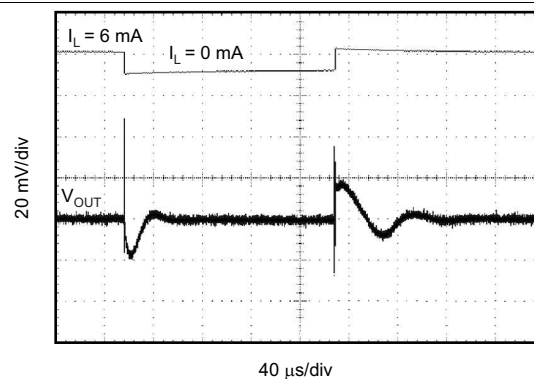
$C_L = 0$

Figure 16. 0-mA to 1-mA Load Transient



$C_L = 0$

Figure 17. 0-mA to 5-mA Load Transient



$C_L = 1\text{ }\mu\text{F}$

Figure 18. 1-mA to 6-mA Load Transient

## Typical Characteristics (continued)

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{ V}$ , and REF3025 used for typical characteristics (unless otherwise noted)

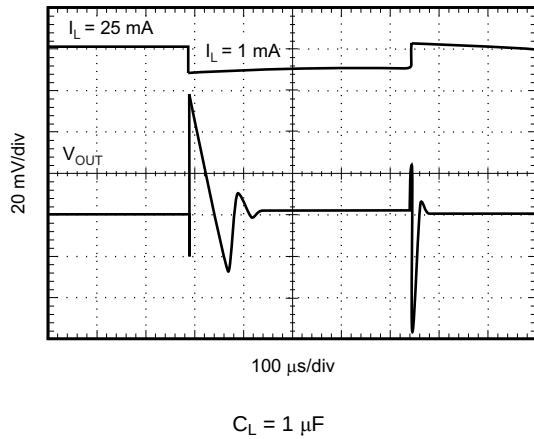


Figure 19. 1-mA to 25-mA Load Transient

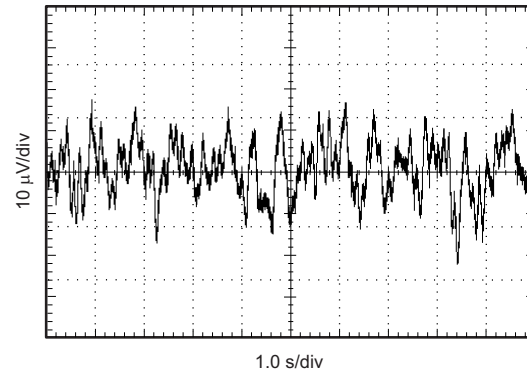


Figure 20. 0.1-Hz to 10-Hz Noise

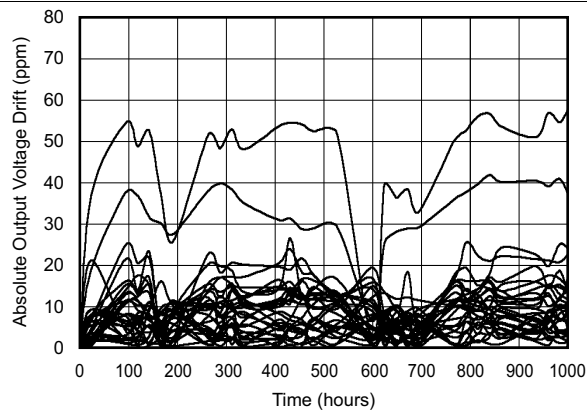


Figure 21. Long-Term Stability: 0 to 1000 Hours

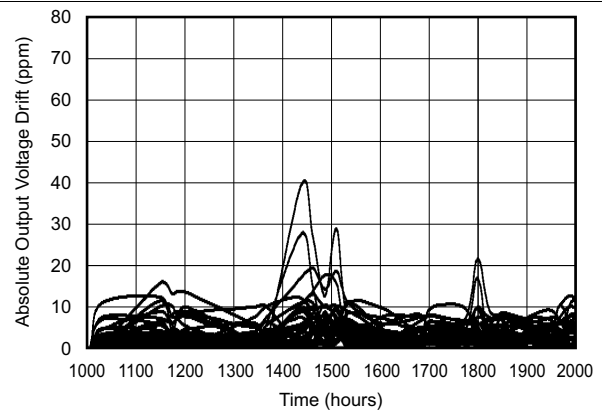


Figure 22. Long-Term Stability: 1000 to 2000 Hours

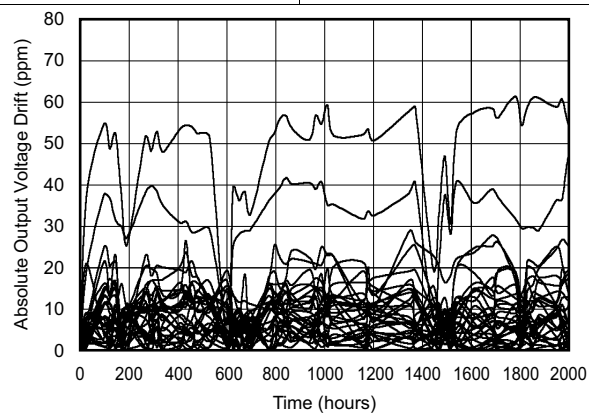


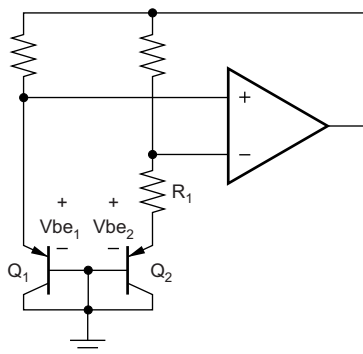
Figure 23. Long-Term Stability: 0 to 2000 Hours

## 8 Detailed Description

### 8.1 Overview

The REF30xx is a series, CMOS, precision bandgap voltage reference. Its basic topology is shown in the [Functional Block Diagram](#) section. Transistors  $Q_1$  and  $Q_2$  are biased so that the current density of  $Q_1$  is greater than that of  $Q_2$ . The difference of the two base-emitter voltages,  $V_{be1} - V_{be2}$ , has a positive temperature coefficient and is forced across resistor  $R_1$ . This voltage is gained up and added to the base-emitter voltage of  $Q_2$ , which has a negative coefficient. The resulting output voltage is virtually independent of temperature. The curvature of the bandgap voltage, as shown in [Figure 3](#), is due to the slightly nonlinear temperature coefficient of the base-emitter voltage of  $Q_2$ .

### 8.2 Functional Block Diagram



Copyright © 2016, Texas Instruments Incorporated

### 8.3 Feature Description

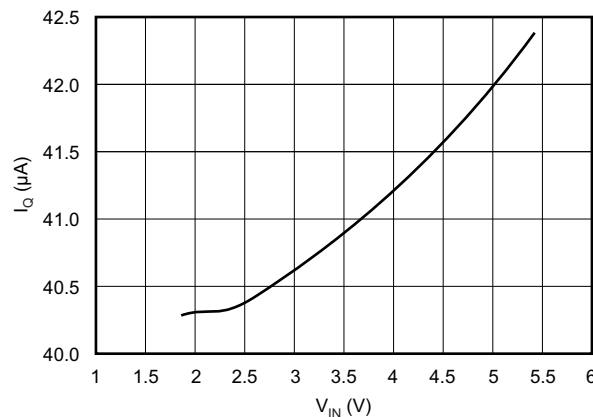
#### 8.3.1 Supply Voltage

The REF30xx family of references features an extremely low dropout voltage. With the exception of the REF3012, which has a minimum supply requirement of 1.8 V, the REF30xx can be operated with a supply of only 1 mV above the output voltage in an unloaded condition. For loaded conditions, a typical dropout voltage versus load is shown on the front page.

The REF30xx features a low quiescent current that is extremely stable over changes in both temperature and supply. The typical room temperature quiescent current is 42  $\mu\text{A}$ , and the maximum quiescent current over temperature is just 59  $\mu\text{A}$ . Additionally, the quiescent current typically changes less than 2.5  $\mu\text{A}$  over the entire supply range, as shown in [Figure 24](#).

Supply voltages below the specified levels can cause the REF30xx to momentarily draw currents greater than the typical quiescent current. Use a power supply with a fast rising edge and low output impedance to easily prevent this issue.

## Feature Description (continued)



**Figure 24. Supply Current vs Supply Voltage**

### 8.3.2 Thermal Hysteresis

Thermal hysteresis for the REF30xx is defined as the change in output voltage after operating the device at 25°C, cycling the device through the specified temperature range, and returning to 25°C, and can be expressed as shown in [Equation 1](#):

$$V_{\text{HYST}} = \left( \frac{\text{abs}|V_{\text{PRE}} - V_{\text{POST}}|}{V_{\text{NOM}}} \right) \cdot 10^6 \text{ (ppm)}$$

where

- $V_{\text{HYST}}$  = Calculated hysteresis
- $V_{\text{PRE}}$  = Output voltage measured at 25°C pretemperature cycling
- $V_{\text{POST}}$  = Output voltage measured when device has been operated at 25°C, cycled through specified range of –40°C to +125°C, and returned to operation at 25°C. (1)

### 8.3.3 Temperature Drift

The REF30xx exhibits minimal drift error, defined as the change in output voltage over varying temperature. Using the *box* method of drift measurement, the REF30xx features a typical drift coefficient of 20 ppm from 0°C to 70°C, the primary temperature range of use for many applications. For industrial temperature ranges of –40°C to +125°C, the REF30xx family drift increases to a typical value of 50 ppm.

### 8.3.4 Noise Performance

The REF30xx generates noise less than 50  $\mu\text{V}_{\text{PP}}$  between frequencies of 0.1 Hz to 10 Hz, and can be seen in [Figure 20](#). The noise voltage of the REF30xx increases with output voltage and operating temperature. Additional filtering may be used to improve output noise levels; however, ensure the output impedance does not degrade AC performance.

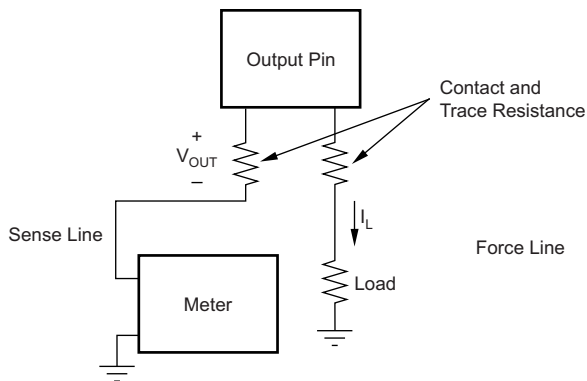
### 8.3.5 Long-Term Stability

Long-term stability refers to the change of the output voltage of a reference over a period of months or years. This effect lessens as time progresses as is apparent by the long-term stability curves. The typical drift value for the REF30xx is 24 ppm from 0 hours to 1000 hours, and 15 ppm from 1000 hours to 2000 hours. This parameter is characterized by measuring 30 units at regular intervals for a period of 2000 hours.

## Feature Description (continued)

### 8.3.6 Load Regulation

Load regulation is defined as the change in output voltage as a result of changes in load current. Load regulation for the REF30xx is measured using force and sense contacts as shown in Figure 25. The force and sense lines tied to the contact area of the output pin reduce the impact of contact and trace resistance, resulting in accurate measurement of the load regulation contributed solely by the REF30xx. For applications requiring improved load regulation, use force and sense lines.



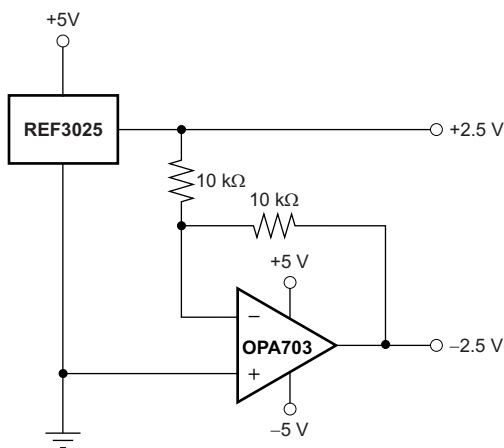
Copyright © 2016, Texas Instruments Incorporated

Figure 25. Accurate Load Regulation of REF30xx

## 8.4 Device Functional Modes

### 8.4.1 Negative Reference Voltage

For applications requiring a negative and positive reference voltage, the OPA703 and REF30xx can be used to provide a dual-supply reference from a  $\pm 5$ -V supply. Figure 26 shows the REF3025 used to provide a  $\pm 2.5$ -V supply reference voltage. The low offset voltage and low drift of the OPA703 complement the low drift performance of the REF30xx to provide an accurate solution for split-supply applications.



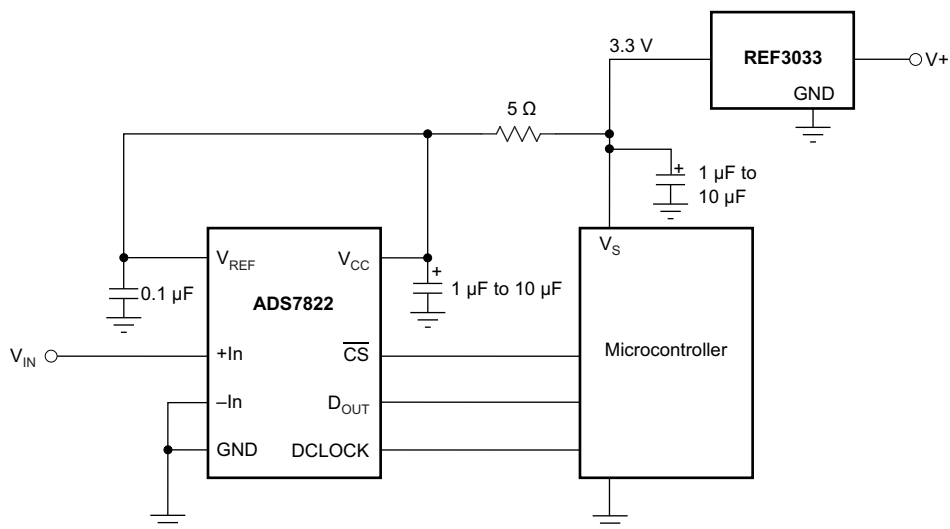
Copyright © 2016, Texas Instruments Incorporated

Figure 26. REF3025 Combined With OPA703 to Create Positive and Negative Reference Voltages.

## Device Functional Modes (continued)

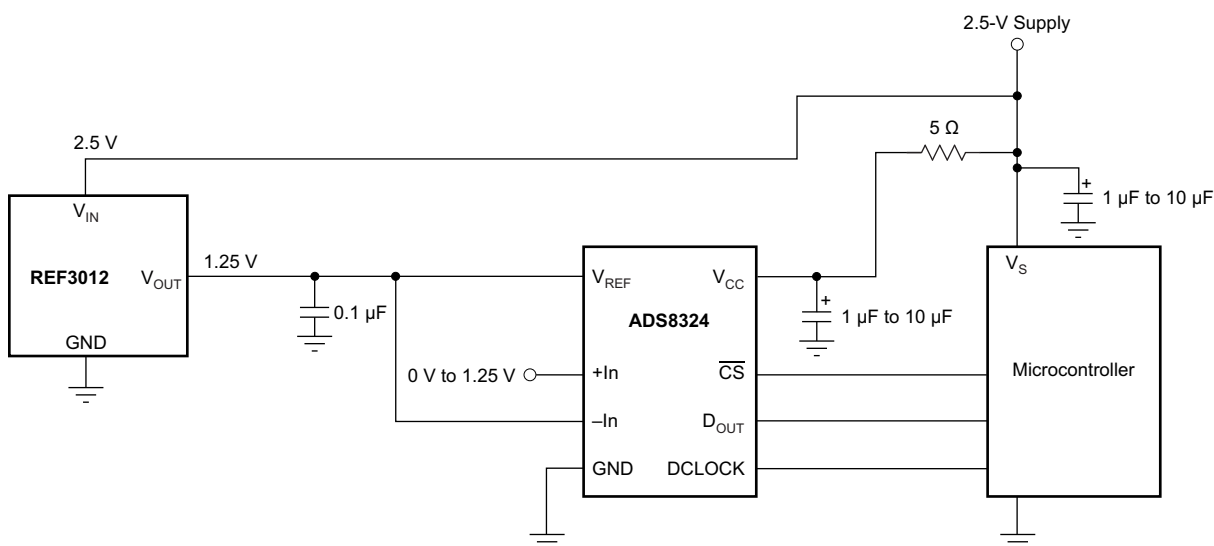
### 8.4.2 Data Acquisition

Often data acquisition systems require stable voltage references to maintain necessary accuracy. The REF30xx family features stability and a wide range of voltages suitable for most microcontrollers and data converters. [Figure 27](#) and [Figure 28](#) show two basic data acquisition systems.



Copyright © 2016, Texas Instruments Incorporated

**Figure 27. Basic Data Acquisition System 1**



Copyright © 2016, Texas Instruments Incorporated

**Figure 28. Basic Data Acquisition System 2**

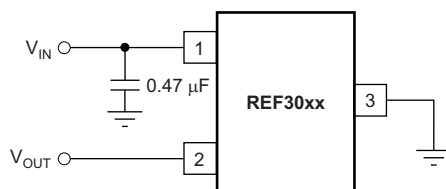
## 9 Application and Implementation

### NOTE

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

### 9.1 Application Information

For normal operation, the REF30xx does not require a capacitor on the output. If a capacitive load is connected, take special care when using low equivalent series resistance (ESR) capacitors and high capacitance. This precaution is especially true for low-output voltage devices; therefore, for the REF3012 use a low-ESR capacitance of 10  $\mu$ F or less. Figure 29 shows the typical connections required for operation of the REF30xx. A supply bypass capacitor of 0.47  $\mu$ F is always recommended.

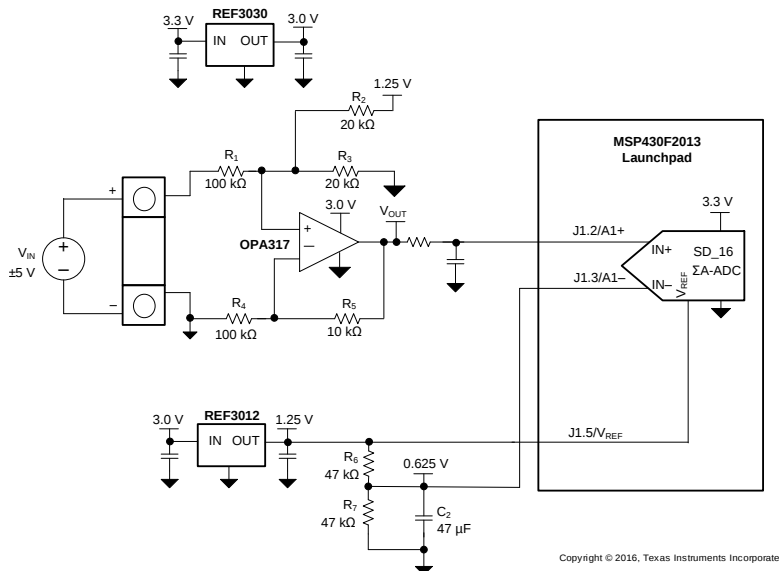


Copyright © 2016, Texas Instruments Incorporated

Figure 29. Typical Connections for Operating REF30xx

### 9.2 Typical Application

Figure 30 shows a low-power reference and conditioning circuit. This circuit attenuates and level-shifts a bipolar input voltage within the proper input range of a single-supply low power 16-Bit  $\Delta\Sigma$  ADC, such as the one inside the MSP430 or other similar single-supply ADCs. Precision reference circuits are used to level-shift the input signal, provide the ADC reference voltage and to create a well-regulated supply voltage for the low-power analog circuitry. A low-power, zero-drift, op-amp circuit is used to attenuate and level-shift the input signal.



Copyright © 2016, Texas Instruments Incorporated

Figure 30. Low-Power Reference and Bipolar Voltage Conditioning Circuit for Low-Power ADCs

## Typical Application (continued)

### 9.2.1 Design Requirements

- Supply Voltage: 3.3 V
- Maximum Input Voltage:  $\pm 6$  V
- Specified Input Voltage:  $\pm 5$  V
- ADC Reference Voltage: 1.25 V

The goal for this design is to accurately condition a  $\pm 5$ -V bipolar input voltage into a voltage suitable for conversion by a low-voltage ADC with a 1.25-V reference voltage,  $V_{REF}$ , and an input voltage range of  $V_{REF} / 2$ . The circuit should function with reduced performance over a wider input range of at least  $\pm 6$  V to allow for easier protection of overvoltage conditions.

### 9.2.2 Detailed Design Procedure

Figure 30 depicts a simplified schematic for this design showing the MSP430 ADC inputs and full input conditioning circuitry. The ADC is configured for a bipolar measurement where final conversion result is the differential voltage between the voltage at the positive and negative ADC inputs. The bipolar, GND-referenced input signal must be level-shifted and attenuated by the op amp so that the output is biased to  $V_{REF} / 2$  and has a differential voltage that is within the  $\pm V_{REF} / 2$  input range of the ADC.

### 9.2.3 Application Curves

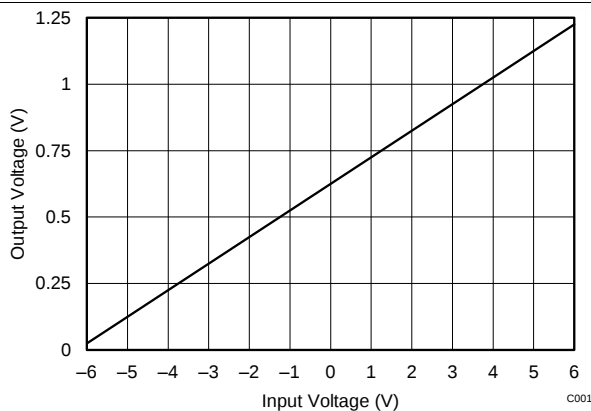


Figure 31. OPA317 Output Voltage vs Input Voltage

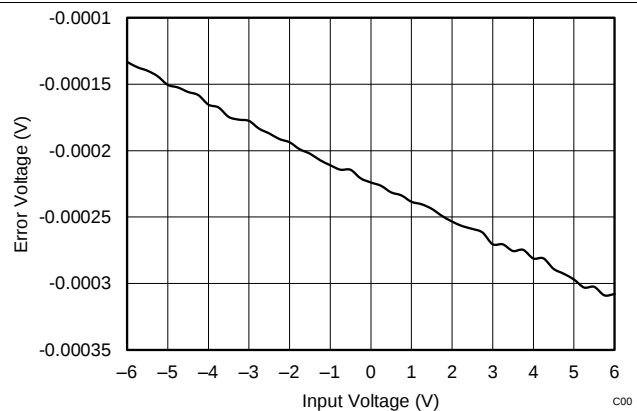


Figure 32. OPA317 Output Voltage Error vs Input Voltage

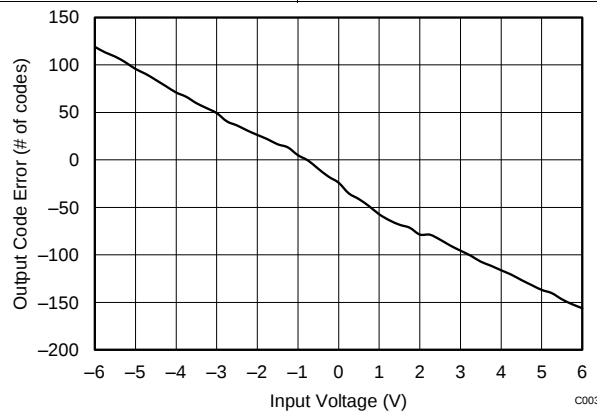


Figure 33. Output Code Error vs Input Voltage

## 10 Power Supply Recommendations

The REF30xx family of references feature an extremely low-dropout voltage. These references can be operated with a supply of only 50 mV above the output voltage. For loaded reference conditions, a typical dropout voltage versus load is shown in the front page plot, [Dropout Voltage vs Load Current](#). Use a supply bypass capacitor greater than 0.47  $\mu$ F.

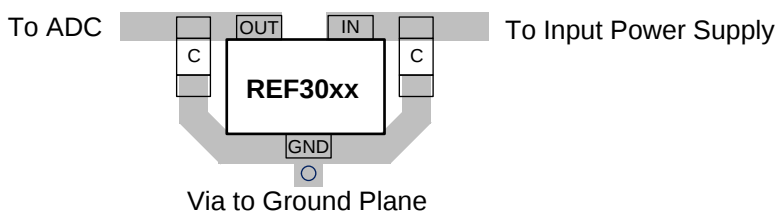
## 11 Layout

### 11.1 Layout Guidelines

[Figure 34](#) illustrates an example of a printed-circuit board (PCB) layout using the REF30xx. Some key considerations are:

- Connect low-ESR, 0.1- $\mu$ F ceramic bypass capacitors at  $V_{IN}$  of the REF30xx
- Decouple other active devices in the system per the device specifications
- Use a solid ground plane to help distribute heat and reduces electromagnetic interference (EMI) noise pickup
- Place the external components as close to the device as possible. This configuration prevents parasitic errors (such as the Seebeck effect) from occurring
- Minimize trace length between the reference and bias connections to the INA and ADC to reduce noise pickup
- Do not run sensitive analog traces in parallel with digital traces. Avoid crossing digital and analog traces if possible, and only make perpendicular crossings when absolutely necessary

### 11.2 Layout Example



**Figure 34. Layout Example**

## 12 Device and Documentation Support

### 12.1 Documentation Support

#### 12.1.1 Related Documentation

[CMOS, Rail-to-Rail, I/O Operational Amplifiers](#) (SBOS180)

[REF29xx 100 ppm/°C, 50  \$\mu\$ A in 3-Pin SOT-23 CMOS Voltage Reference](#) (SBVS033)

### 12.2 Related Links

**Table 1** lists quick access links. Categories include technical documents, support and community resources, tools and software, and quick access to sample or buy.

**Table 1. Related Links**

| PARTS   | PRODUCT FOLDER             | SAMPLE & BUY               | TECHNICAL DOCUMENTS        | TOOLS & SOFTWARE           | SUPPORT & COMMUNITY        |
|---------|----------------------------|----------------------------|----------------------------|----------------------------|----------------------------|
| REF3012 | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> |
| REF3020 | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> |
| REF3025 | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> |
| REF3030 | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> |
| REF3033 | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> |
| REF3040 | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> | <a href="#">Click here</a> |

### 12.3 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on ti.com. In the upper right corner, click on *Alert me* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

### 12.4 Community Resources

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's [Terms of Use](#).

**TI E2E™ Online Community** *TI's Engineer-to-Engineer (E2E) Community*. Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

**Design Support** *TI's Design Support* Quickly find helpful E2E forums along with design support tools and contact information for technical support.

### 12.5 Trademarks

E2E is a trademark of Texas Instruments.

All other trademarks are the property of their respective owners.

### 12.6 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 12.7 Glossary

[SLYZ022](#) — *TI Glossary*.

This glossary lists and explains terms, acronyms, and definitions.

## 13 Mechanical, Packaging, and Orderable Information

The following pages include mechanical packaging and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

**PACKAGING INFORMATION**

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)            | Lead/Ball Finish<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|----------------------------|-------------------------|----------------------|--------------|-------------------------|-------------------------|
| REF3012AIDBZR    | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30A                    | <a href="#">Samples</a> |
| REF3012AIDBZRG4  | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30A                    | <a href="#">Samples</a> |
| REF3012AIDBZT    | ACTIVE        | SOT-23       | DBZ                | 3    | 250            | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30A                    | <a href="#">Samples</a> |
| REF3012AIDBZTG4  | ACTIVE        | SOT-23       | DBZ                | 3    | 250            | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30A                    | <a href="#">Samples</a> |
| REF3020AIDBZR    | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30B                    | <a href="#">Samples</a> |
| REF3020AIDBZRG4  | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30B                    | <a href="#">Samples</a> |
| REF3020AIDBZT    | ACTIVE        | SOT-23       | DBZ                | 3    | 250            | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30B                    | <a href="#">Samples</a> |
| REF3020AIDBZTG4  | ACTIVE        | SOT-23       | DBZ                | 3    | 250            | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30B                    | <a href="#">Samples</a> |
| REF3025AIDBZR    | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30C                    | <a href="#">Samples</a> |
| REF3025AIDBZRG4  | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30C                    | <a href="#">Samples</a> |
| REF3025AIDBZT    | ACTIVE        | SOT-23       | DBZ                | 3    | 250            | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30C                    | <a href="#">Samples</a> |
| REF3025AIDBZTG4  | ACTIVE        | SOT-23       | DBZ                | 3    | 250            | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30C                    | <a href="#">Samples</a> |
| REF3030AIDBZR    | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30F                    | <a href="#">Samples</a> |
| REF3030AIDBZRG4  | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30F                    | <a href="#">Samples</a> |
| REF3030AIDBZT    | ACTIVE        | SOT-23       | DBZ                | 3    | 250            | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30F                    | <a href="#">Samples</a> |
| REF3030AIDBZTG4  | ACTIVE        | SOT-23       | DBZ                | 3    | 250            | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30F                    | <a href="#">Samples</a> |
| REF3033AIDBZR    | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30D                    | <a href="#">Samples</a> |

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)            | Lead/Ball Finish<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5) | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|----------------------------|-------------------------|----------------------|--------------|-------------------------|-------------------------|
| REF3033AIDBZRG4  | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30D                    | <a href="#">Samples</a> |
| REF3033AIDBZT    | ACTIVE        | SOT-23       | DBZ                | 3    | 250            | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30D                    | <a href="#">Samples</a> |
| REF3033AIDBZTG4  | ACTIVE        | SOT-23       | DBZ                | 3    | 250            | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30D                    | <a href="#">Samples</a> |
| REF3040AIDBZR    | ACTIVE        | SOT-23       | DBZ                | 3    | 3000           | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30E                    | <a href="#">Samples</a> |
| REF3040AIDBZT    | ACTIVE        | SOT-23       | DBZ                | 3    | 250            | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30E                    | <a href="#">Samples</a> |
| REF3040AIDBZTG4  | ACTIVE        | SOT-23       | DBZ                | 3    | 250            | Green (RoHS<br>& no Sb/Br) | NIPDAUAG                | Level-1-260C-UNLIM   | -40 to 125   | R30E                    | <a href="#">Samples</a> |

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "-" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

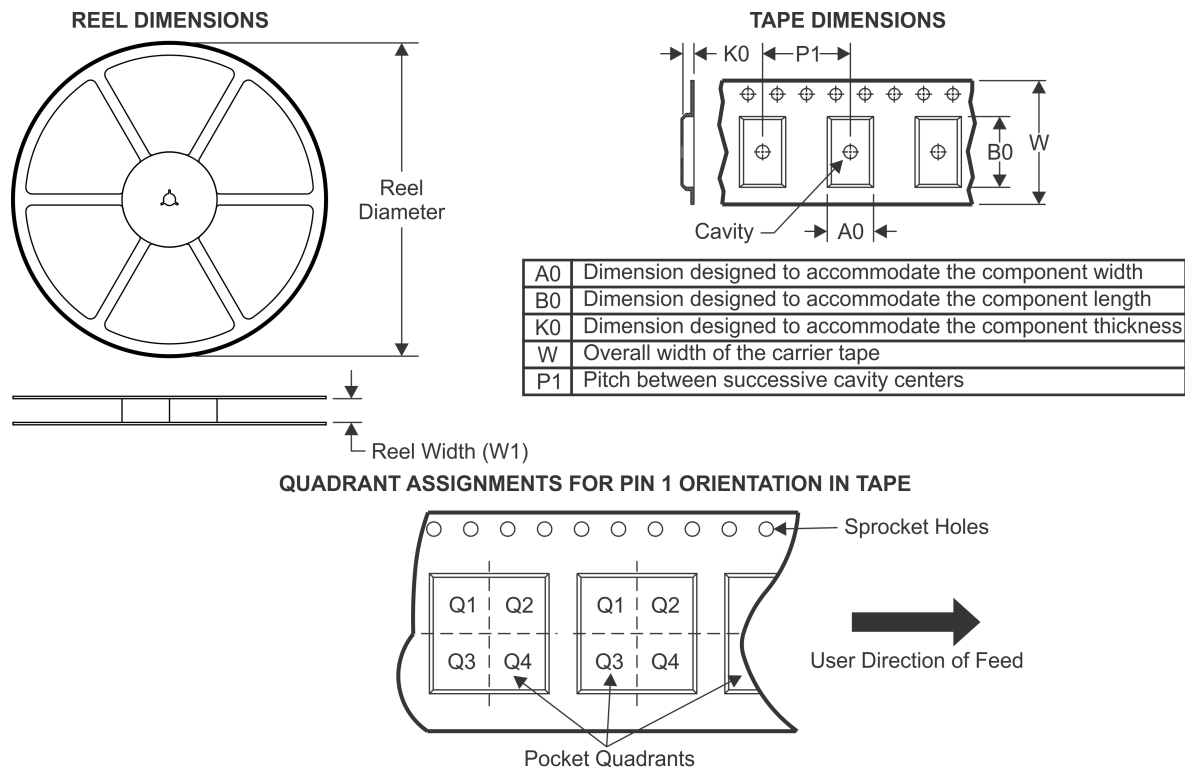
In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

**OTHER QUALIFIED VERSIONS OF REF3033 :**

- Automotive: [REF3033-Q1](#)

NOTE: Qualified Version Definitions:

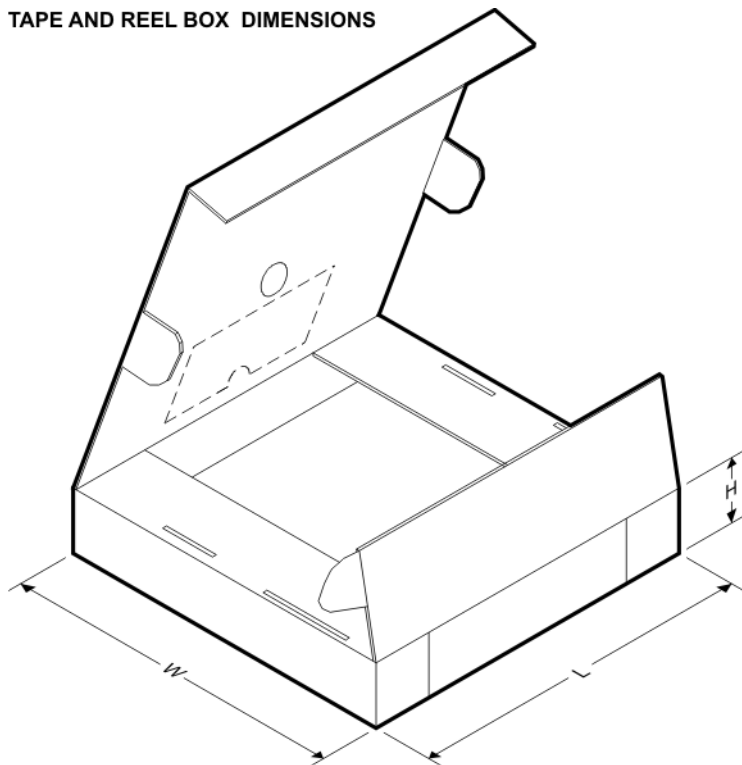
- Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects

**TAPE AND REEL INFORMATION**


\*All dimensions are nominal

| Device        | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|---------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| REF3012AIDBZR | SOT-23       | DBZ             | 3    | 3000 | 179.0              | 8.4                | 3.15    | 2.95    | 1.22    | 4.0     | 8.0    | Q3            |
| REF3012AIDBZT | SOT-23       | DBZ             | 3    | 250  | 179.0              | 8.4                | 3.15    | 2.95    | 1.22    | 4.0     | 8.0    | Q3            |
| REF3020AIDBZR | SOT-23       | DBZ             | 3    | 3000 | 179.0              | 8.4                | 3.15    | 2.95    | 1.22    | 4.0     | 8.0    | Q3            |
| REF3020AIDBZT | SOT-23       | DBZ             | 3    | 250  | 179.0              | 8.4                | 3.15    | 2.95    | 1.22    | 4.0     | 8.0    | Q3            |
| REF3025AIDBZR | SOT-23       | DBZ             | 3    | 3000 | 180.0              | 8.4                | 3.15    | 2.95    | 1.22    | 4.0     | 8.0    | Q3            |
| REF3025AIDBZT | SOT-23       | DBZ             | 3    | 250  | 179.0              | 8.4                | 3.15    | 2.95    | 1.22    | 4.0     | 8.0    | Q3            |
| REF3030AIDBZR | SOT-23       | DBZ             | 3    | 3000 | 179.0              | 8.4                | 3.15    | 2.95    | 1.22    | 4.0     | 8.0    | Q3            |
| REF3030AIDBZT | SOT-23       | DBZ             | 3    | 250  | 179.0              | 8.4                | 3.15    | 2.95    | 1.22    | 4.0     | 8.0    | Q3            |
| REF3033AIDBZR | SOT-23       | DBZ             | 3    | 3000 | 179.0              | 8.4                | 3.15    | 2.95    | 1.22    | 4.0     | 8.0    | Q3            |
| REF3033AIDBZT | SOT-23       | DBZ             | 3    | 250  | 179.0              | 8.4                | 3.15    | 2.95    | 1.22    | 4.0     | 8.0    | Q3            |
| REF3040AIDBZR | SOT-23       | DBZ             | 3    | 3000 | 179.0              | 8.4                | 3.15    | 2.95    | 1.22    | 4.0     | 8.0    | Q3            |
| REF3040AIDBZT | SOT-23       | DBZ             | 3    | 250  | 179.0              | 8.4                | 3.15    | 2.95    | 1.22    | 4.0     | 8.0    | Q3            |

## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

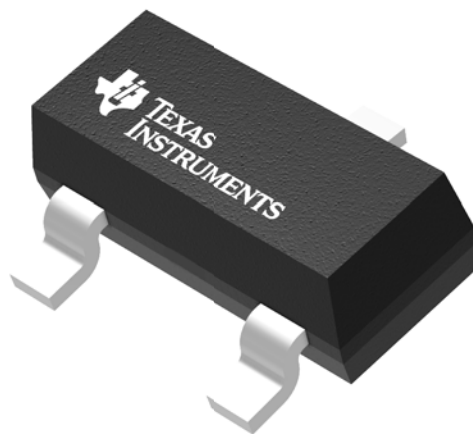
| Device        | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|---------------|--------------|-----------------|------|------|-------------|------------|-------------|
| REF3012AIDBZR | SOT-23       | DBZ             | 3    | 3000 | 203.0       | 203.0      | 35.0        |
| REF3012AIDBZT | SOT-23       | DBZ             | 3    | 250  | 203.0       | 203.0      | 35.0        |
| REF3020AIDBZR | SOT-23       | DBZ             | 3    | 3000 | 203.0       | 203.0      | 35.0        |
| REF3020AIDBZT | SOT-23       | DBZ             | 3    | 250  | 203.0       | 203.0      | 35.0        |
| REF3025AIDBZR | SOT-23       | DBZ             | 3    | 3000 | 203.0       | 203.0      | 35.0        |
| REF3025AIDBZT | SOT-23       | DBZ             | 3    | 250  | 203.0       | 203.0      | 35.0        |
| REF3030AIDBZR | SOT-23       | DBZ             | 3    | 3000 | 203.0       | 203.0      | 35.0        |
| REF3030AIDBZT | SOT-23       | DBZ             | 3    | 250  | 203.0       | 203.0      | 35.0        |
| REF3033AIDBZR | SOT-23       | DBZ             | 3    | 3000 | 203.0       | 203.0      | 35.0        |
| REF3033AIDBZT | SOT-23       | DBZ             | 3    | 250  | 203.0       | 203.0      | 35.0        |
| REF3040AIDBZR | SOT-23       | DBZ             | 3    | 3000 | 203.0       | 203.0      | 35.0        |
| REF3040AIDBZT | SOT-23       | DBZ             | 3    | 250  | 203.0       | 203.0      | 35.0        |

## GENERIC PACKAGE VIEW

**DBZ 3**

**SOT-23 - 1.12 mm max height**

SMALL OUTLINE TRANSISTOR



Images above are just a representation of the package family, actual package may vary.  
Refer to the product data sheet for package details.

4203227/C

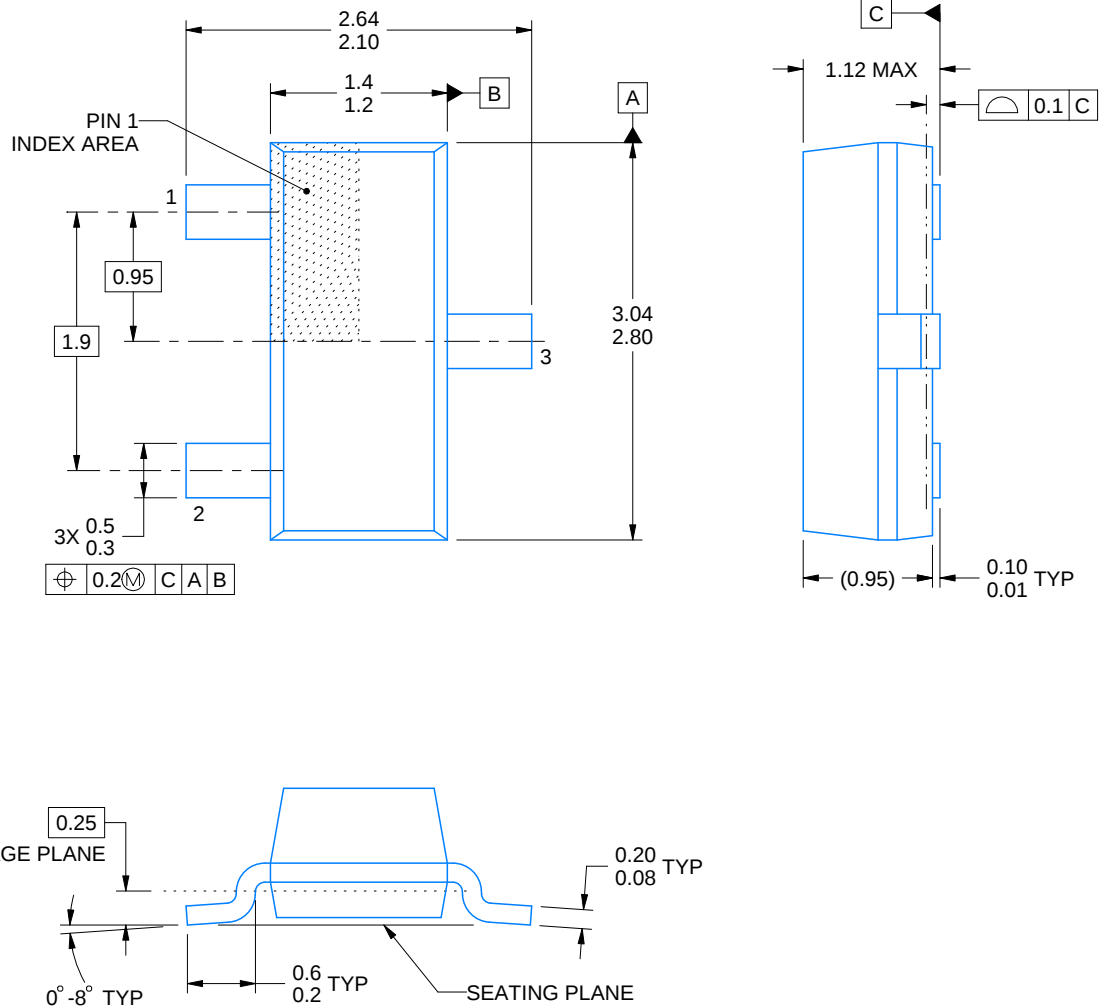
DBZ0003A



## PACKAGE OUTLINE

SOT-23 - 1.12 mm max height

SMALL OUTLINE TRANSISTOR



4214838/C 04/2017

### NOTES:

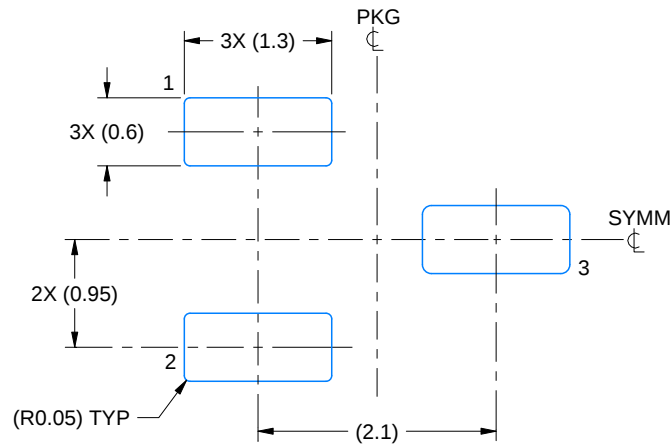
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC registration TO-236, except minimum foot length.

# EXAMPLE BOARD LAYOUT

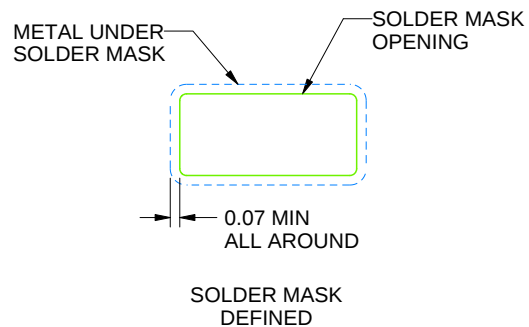
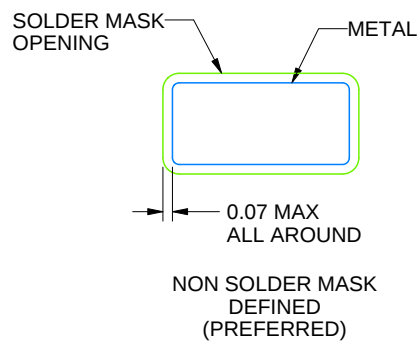
DBZ0003A

SOT-23 - 1.12 mm max height

SMALL OUTLINE TRANSISTOR



LAND PATTERN EXAMPLE  
SCALE:15X



SOLDER MASK DETAILS

4214838/C 04/2017

NOTES: (continued)

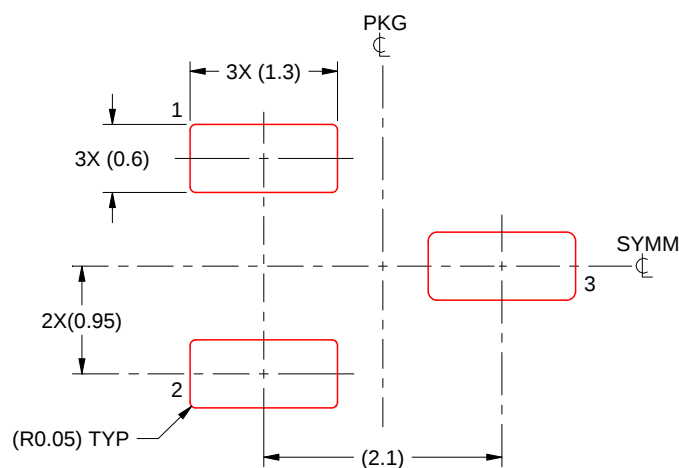
4. Publication IPC-7351 may have alternate designs.
5. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

## EXAMPLE STENCIL DESIGN

DBZ0003A

SOT-23 - 1.12 mm max height

SMALL OUTLINE TRANSISTOR



SOLDER PASTE EXAMPLE  
BASED ON 0.125 THICK STENCIL  
SCALE:15X

4214838/C 04/2017

NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
7. Board assembly site may have different recommendations for stencil design.

## IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale ([www.ti.com/legal/termsofsale.html](http://www.ti.com/legal/termsofsale.html)) or other applicable terms available either on [ti.com](http://ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2020, Texas Instruments Incorporated