



Platform Flash PROM Data Sheet (DS123) and User Guide (UG161) Revisions

XCN09029 (v1.0) October 26, 2009

Customer Notification – For Your Information

Overview

The purpose of this notification is to communicate some revisions to the Platform Flash PROM DS123, v2.17, data sheet and UG161, v1.5, user guide. The Platform Flash PROM family includes the XCF01S, XCF02S, XCF04S, XCF08P, XCF16P, and XCF32P devices. There is no change to the form, fit, or function of these devices.

Note that this notification applies to Commercial (C) grade XCFxxS and XCFxxP PROM products only. This notification does not apply to the Platform Flash XL (XCF128X) device or Military XQF32PVO48M grade device.

Description

The Platform Flash PROM DS123 data sheet and UG161 user guide have been revised according to Xilinx product documentation standards such that the DS123 data sheet is the primary location for product specifications and the UG161 user guide is the primary location for product usage information. These revisions also include clarifications for additional product support and usage. The details of the key revisions are listed below to insure identification of information that has changed locations, has been clarified, or has been listed as important. All the revisions are also listed within the Revision History sections of DS123 data sheet and UG161 user guide. There is no change to the form, fit, or function of these devices.

Key revisions in DS123, Platform Flash In-System Programmable Configuration PROMs data sheet, v2.17:

- Removed empty placeholders for XCFxxP VCCO AC/DC Characteristics at 1.5V operation. Although the product plans included extension of the VCCO operation down to 1.5V, the characteristics of XCFxxP VCCO operation at 1.5V were never specified and the empty specification placeholders have been removed along with all references to XCFxxP VCCO operation at 1.5V.
- Moved product user guidance table of “Xilinx FPGAs and Compatible Platform Flash PROMs” from the DS123 data sheet to the UG161 user guide.
- Removed “PROM to FPGA Configuration Mode and Connections Summary” section that was a redundant and less complete form of the information contained in the UG161 user guide and the FPGA configuration user guides.
- Removed “Configuration PROM-to-FPGA Device Interface Connection Diagrams” that were a redundant and a less complete set of reference schematics contained in the UG161 user guide.

Key revisions in UG161, Platform Flash PROM User Guide, v1.5:

- Removed references to the never-specified XCFxxP VCCO operation at 1.5V. See the “Removed empty placeholders for XCFxxP VCCO AC/DC Characteristics at 1.5V operation” item in the DS123 data sheet revision list, above.
- Added product user guidance table of “Xilinx FPGAs and Compatible Platform Flash PROMs” that was moved from the DS123 data sheet to the UG161 user guide.
- Added Spartan-6 and Virtex-6 FPGAs to the table of “Xilinx FPGAs and Compatible Platform Flash PROMs”.
- Replaced example third-party voltage regulator part number with reference to Xilinx Power Solutions website (<http://www.xilinx.com/power>) for access to a wider range of applicable voltage regulator solutions.
- Updated software flows to ISE® software 11.x.

- Miscellaneous usage clarifications:
 - o Clarified XCFxxP PROM internal 40 MHz clock feature is valid with only the XCFxxP PROM decompression feature that divides the internal clock frequency in half before driving the CLKOUT pin. This has always been true based on the related timing specifications (TCCD and FF) in the DS123 data sheet.
 - o Clarified that third-party programmers require PROM MCS formatted file for input PROM data. This has always been true but was not clearly specified on some third-party programmers.

Products Affected

This notice applies to the Platform Flash PROM devices named in the DS123 data sheet. The DS123 data sheet changes do not affect the Platform Flash XL (XCF128X) or Military (XQF32PVO48M) grade devices which are specified in separate data sheets. Affected part numbers are included in the following table:

Table 1: Affected Product Ordering Codes

Platform Flash PROM Family Ordering Codes		
XCF01SVO20C	XCF02SVO20C	XCF04SVO20C
XCF01SVO20C0100	XCF02SVO20C0100	XCF04SVO20C0100
XCF01SVO20C0936	XCF02SVO20C0936	XCF04SVO20C0936
XCF01SVOG20C	XCF02SVOG20C	XCF04SVOG20C
XCF01SVOG20C0100	XCF02SVOG20C0100	XCF04SVOG20C0100
XCF01SVOG20C0936	XCF02SVOG20C0936	XCF04SVOG20C0936
XCF08PFS48C	XCF16PFS48C	XCF32PFS48C
XCF08PFS48C0973	XCF16PFS48C0973	XCF32PFS48C0973
XCF08PFSG48C	XCF16PFSG48C	XCF32PFSG48C
XCF08PVO48C	XCF16PVO48C	XCF32PVO48C
XCF08PVO48C0973	XCF16PVO48C0973	XCF32PVO48C0973
XCF08PVOG48C	XCF16PVOG48C	XCF32PVOG48C

Key Dates

This notification applies to the documentation revisions shown in Table 2:

Table 2: Key Dates

Document Number	Document Title	Document Revision	Publication Date
DS123	<i>Platform Flash In-System Programmable Configuration PROMs</i>	v2.17	10/26/09
UG161	<i>Platform Flash PROM User Guide</i>	v1.5	10/26/09

Response

No response is required. For additional information or questions, please contact [Xilinx Technical Support](#).

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Additional Documentation

DS123, *Platform Flash In-System Programmable Configuration PROMs*, data sheet:

http://www.xilinx.com/support/documentation/data_sheets/ds123.pdf

UG161, *Platform Flash PROM User Guide*:

http://www.xilinx.com/support/documentation/user_guides/ug161.pdf

Revision History

The following table shows the revision history for this document.

Date	Version	Revision
10/26/09	1.0	Initial release.

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