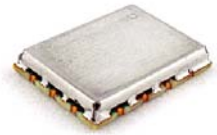
Z~Communications, Inc.
14118 Stowe Drive, Suite B | Poway, CA 92064
TEL: (858) 621-2700 | FAX: (858) 486-1927

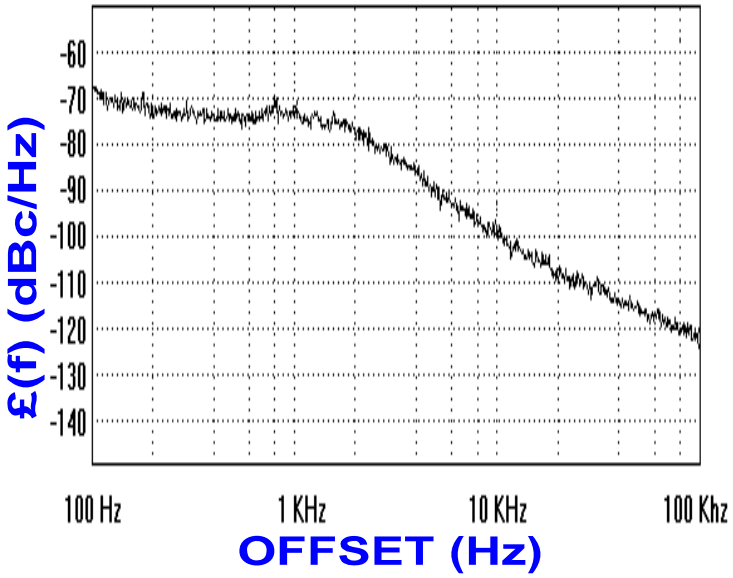
PSN2716A

PHASE LOCKED LOOP

Rev A



PHASE NOISE (1 Hz BW, typical)



FEATURES

- Frequency Range: 2715 - 2717 MHz
- Step Size: 1000 KHz
- PLL - Style Package

APPLICATIONS

- Basestations
- Mobile Radios
- Satellite Communications

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	2715 - 2717	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-102	dBc/Hz
Harmonic Suppression (2nd, typ.)	-20	dBc
Sideband Spurs (typ.)	-80	dBc
Power Output	4±2	dBm
Load Impedance	50	Ω
Step Size	1000	kHz
Charge Pump Output Current	250	µA
Switching Speed (typ., adjacent channel)	2	mSec
Startup Lock Time (typ.)	5	mSec
Operating Temperature Range	-40 to 85	°C
Package Style	PLL	
POWER SUPPLY REQUIREMENTS		
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	30	mA

All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-202 : PLL Functional Description

NOTES:

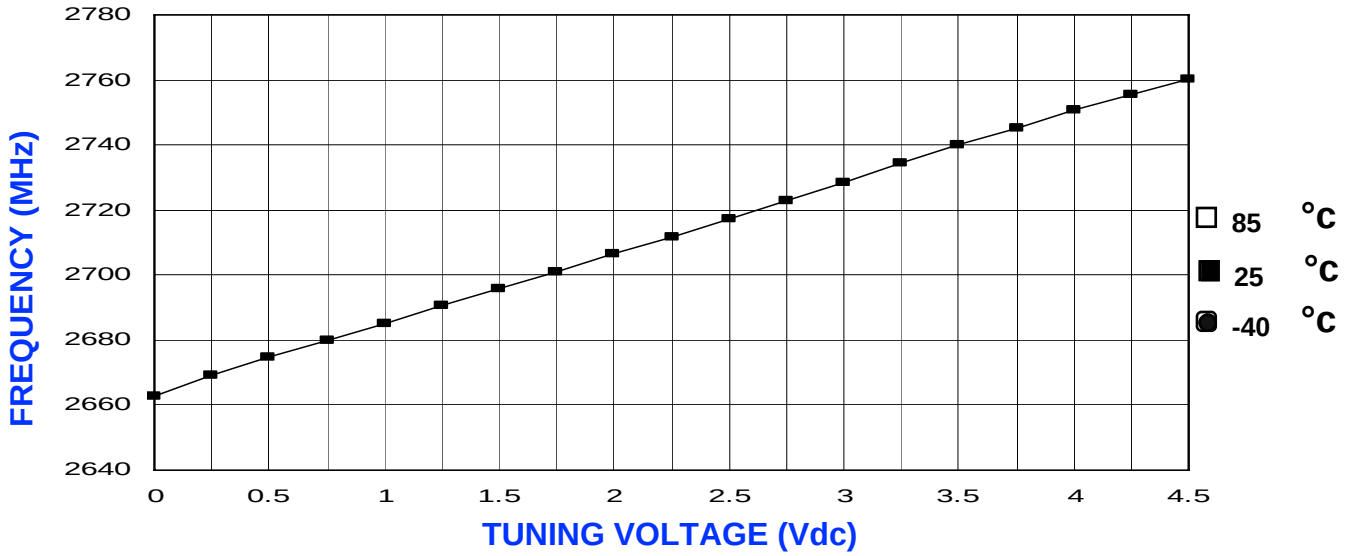
Reference Oscillator Signal: 5 MHz<f_{osc}<40 MHz

Frequency Synthesizer: National Semiconductor - LMX2326

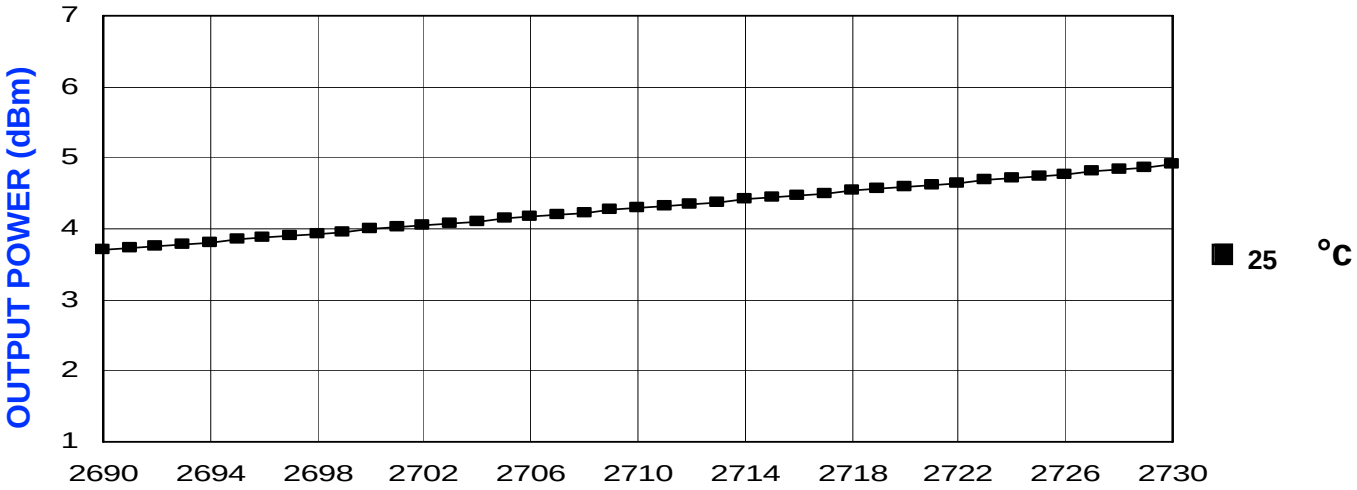
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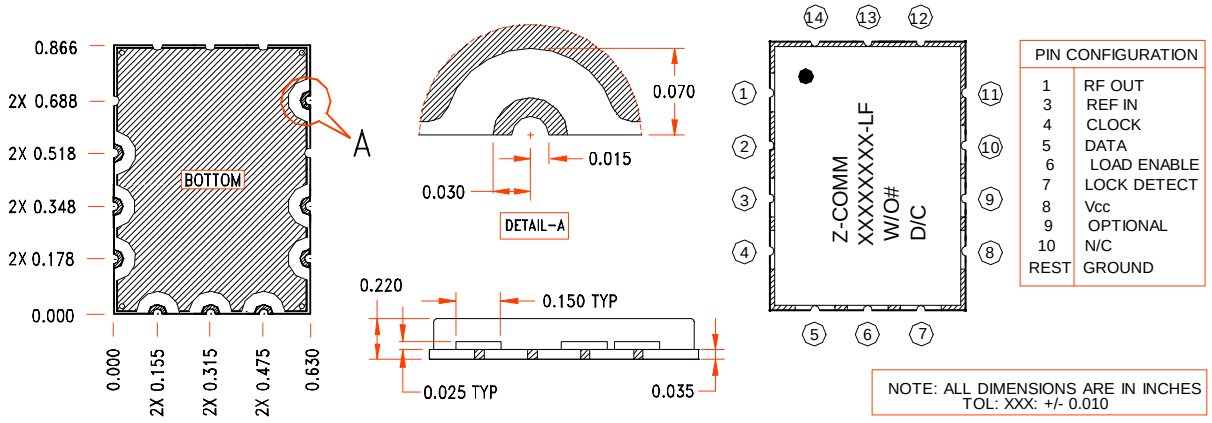
VCO TUNING CURVE, typ.



VCO POWER CURVE, typ.



PHYSICAL DIMENSIONS



PIN CONFIGURATION	
1	RF OUT
3	REF IN
4	CLOCK
5	DATA
6	LOAD ENABLE
7	LOCK DETECT
8	Vcc
9	OPTIONAL
10	N/C
REST	GROUND

NOTE: ALL DIMENSIONS ARE IN INCHES
TOL: XXX: +/- 0.010