SynQor

Technical Specification

Half Brick

$48V_{in}$ $15V_{out}$ 10A

150 Watt, No Heatsink, Isolated DC/DC Converter

The PQ48150HGA10 PowerQor™ Giga converter is a next-generation, board-mountable, isolated, fixed switching frequency DC/DC converter that uses synchronous rectification to achieve extremely high conversion efficiency. The power dissipated by the converter is so low that a heatsink is not required, which saves cost, weight, height, and application effort. The Giga series converters offer enhanced useable output current in a standard "half-brick" module. The Giga units also feature active current sharing for N+1 and parallel applications.

PowerQor Giga



PQ48150HGA10 Module

Operational Features

- Ultra-high efficiency, 92% at full rated load current
- Delivers up to 150 watts of output power with minimal derating - no heatsink required
- Wide input voltage range: 35V – 75V meets or exceeds all 48V bus standards
- Fixed frequency switching provides predictable EMI performance

Mechanical Features

- Industry standard pin-out configuration
- Industry standard size: 2.3" x 2.4"
- Total height only 0.40", permits better airflow and smaller card pitch
- Total weight: 2.1 oz. (59 grams)

Safety Features

- 2000V, 10 MΩ input-to-output isolation
- UL/cUL 60950 recognized (US & Canada), basic insulation rating
- TUV certified to EN60950
- Meets 72/23/EEC and 93/68/EEC directives
- Meets UL94V-0 flammability requirements

Protection Features

- Input under-voltage lockout and over-voltage shutdown protects against abnormal input voltages
- Output current limit and short circuit protection
- Output over-voltage protection
- Thermal shutdown

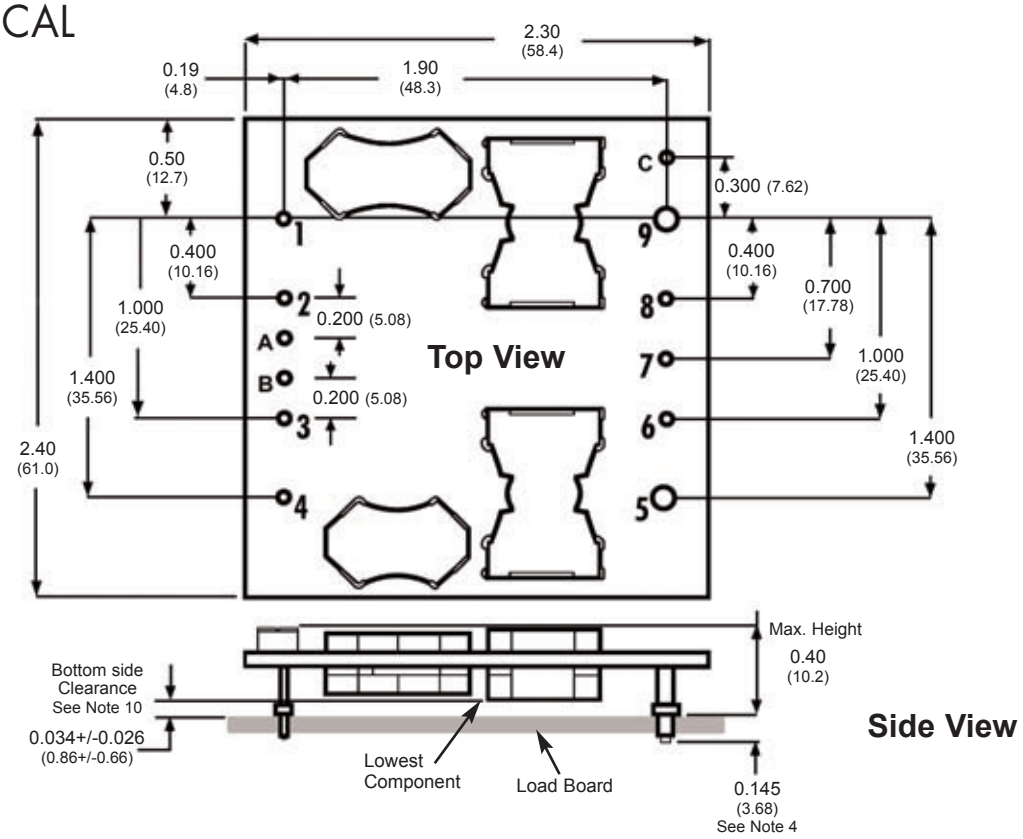
Control Features

- On/Off control referenced to input side (positive and negative logic options are available)
- Remote sense for the output voltage compensates for output distribution drops
- Output voltage trim permits custom voltages and voltage margining

Optional Features (Full-Feature modules)

- Active current share for N+1 and parallel applications
- External Clock Synchronization pin for better EMI characteristics
- Startup Synchronization pin for more consistent start-up sequence
- ORing FET drive supply for efficient ORing protection

MECHANICAL DIAGRAM



NOTES

1) Pins 1-4, 6-8, A-C are 0.040" (1.02mm) diameter. with 0.080" (2.03mm) diameter standoff shoulders.
2) Pins 5 and 9 are 0.080" (2.03 mm) diameter with 0.125" (3.18mm) diameter standoff shoulders.
3) Pins 3, A, B & C only included in Full-Feature models.
4) Other pin extension lengths available. Recommended pin length is 0.03" (0.76mm) greater than the PCB thickness.
5) All Pins: Material - Copper Alloy
   Finish - Tin/Lead over Nickel plate
6) Undimensioned components are shown for visual reference only.
7) All dimensions in inches (mm)
   Tolerances: x.xx +/-0.02 in. (x.x +/-0.5mm)
   x.xxx +/-0.010 in. (x.xx +/-0.25mm)
8) Weight: 2.1 oz. (59 g) typical
9) Workmanship: Meets or exceeds IPC-A-610C Class II
10) UL/TUV standards require a clearance greater than 0.04" (1.02mm) between input and output for Basic insulation. This issue should be considered if any copper traces are on the top side of the user's board. Note that the ferrite cores are considered part of the input/primary circuit.

PIN CONNECTIONS

Pin No.	Name	Function
1	Vin(+)	Positive input voltage
2	ON/OFF	TTL input to turn converter on and off, referenced to Vin(-), with internal pull up.
3	*I share*	*Single wire parallel signal*
4	Vin(-)	Negative input voltage
5	Vout(-)	Negative output voltage
6	SENSE(-)	Return remote sense
7	TRIM	Output voltage trim
8	SENSE(+)	Positive remote sense
9	Vout(+)	Positive output voltage
A	*Clock Sync*	*Clock synchronization*
B	*Start Sync*	*Startup synchronization*
C	*OR Drive*	*External OR-FET drive*

Pins in Italics Shaded text are Optional

PQ48150HGA10 ELECTRICAL CHARACTERISTICS

T_A=25°C, airflow rate=300 LFM, V_{in}=48Vdc unless otherwise noted; full operating temperature range is -40°C to +100°C ambient temperature with appropriate power derating. Specifications subject to change without notice.

Parameter	Min.	Typ.	Max.	Units	Notes & Conditions
ABSOLUTE MAXIMUM RATINGS					
Input Voltage					
Non-Operating			100	V	continuous
Operating			80	V	continuous
Operating Transient Protection			100	V	≤ 10µs
Isolation Voltage (input to output)			2000	V	Basic insulation level, Pollution degree 2
Operating Temperature	-40		100	°C	
Storage Temperature	-55		125	°C	
Voltage at ON/OFF input pin	-2		18	V	
Voltage at Clock Sync pin	-15		18	V	applies to Full Feature option only
INPUT CHARACTERISTICS					
Operating Input Voltage Range	35	48	75	V	
Input Under-Voltage Lockout					
Turn-On Voltage Threshold	31.5	33.3	34.4	V	
Turn-Off Voltage Threshold	29.5	31.0	32.4	V	
Lockout Hysteresis Voltage	2.2	2.3	2.4	V	
Input Over-Voltage Shutdown	76.7	78.3	79.5	V	
Maximum Input Current			4.8	A	100% Load, 35 Vin
No-Load Input Current		75	95	mA	
Disabled Input Current		2	5	mA	
Inrush Current Transient Rating		0.03		A^2s	
Response to Input Transient		N/A		mV	1000V/ms input transient
Input Reflected-Ripple Current		4		mA	RMS thru 10µH inductor; Figs. 13 & 15
Recommended Input Fuse			20	A	fast blow external fuse recommended
Input Filter Component Values (C1\L\C2)		1.6\4\3.3		µF\µH\µF	internal values, see Figure E
Recommended External Input Capacitance	33	47		µF	see Figure 13
OUTPUT CHARACTERISTICS					
Output Voltage Set Point	14.85	15.00	15.15	V	
Output Voltage Regulation					
Over Line		±0.05 \ 8	±0.1 \ 15	%\mV	
Over Load		±0.1 \ 15	±0.2 \ 30	%\mV	
Over Temperature		±45	±90	mV	
Total Output Voltage Range	14.715		15.285	V	over sample, line, load, temperature & life
Output Voltage Ripple and Noise					20MHz bandwidth; Fig. 13 & 16
Peak-to-Peak		50	100	mV	Full Load, see Figures 13 & 16
RMS		15	30	mV	Full Load, see Figures 13 & 16
Operating Output Current Range	0		10	A	
Output DC Current-Limit Inception	11	12	13	A	Output Voltage 10% Low; Fig. 17
Output DC Current-Limit Shutdown Voltage		10.0		V	
Current Share Accuracy (2 units paralleled)		±2	±5	%	% of rated output current
Back-Drive Current Limit while Enabled	0.1	0.4	1.0	A	Negative current drawn from output
Back-Drive Current Limit while Disabled	0	10	50	mA	Negative current drawn from output
Maximum Output Capacitance			>5,000	µF	15Vout at 10A Resistive Load
DYNAMIC CHARACTERISTICS					
Input Voltage Ripple Rejection		70		dB	120 Hz; Fig. 20
Output Voltage During Load Current Transient					
Step Change in Output Current (0.1A/µs)		750		mV	50% to 75% to 50% Iout max; Figure 11
Step Change in Output Current (5A/µs)		500		mV	50% to 75% to 50% Iout max; Figure 12
Settling Time		1		ms	to within 1% Vout nom
Turn-On Transient					
Turn-On Time		20	30	ms	Full load, Vout=90% nom.; Figs. 9 & 10
Start-Up Inhibit Time	180	200	215	ms	-40°C to +125°C; Figure F
Output Voltage Overshoot		0		%	10,000 µF load capacitance, Iout = 0A
EFFICIENCY					
100% Load		92		%	Figures 1 - 4
50% Load		92		%	Figures 1 - 4
TEMPERATURE LIMITS FOR POWER DERATING CURVES					
Semiconductor Junction Temperature			125	°C	Package rated to 150°C
Board Temperature			125	°C	UL rated max operating temp 130°C
Transformer Temperature			125	°C	See Figures 5 - 8 for derating curves
ISOLATION CHARACTERISTICS					
Isolation Voltage		2000		V	
Isolation Resistance		10		MΩ	
Isolation Capacitance		3300		pF	

ELECTRICAL CHARACTERISTICS (Continued)

Parameter	Min.	Typ.	Max.	Units	Notes & Conditions
FEATURE CHARACTERISTICS					
Switching Frequency	205	230	255	kHz	
ON/OFF Control (Option P)					
Off-State Voltage	-2		0.8	V	
On-State Voltage	2.7		18	V	
ON/OFF Control (Option N)					
Off-State Voltage	2.7		18	V	
On-State Voltage	-2		0.8	V	
ON/OFF Control (Either Option)					Figures A, B
Pull-Up Voltage		Vin/6	15	V	
Pull-Up Resistance		42		kΩ	
Output Voltage Trim Range	-20		+7	%	Measured across Pins 9 & 5; Figure C
Output Voltage Remote Sense Range			+7	%	Measured across Pins 9 & 5
Output Over-Voltage Protection	108	116	121	%	Over full temp range; % of nominal Vout
Over-Temperature Shutdown		120		°C	Average PCB Temperature
Over-Temperature Shutdown Restart Hysteresis		10		°C	
RELIABILITY CHARACTERISTICS					
Calculated MTBF		2.3		10^6 Hrs.	Telcordia TR-NWT-000332; 80% load,300LFM, 40°C T_a
Calculated MTBF		1.5		10^6 Hrs.	MIL-HDBK-217F; 80% load, 300LFM, 40°C T_a
Demonstrated MTBF		>20		10^6 Hrs.	Field demonstrated MTBF

STANDARDS COMPLIANCE

Parameter	Notes
STANDARDS COMPLIANCE	
UL/cUL 60950	File # E194341, Basic insulation & pollution degree 2
EN60950	Certified by TUV
72/23/EEC	
93/68/EEC	
Needle Flame Test (IEC 695-2-2)	test on entire assembly; board & plastic components UL94V-0 compliant
IEC 61000-4-2	ESD test, 8kV - NP, 15kV air - NP
GR-1089-CORE	Section 7 - electrical safety, Section 9 - bonding/grounding

• An external input fuse must always be used to meet these safety requirements

QUALIFICATION TESTING

Parameter	# Units	Test Conditions
QUALIFICATION TESTING		
Life Test	32	95% rated Vin and load, units at derating point, 1000 hours
Vibration	5	10-55Hz sweep, 0.060" total excursion,1 min./sweep, 120 sweeps for 3 axis
Mechanical Shock	5	100g minimum, 2 drops in x and y axis, 1 drop in z axis
Temperature Cycling	10	-40°C to 100°C, unit temp. ramp 15°C/min., 500 cycles
Power/Thermal Cycling	5	Toperating = min to max, Vin = min to max, full load, 100 cycles
Design Marginality	5	Tmin-10°C to Tmax+10°C, 5°C steps, Vin = min to max, 0-105% load
Humidity	5	85°C, 85% RH, 1000 hours, 2 minutes on and 6 hours off
Solderability	15 pins	MIL-STD-883, method 2003

• Extensive characterization testing of all SynQor products and manufacturing processes is performed to ensure that we supply robust, reliable product. Contact factory for more information about Proof of Design and Proof of Manufacturing processes.

OPTIONS

SynQor provides various options for Logic Sense, Pin Length and Feature Set for this family of DC/DC converters. Please consult the last page of this specification sheet for information on available options.

PATENTS

SynQor is protected under various patents, including but not limited to U.S. Patent # 5,999,417.

Performance Curves

Half Brick $48V_{in}$ $15V_{out}$ 10A

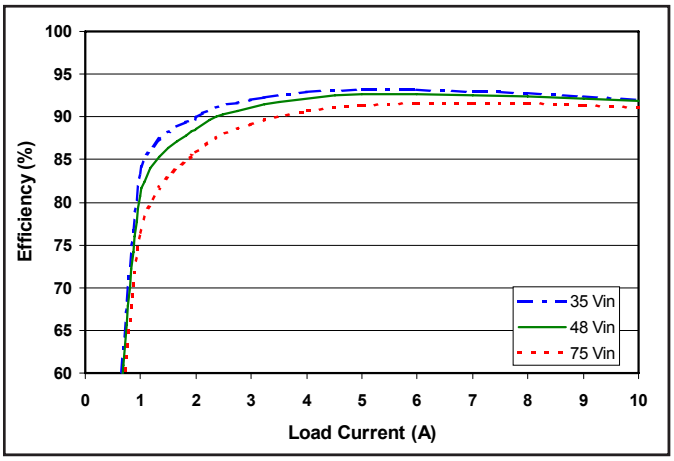


Figure 1: *Efficiency at nominal output voltage vs. load current for minimum, nominal, and maximum input voltage at 25°C.*

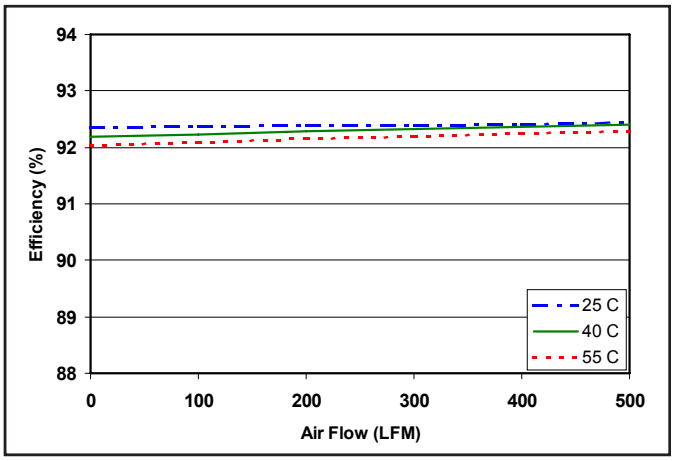


Figure 2: *Efficiency at nominal output voltage and 60% rated power vs. airflow rate for ambient air temperatures of 25°C, 40°C, and 55°C (nominal input voltage).*

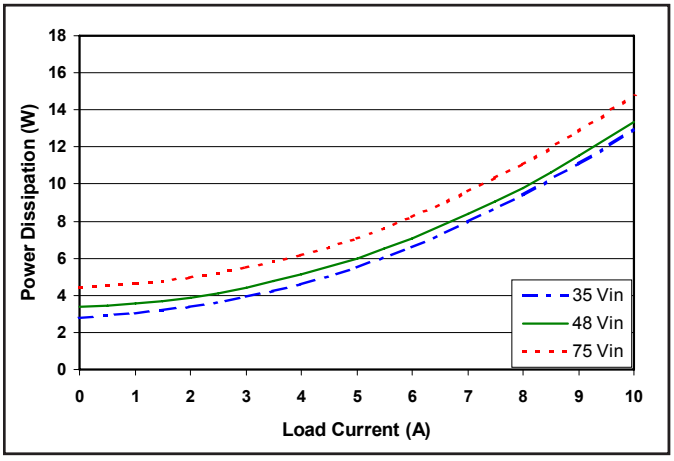


Figure 3: *Power dissipation at nominal output voltage vs. load current for minimum, nominal, and maximum input voltage at 25°C.*

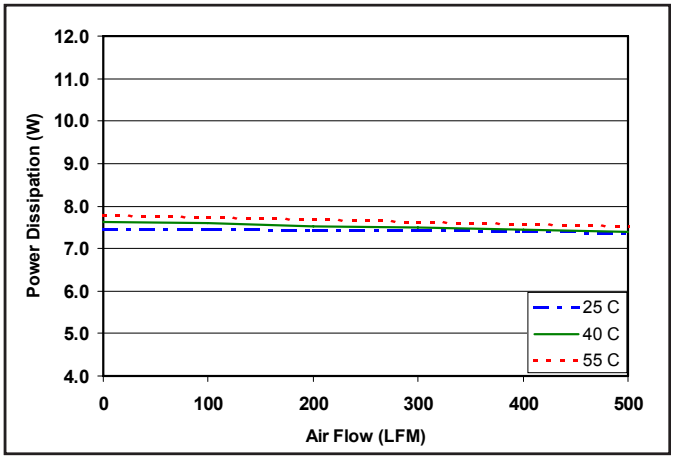


Figure 4: *Power dissipation at nominal output voltage and 60% rated power vs. airflow rate for ambient air temperatures of 25°C, 40°C, and 55°C (nominal input voltage).*

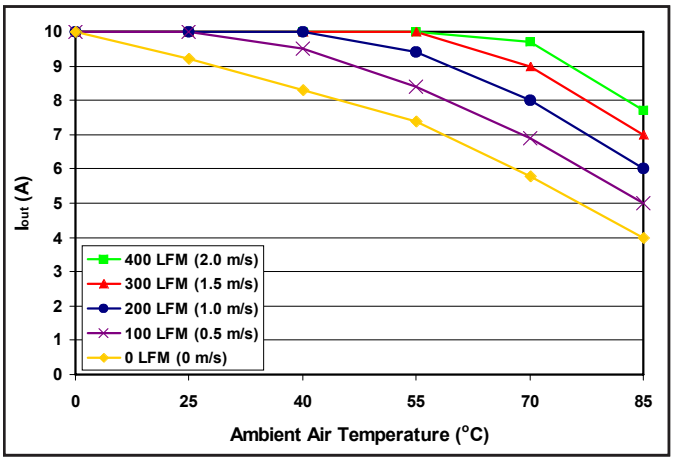


Figure 5: *Maximum output power derating curves vs. ambient air temperature for airflow rates of 0 LFM through 400 LFM with air flowing from input to output (nominal input voltage).*

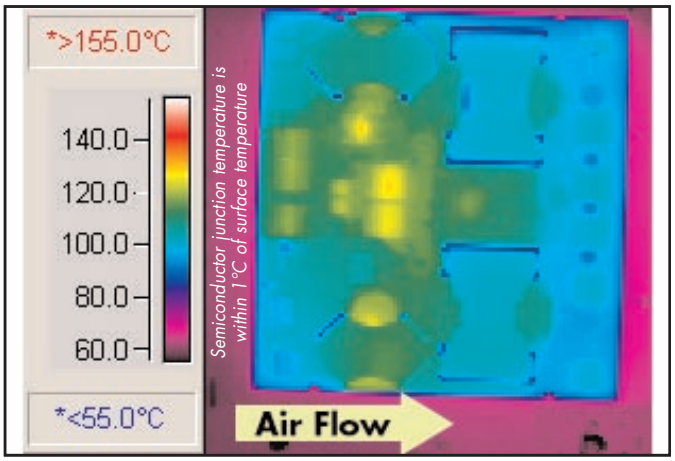


Figure 6: *Thermal plot of converter at 9.4 amp load current (141W) with 55°C air flowing at the rate of 200 LFM. Air is flowing across the converter from input to output (nominal input voltage).*

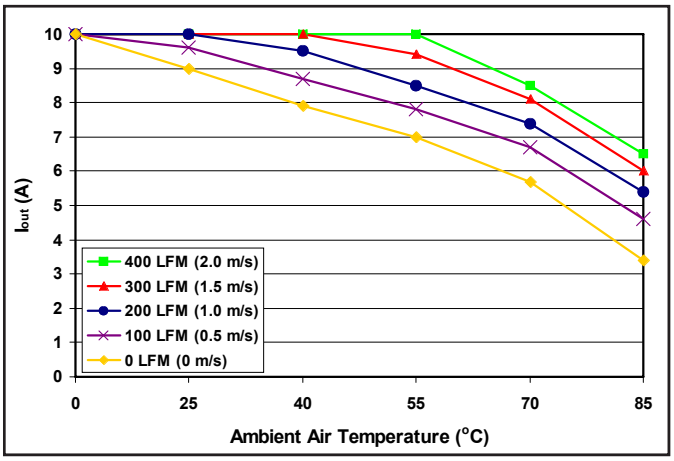


Figure 7: *Maximum output power derating curves vs. ambient air temperature for airflow rates of 0 LFM through 400 LFM with air flowing from output to input (nominal input voltage).*

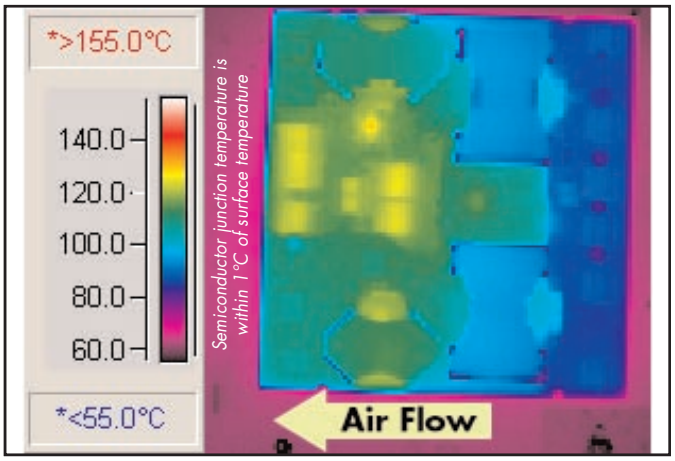


Figure 8: *Thermal plot of converter at 8.5 amp load current (128W) with 55°C air flowing at the rate of 200 LFM. Air is flowing across the converter from output to input (nominal input voltage).*

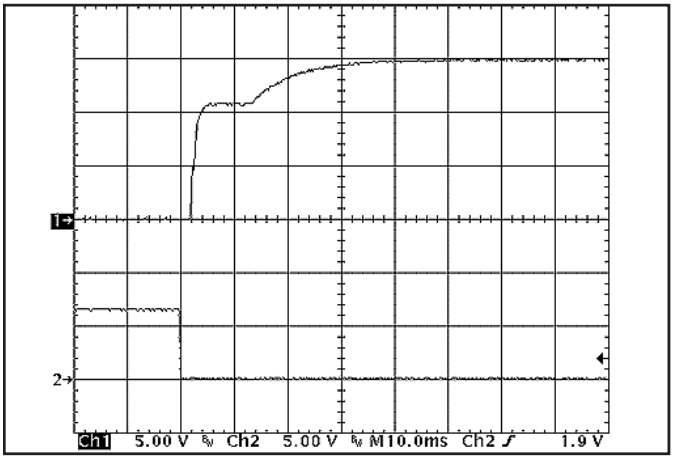


Figure 9: *Turn-on transient at full load (resistive load) (10 ms/div).*
Top Trace: Vout (5V/div)
Bottom Trace: ON/OFF input (5V/div)

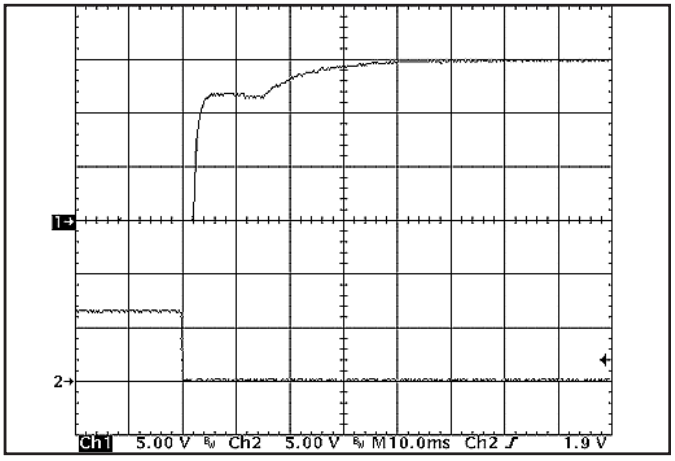


Figure 10: *Turn-on transient at zero load (10 ms/div).*
Top Trace: Vout (5V/div)
Bottom Trace: ON/OFF input (5V/div)

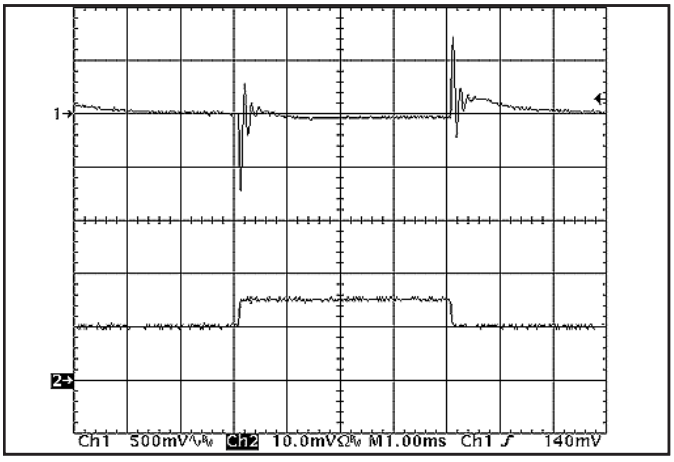


Figure 11: *Output voltage response to step-change in load current (50%-75%-50% of Iout(max); dI/dt = 0.1A/μs). Load cap: 15μF, 450 mΩ ESR tantalum capacitor and 1μF ceramic capacitor. Top trace: Vout (500mV/div), Bottom trace: Iout (5A/div).*

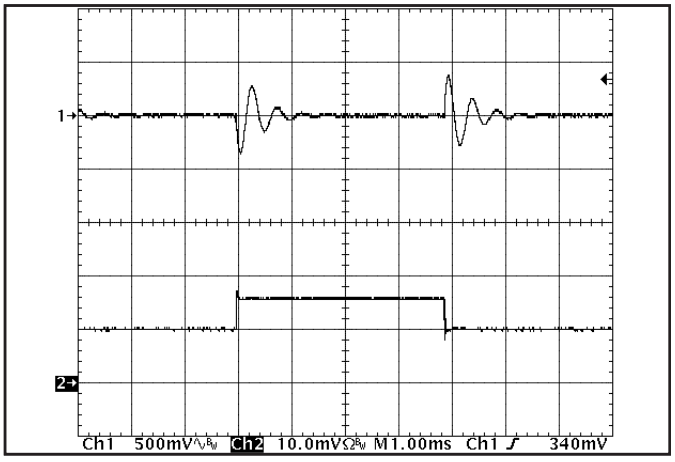


Figure 12: *Output voltage response to step-change in load current (50%-75%-50% of Iout(max): dI/dt = 5A/μs). Load cap: 480μF, 15 mΩ ESR tantalum capacitor and 1μF ceramic capacitor. Top trace: Vout (500mV/div), Bottom trace: Iout (5A/div).*

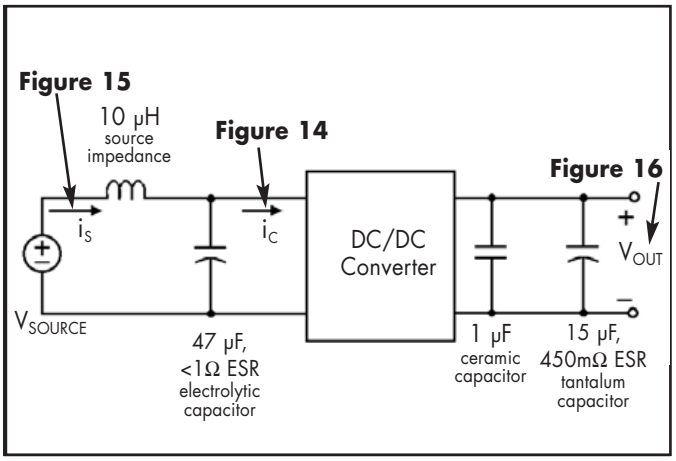


Figure 13: *Test set-up diagram showing measurement points for Input Terminal Ripple Current (Figure 14), Input Reflected Ripple Current (Figure 15) and Output Voltage Ripple (Figure 16).*

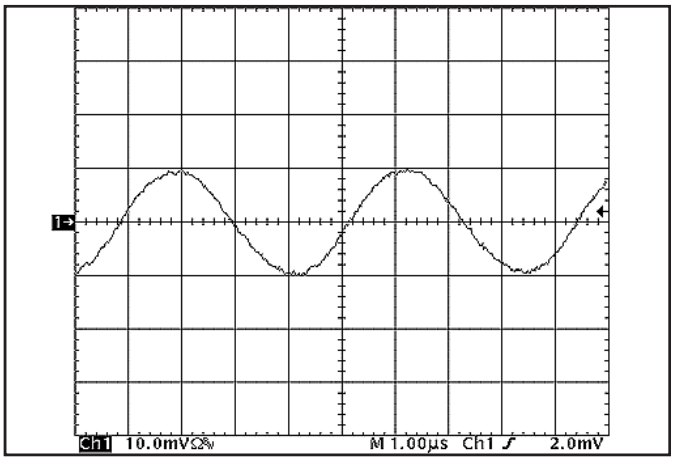


Figure 14: *Input Terminal Ripple Current, i_C, at full rated output current and nominal input voltage with 10µH source impedance and 47µF electrolytic capacitor (100 mA/div). (See Figure 13)*

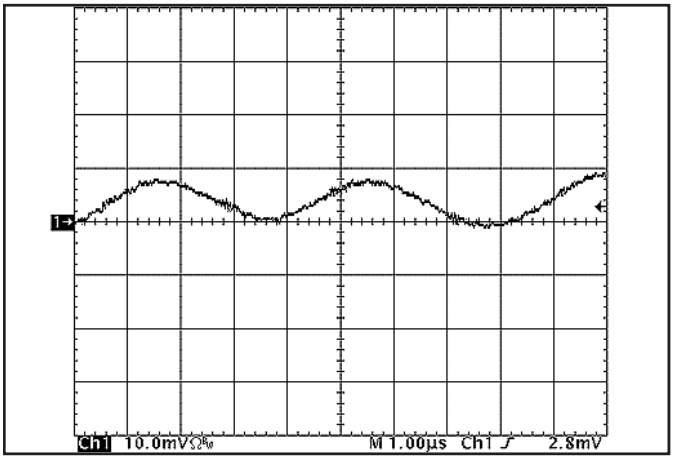


Figure 15: *Input reflected ripple current, i_S, through a 10 µH source inductor at nominal input voltage and rated load current (5 mA/div). (See Figure 13)*

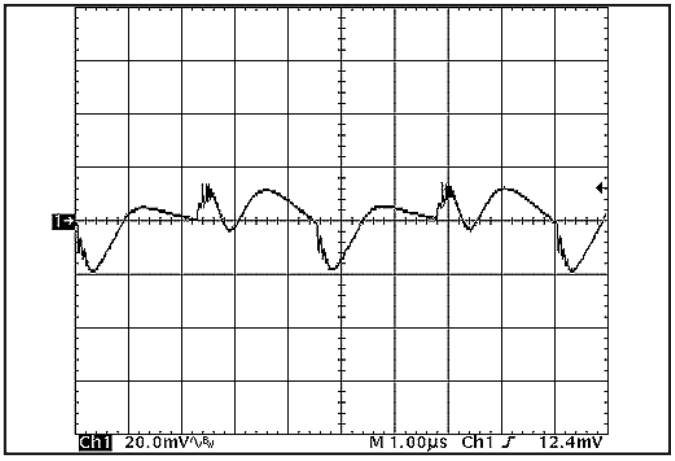


Figure 16: *Output voltage ripple at nominal input voltage and rated load current (20 mV/div). Load capacitance: 1µF ceramic capacitor and 10µF tantalum capacitor. Bandwidth: 20 MHz. (See Figure 13)*

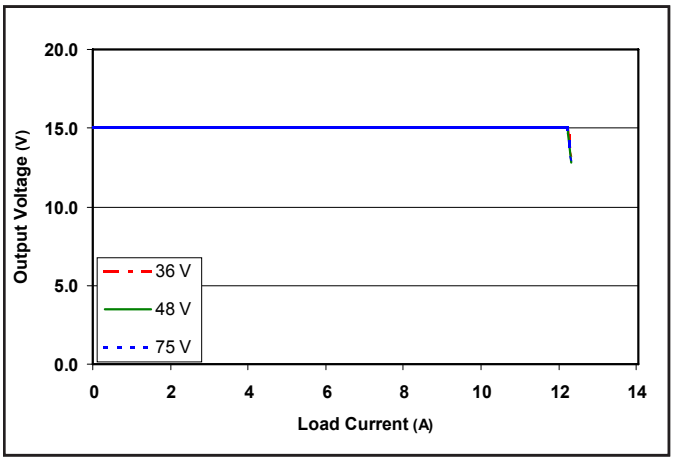


Figure 17: *Output voltage vs. load current showing typical current limit curves and converter shutdown points.*

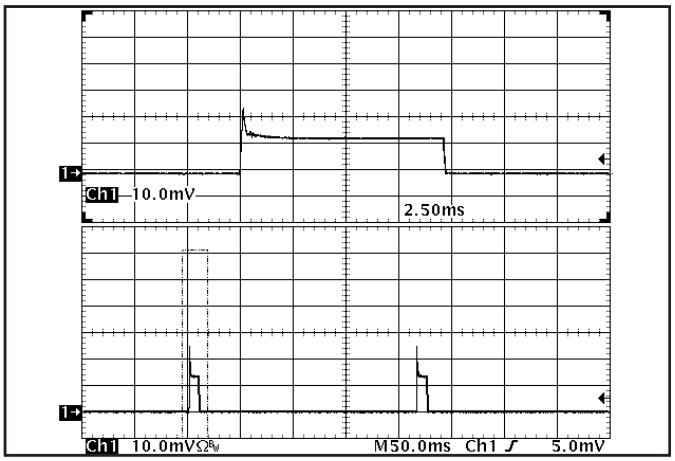


Figure 18: *Load current (10A/div) as a function of time when the converter attempts to turn on into a 1 mΩshort circuit. Top trace (2.5ms/div) is an expansion of the on-time portion of the bottom trace.*

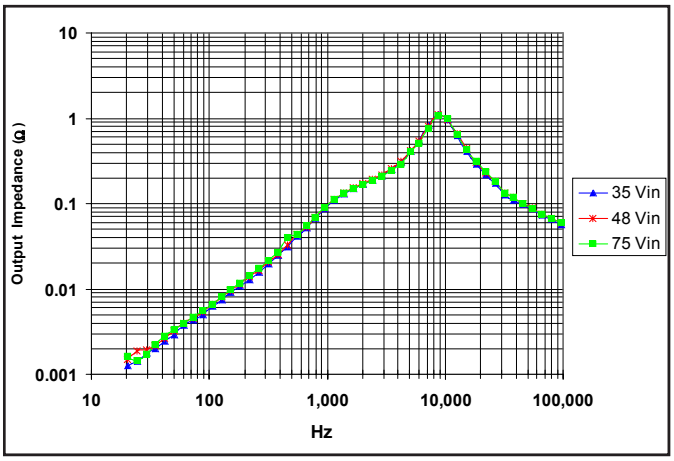


Figure 19: *Magnitude of incremental output impedance (Z_{out} = v_{out}/i_{out}) for minimum, nominal, and maximum input voltage at full rated power.*

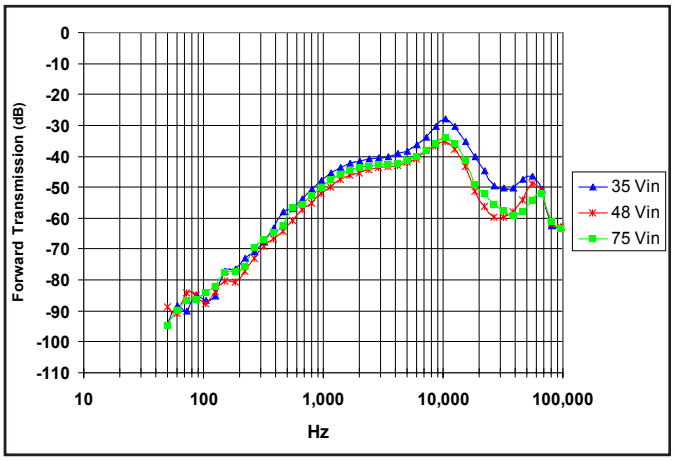


Figure 20: *Magntiude of incremental forward transmission (FT = v_{out}/v_{in}) for minimum, nominal, and maximum input voltage at full rated power.*

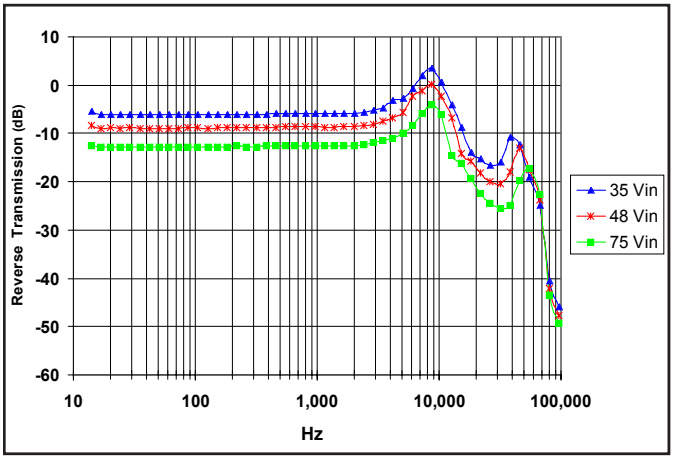


Figure 21: *Magnitude of incremental reverse transmission (RT = i_{in}/i_{out}) for minimum, nominal, and maximum input voltage at full rated power.*

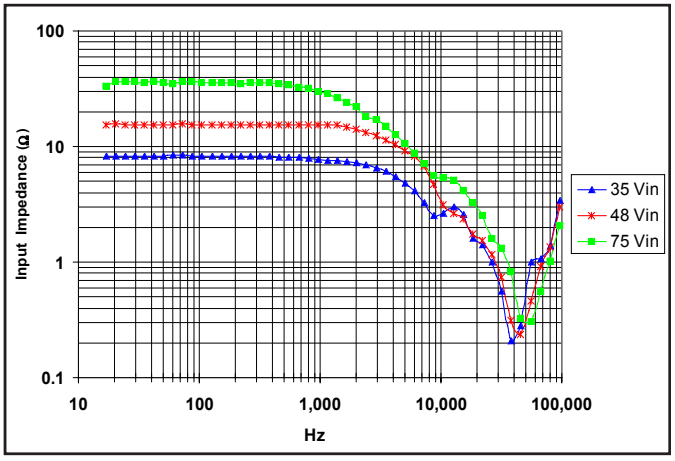


Figure 22: *Magnitude of incremental input impedance (Z_{in} = v_{in}/i_{in}) for minimum, nominal, and maximum input voltage at full rated power.*

BASIC OPERATION AND FEATURES

The *Power*Qor series converter uses a two-stage power circuit topology. The first stage is a buck-converter that keeps the output voltage constant over variations in line, load, and temperature. The second stage uses a transformer to provide the functions of input/output isolation and voltage step-down to achieve the low output voltage required.

Both the first stage and the second stage switch at a fixed frequency for predictable EMI performance. Rectification of the transformer's output is accomplished with synchronous rectifiers. These devices, which are MOSFETs with a very low on-state resistance, dissipate far less energy than Schottky diodes. This is the primary reason that the *Power*Qor converter has such high efficiency, even at very low output voltages and very high output currents.

Dissipation throughout the converter is so low that it does not require a heatsink for operation. Since a heatsink is not required, the *Power*Qor converter does not need a metal baseplate or potting material to help conduct the dissipated energy to the heatsink. The *Power*Qor converter can thus be built more simply and reliably using high yield surface mount techniques on a PCB substrate.

The *Power*Qor series of half-brick and quarter-brick converters uses the industry standard footprint and pin-out configuration.

CONTROL FEATURES

REMOTE ON/OFF (Pin 2): The ON/OFF input, Pin 2, permits the user to control when the converter is *on* or *off*. This input is referenced to the return terminal of the input bus, Vin(-). There are two versions of the converter that differ by the sense of the logic used for the ON/OFF input.

In the positive logic version, the ON/OFF input is active high (meaning that a high turns the converter *on*). In the negative logic version, the ON/OFF signal is active low (meaning that a low turns the converter *on*). Figure A details five possible circuits for driving the ON/OFF pin. Figure B is a detailed look of the internal ON/OFF circuitry.

REMOTE SENSE(+) (Pins 8 and 6): The SENSE(+) inputs correct for voltage drops along the conductors that connect the converter's output pins to the load.

Pin 8 should be connected to Vout(+) and Pin 6 should be connected to Vout(-) at the point on the board where regulation is desired. A remote connection at the load can adjust for a voltage drop only as large as that specified in this datasheet, that is

$$[\text{Vout}(+) - \text{Vout}(-)] - [\text{SENSE}(+) - \text{SENSE}(-)] \leq \text{Sense Range \%} \times \text{Vout}$$

Pins 8 and 6 must be connected for proper regulation of the output voltage. If these connections are not made, the converter will deliver an output voltage that is slightly lower than its specified value.

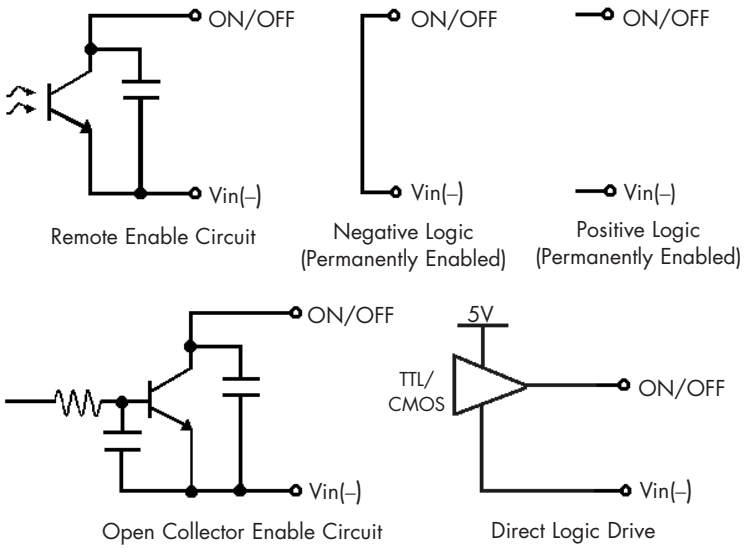


Figure A: *Various circuits for driving the ON/OFF pin.*

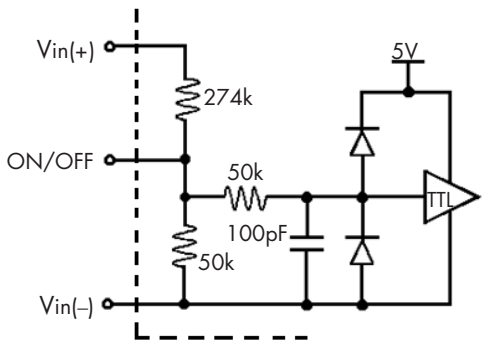


Figure B: *Internal ON/OFF pin circuitry*

Note: the output over-voltage protection circuit senses the voltage across the sense leads (pins 8 and 6) to determine when it should trigger, not the voltage across the converter's output pins (pins 9 and 5).

OUTPUT VOLTAGE TRIM (Pin 7): The TRIM input permits the user to adjust the output voltage across the sense leads up or down according to the trim range specifications.

To decrease the output voltage, the user should connect a resistor between Pin 7 and Pin 6 (SENSE(-) input). For a desired decrease of the nominal output voltage, the value of the resistor should be

$$R_{trim\text{-}down} = \left(\frac{100\%}{\Delta}\right) - 2\ k\Omega$$

where

$$\Delta\% = \left(\frac{Vnominal - Vdesired}{Vnominal}\right) \times 100\%$$

To increase the output voltage, the user should connect a resistor between Pin 7 and Pin 8 (SENSE(+) input). For a desired increase of the nominal output voltage, the value of the resistor should be

$$R_{trim\text{-}up} = \frac{\left(\frac{V_{NOM}}{V_{REF}} - 2\right) \bullet V_{DES} + V_{NOM}}{V_{DES} - V_{NOM}}\ k\Omega$$

Figure C graphs the relationship between the trim resistor value and Rtrim-up and Rtrim-down, showing the total range the output voltage can be trimmed up or down.

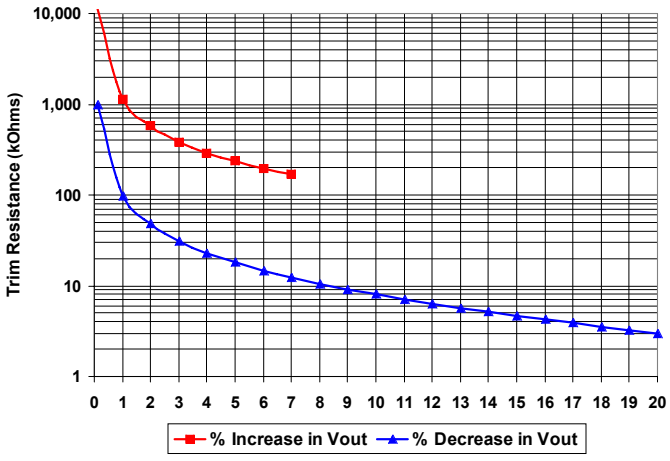


Figure C: Trim Graph for 15Vout module

Note: the TRIM feature does not affect the voltage at which the output over-voltage protection circuit is triggered. Trimming the output voltage too high may cause the over-voltage protection circuit to engage, particularly during transients.

It is not necessary for the user to add capacitance at the Trim pin. The node is internally bypassed to eliminate noise.

Total DC Variation of Vout: For the converter to meet its full specifications, the maximum variation of the DC value of Vout, due to both trimming and remote load voltage drops, should not be greater than that specified for the output voltage trim range.

PROTECTION FEATURES

Input Under-Voltage Lockout: The converter is designed to turn off when the input voltage is too low, helping avoid an input system instability problem, described in more detail in the application note titled "Input System Instability". The lockout circuitry is a comparator with DC hysteresis. When the input voltage is rising, it must exceed the typical Turn-On Voltage Threshold value (listed on the specification page) before the converter will turn on. Once the converter is on, the input voltage must fall below the typical Turn-Off Voltage Threshold value before the converter will turn off.

Input Over-Voltage Shutdown: Available on PQ48 models only. The converter turns off when the input voltage is too high, allowing the converter to withstand an input voltage as high as 100V without destruction. The shutdown circuitry is a comparator with DC hysteresis. When the input voltage exceeds the typical Input Over-Voltage Shutdown value, the converter will turn off. Once the converter is off, it will turn back on when the input voltage falls below the minimum Input Over-Voltage Shutdown value.

Output Current Limit: The maximum current limit remains constant as the output voltage drops. However, once the impedance of the short across the output is small enough to make the output voltage drop below the specified Output DC Current-Limit Shutdown Voltage, the converter turns off.

The converter then enters a "hiccup mode" where it repeatedly turns on and off at a 5 Hz (nominal) frequency with a 5% duty cycle until the short circuit condition is removed. This prevents excessive heating of the converter or the load board.

Output Over-Voltage Limit: If the voltage across the output pins exceeds the Output Over-Voltage Protection threshold, the converter will immediately stop switching. This prevents damage to the load circuit due to 1) excessive series resistance in output current path from converter output pins to sense point, 2)

a release of a short-circuit condition, or 3) a release of a current limit condition. Load capacitance determines exactly how high the output voltage will rise in response to these conditions. After 200 ms the converter will automatically restart.

Over-Temperature Shutdown: A temperature sensor on the converter senses the average temperature of the module. The thermal shutdown circuit is designed to turn the converter off when the temperature at the sensed location reaches the Over-Temperature Shutdown value. It will allow the converter to turn on again when the temperature of the sensed location falls by the amount of the Over-Temperature Shutdown Restart Hysteresis value.

APPLICATION CONSIDERATIONS

Input System Instability: This condition can occur because any DC/DC converter appears incrementally as a negative resistance load. A detailed application note titled "Input System Instability" is available on the SynQor web site (www.synqor.com) which provides an understanding of why this instability arises, and shows the preferred solution for correcting it.

Application Circuits: Figure D below provides a typical circuit diagram which details the input filtering and voltage trimming.

Input Filtering and External Capacitance: Figure E below provides a diagram showing the internal input filter components. This filter dramatically reduces input terminal ripple current, which otherwise could exceed the rating of an external electrolytic input capacitor. The recommended external input capacitance is specified in the "Input Characterisitcs" section. More detailed information is available in the application note titled "EMI Characteristics" on the SynQor website.

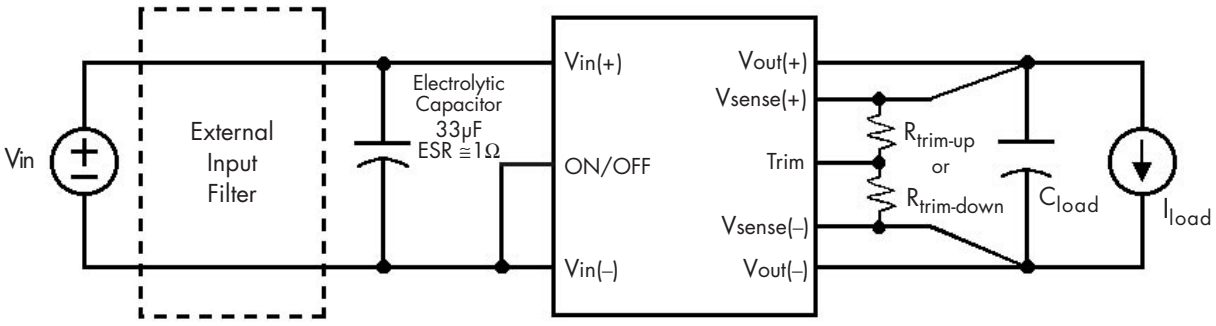


Figure D: *Typical application circuit (negative logic unit, permanently enabled).*

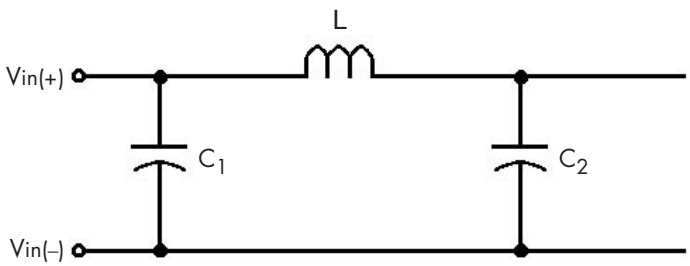


Figure E: *Internal Input Filter Diagram (component values listed on page 3).*

Startup Inhibit Period: The Startup Inhibit Period ensures that the converter will remain off for at least 200ms when it is shut down for any reason. When an output short is present, this generates a 5Hz "hiccup mode," which prevents the converter from overheating. In all, there are seven ways that the converter can be shut down, initiating a Startup Inhibit Period:

- Input Under-Voltage Lockout
- Input Over-Voltage Shutdown (not present in Quarter-brick)
- Output Over-Voltage Protection
- Over Temperature Shutdown
- Current Limit
- Short Circuit Protection
- Turned off by the ON/OFF input

Figure F shows three turn-on scenarios, where a Startup Inhibit Period is initiated at t_0, t_1, and t_2:

Before time t_0, when the input voltage is below the UVL threshold, the unit is disabled by the Input Under-Voltage Lockout feature. When the input voltage rises above the UVL threshold, the Input Under-Voltage Lockout is released, and a Startup Inhibit Period is initiated. At the end of this delay, the ON/OFF pin is evaluated, and since it is active, the unit turns on.

At time t_1, the unit is disabled by the ON/OFF pin, and it cannot be enabled again until the Startup Inhibit Period has elapsed.

When the ON/OFF pin goes high after t_2, the Startup Inhibit Period has elapsed, and the output turns on within the typical Turn-On Time.

FULL FEATURE APPLICATION NOTES

This section provides some basic application information for the full-feature version of the *Power*Qor series converter. The pin-out configuration for these optional feature pins is shown on page 2, and the part numbering format is shown on the last page of this specification sheet.

All units in this product family include back-drive protection to simplify the use of multiple converters in a parallel or sequencing application. However, any voltage applied to the output of the converter should be kept below 120% of the rated output voltage of the converter.

In addition to back-drive protection, these units include the following features (pins):

Current Share (pin 3): The active current share feature allows for N+1 and parallel applications. To achieve load sharing, directly connect the I share pins of multiple units. The load current will share equally among the multiple units (±5% at full rated current). It is important that the Vin(-) pins of the sharing units be directly connected and NOT placed outside of an EMI filter or other impedance path. The voltage at the I Share pin will range from 0 to 5 volts (at full rated current), referenced to

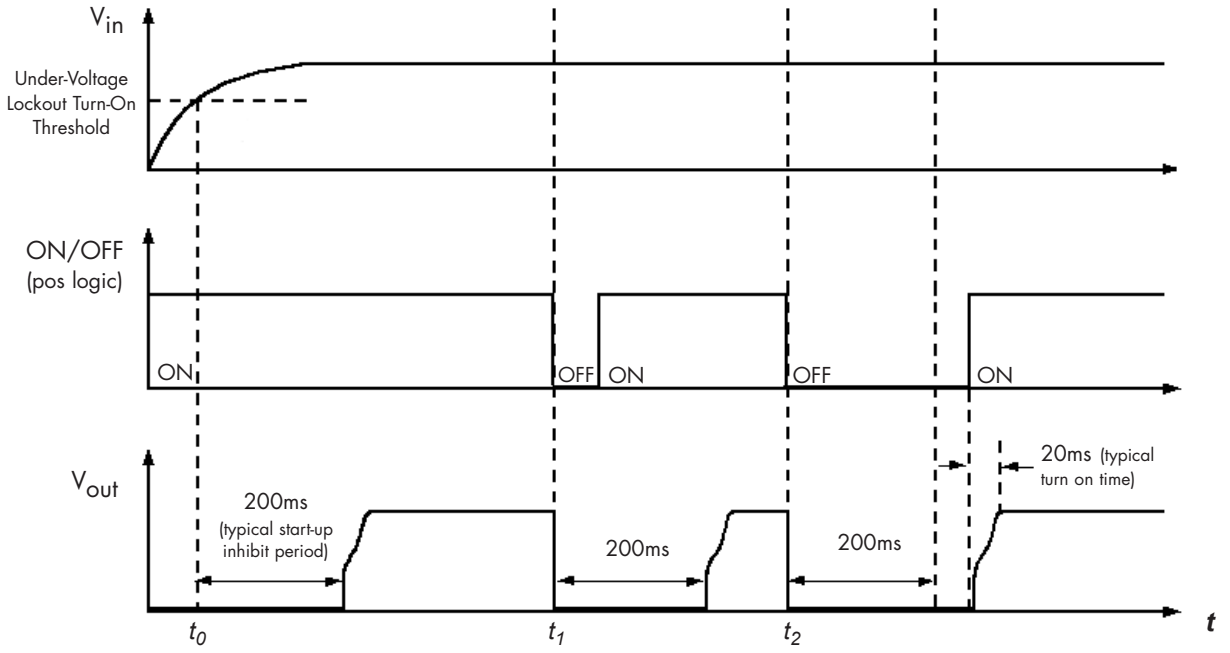


Figure F: *Startup Inhibit Period (turn-on time not to scale)*

the primary-side ground, Vin(-).

Start Sync (pin B): The Start Synchronization pin will allow a more consistent start-up sequence. To operate this feature, connect together the Start-Sync pins of multiple current-sharing units. This will permit immediate start-up with loads greater than the current limit of a single unit. Without this connection, any set of converters attempting to asynchronously start (or re-start) with a load greater than the current limit of a single unit will "hiccup". This "hiccup" mode will continue until one converter attempts a start at the same time as the minimum number of additional units necessary to sustain the load condition. For example, three 50 amp units starting into a 90 amp load would require two units to simultaneously attempt a start. The Start Sync connection synchronizes these starting attempts and provides a more consistent and reliable start-up sequence. For details about the "hiccup mode" or repeated startup attempts, please see the "Startup Inhibit Period" note in this Technical Specification.

Clock Sync (pin A): The External Clock Synchronization pin provides the ability for the user to control the EMI signature and synchronize sensitive circuitry to quiet periods in the converter operation. With this option, the converter can be synchronized to an external clock signal whose frequency is greater than that of the free-running internal clock. However, substantially raising the converter's frequency will reduce its efficiency. Therefore, the recommended frequency range for the external clock synchronization signal applied to this pin would be the lowest value possible without dropping below the minimum frequencies listed below:

275 KHz for the 1.5V and 3.3V units

225 KHz for the 1.8V and 2.0V units

255 KHz for the 2.5V, 5V, 12V and 15V units

The following requirements should also be met:

- The external clock signal should be referenced to the negative input voltage, Vin(-).
- The high level of the signal should be between 3.5V and 5.0V.
- The low level should be between -0.5V and +1.2V.
- Do not apply a clock signal lower than the specified frequency.

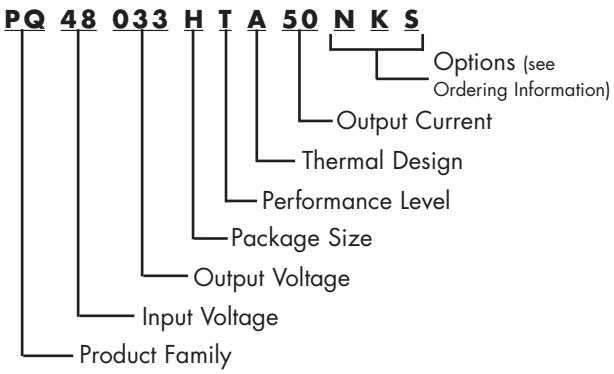
OR Drive (pin C): This pin provides a signal which can be used to drive an OR-ing FET and/or an auxiliary external control circuitry. This pin may also be used as a basic "Power-Good" signal. This pin is referenced to the secondary-side ground, Vsense(-). The voltage at this pin tracks the output voltage but has a value several times higher. For example, during normal operation the OR Drive voltage is at least high enough to fully gate on a standard MOSFET, whose source voltage is equal to the output voltage of the converter. With nominal output voltage, the OR Drive signals are listed in the table below for each output voltage. An internal 2.7 KΩ source resistance will limit the current drawn from this pin. However, the user is responsible for the overvoltage protection of the MOSFET or other external circuitry driven by this voltage. It is recommended that the user include a zener diode between the gate and source of the external MOSFET(s) driven by the OR Drive pin. For more information, refer to the Full Feature Application Note found on the SynQor web site (www.synqor.com).

OR-Drive Voltages

Vout	No Load	Full Load
1.2	TBD	TBD
1.5	9	12
1.8	11	14
2.0	11	16
2.5	13	21
3.3	13	16
5.0	19	20
12	24	26
15	26	28

PART NUMBERING SYSTEM

The part numbering system for SynQor's *Power*Qor DC/DC converters follows the format shown in the example below.



The first 12 characters comprise the base part number and the last 3 characters indicate available options. Although there are no default values for enable logic and pin length, the most common options are negative logic and 0.145" pins. These part numbers are more likely to be readily available in stock for evaluation and prototype quantities.

Application Notes

A variety of application notes and technical white papers can be downloaded in pdf format at www.synqor.com.

ORDERING INFORMATION

The tables below show the valid model numbers and ordering options for converters in this product family. When ordering SynQor converters, please ensure that you use the complete 15 character part number consisting of the 12 character base part number and the additional 3 characters for options.

Model Number	Input Voltage	Output Voltage	Max Output Current
PQ48015HGA40*xyz*	35 - 75 V	1.5 V	40 A
PQ48018HGA40*xyz*	35 - 75 V	1.8 V	40 A
PQ48020HGA40*xyz*	35 - 75 V	2.0 V	40 A
PQ48025HGA40*xyz*	35 - 75 V	2.5 V	40 A
PQ48033HGA40*xyz*	35 - 75 V	3.3 V	40 A
PQ48050HGA30*xyz*	35 - 75 V	5.0 V	30 A
PQ48120HGA12*xyz*	35 - 75 V	12 V	12.5 A
PQ48150HGA10*xyz*	35 - 75 V	15 V	10 A

The following option choices must be included in place of the *x y z* spaces in the model numbers listed above.

Options Description: *x y z*		
Enable Logic	**Pin Length**	**Feature Set**
P - Positive N - Negative	K - 0.110" N - 0.145" R - 0.180" Y - 0.250"	S - Standard F - Full Feature

Contact SynQor for further information:

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E-mail: sales@synqor.com
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Hudson, MA 01749

Warranty
SynQor offers a three (3) year limited warranty. Complete warranty information is listed on our web site or is available upon request from SynQor.

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