

850 nm 4.25 G VCSEL LC TOSA

PL-SLx-00-SG0-Cx



Key Features

- Data rates up to 4.25 Gbps
- Optimized for -40 °C to 85 °C transceiver applications
- Optical power monitor with excellent tracking
- Common cathode, common anode, or differential drive
- VCSEL TO package aligned to a plastic LC barrel
- Custom specification tolerances available
- Fully burned-in and stabilized

Benefits

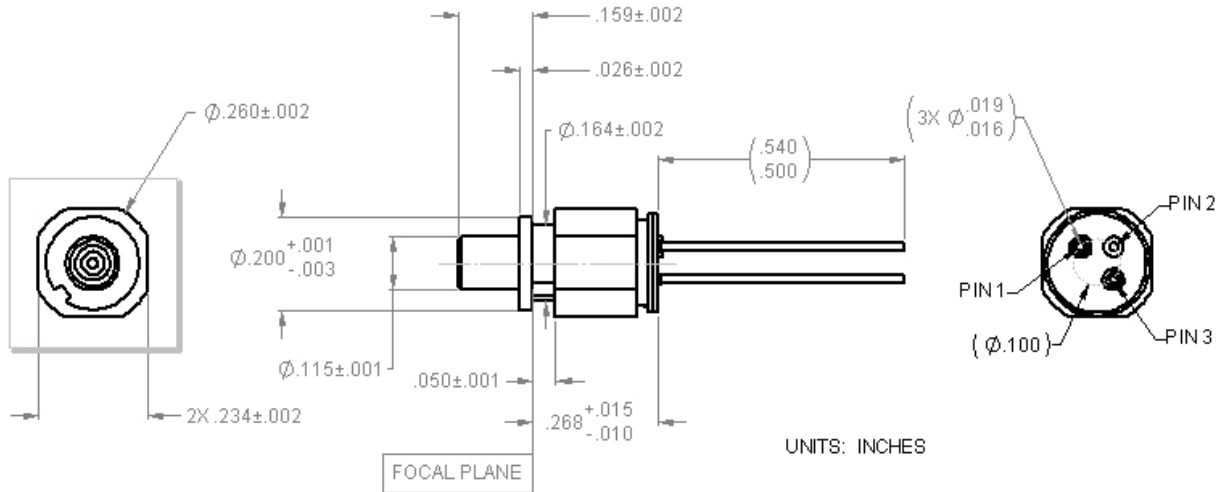
- Proven performance and reliability per GR-468-CORE
- Very high reliability
 - Low FIT
 - High MTBF
- Industrial temperature operation
- Qualified AC modulation performance

This JDSU 850 nm LC connectorized TOSA (Transmit Optical Sub-Assembly) meets the requirements of demanding 4.25 Gb/s transceiver modules. The product utilizes a high performance, high reliability VCSEL integrated in a TO-46 package. Each device is actively aligned to a precision plastic LC barrel using a proprietary alignment algorithm and tested to precise requirements. The JDSU design produces excellent monitor current tracking over temperature. Each unit receives JDSU's proprietary burn-in and stabilization process to ensure a low failure rate and long life expectancy while providing continuously consistent performance. Each part is electro-optically tested to ensure optimum performance and yield in the application.

The PL-SLx-00-SG0-Cx converts an electrical signal into optical power at data rates up to 4.25 Gb/s and it is designed for performance over extended operating temperatures and power conditions with high reliability. The circular optical beam provides optimal power coupling to both 50/125 μm and 62.5/125 μm multimode fibers.

2

Mounting Dimensions (3 Pin Versions)



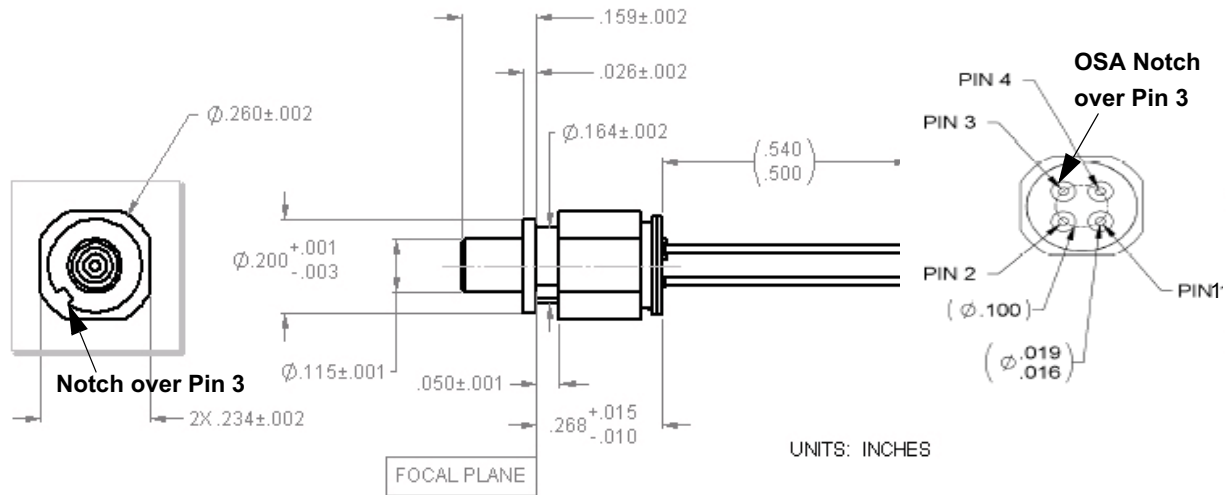
PL-SLC-00-SG0-C0

Pin	Symbol	Function
1	A _{LD}	VCSEL Anode
2	K _{LD} , A _{PD}	VCSEL Cathode, Monitor Anode, Case
3	K _{PD}	Monitor Cathode

PL-SLA-00-SG0-C0

Pin	Symbol	Function
1	K _{LD}	VCSEL Cathode
2	A _{LD} , K _{PD}	VCSEL Anode, Monitor Cathode, Case
3	A _{PD}	Monitor Anode

3

Mounting Dimensions
(PL-SLD-00-SG0-C0 and
PL-SLD-00-SG0-C1)

PL-SLD-00-SG0-C0

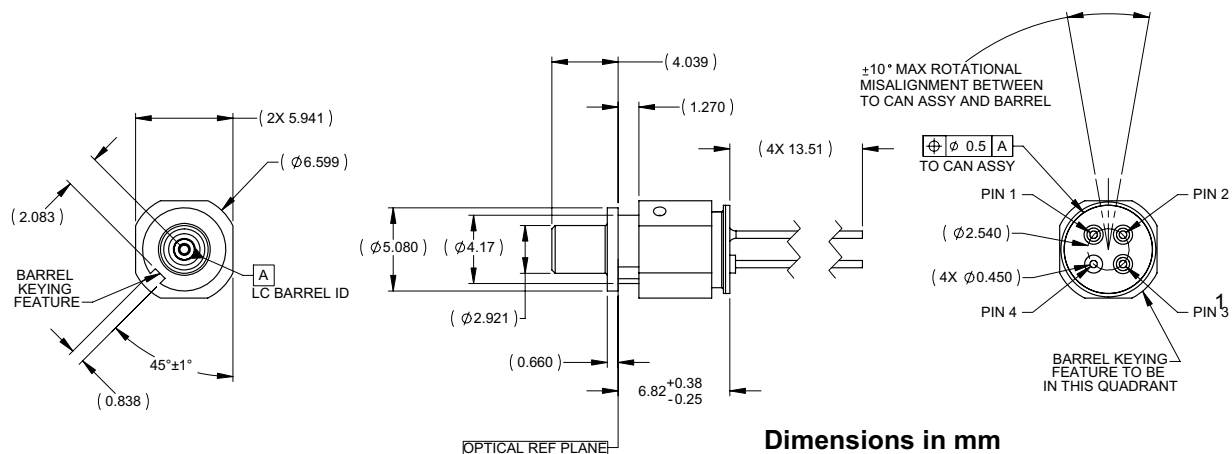
Pin	Symbol	Function
1	A _{LD}	VCSEL Anode
2	K _{LD}	VCSEL Cathode
3	A _{PD}	Monitor Anode
4	K _{PD}	Monitor Cathode

PL-SLD-00-SG0-C1

Pin	Symbol	Function
1	A _{LD}	VCSEL Anode
2	K _{LD}	VCSEL Cathode
3	K _{PD}	Monitor Cathode
4	A _{PD}	Monitor Anode

4

Mounting Dimensions (PL-SLD-00-SG0-C3)



PL-SLD-00-SG0-C3

Pin	Symbol	Function
1	A _{LD}	VCSEL Anode
2	K _{LD}	VCSEL Cathode
3	K _{PD}	Monitor Cathode
4	GND/A _{PD}	Case ground/Monitor Anode

Shipping Information

Shipped in anti-static stackable trays.

Absolute Maximum Ratings

(T_{case} = 25 °C , Continuous Wave (CW) operation unless otherwise stated)

Parameter	Symbol	Ratings	Unit
Storage temperature	T _{st}	-40 to +125	°C
Lead solder temperature	T _s	260 °C for 10 sec. 2 mm from case	
Laser forward current ¹	I _f	10	mA
Laser reverse voltage	BVR _{LD}	-5	V
Photodiode forward current	I _{fm}	10	mA
ESD ²		Class 1	

Note:
Conditions exceeding those listed may cause permanent damage to the device. Devices subjected to conditions beyond the limits specified for extended periods of time may adversely affect reliability.

1. Peak

2. HBM

5

Electro-optical Characteristics(T_{case} = 25 °C , CW operation unless otherwise stated)

VCSEL Parameter	Symbol	Test Condition	Minimum	Typical	Maximum	Unit
Case operating temperature	T _{op}		-40		85	°C
Peak emission wavelength	λ_p		840	850	860	nm
RMS spectral width	$\Delta\lambda$	I _f = 6 mA			0.65	nm
λ_p temperature coefficient	$\Delta\lambda_p$	T = -40 °C to 85 °C		0.06		nm/°C
Threshold current	I _{th}			1.0	2.0	mA
I _{th} temperature variation	ΔI_{th}	T = -40 °C to 85 °C			1.0	mA
Slope efficiency ¹	η	T = 25 °C	0.10	0.125	0.15	mW/mA
	η	P _{out} = 0.5 mW				
	η	T = -40 °C			0.17	mW/mA
	η	T = 85 °C	0.08			mW/mA
Slope efficiency temperature variation	$\Delta\eta/\Delta T$			-6000		PPM/°C
Laser forward voltage	V _f	I _f = 6 mA		1.8	2.2	V
Series resistance	R _s	T=25 °C, I _f = 6 mA	40	55	65	Ohms
	R _s	T=-40 °C			75	Ohms
	R _s	T=85 °C	35			Ohms
Series resistance temperature coefficient	$\Delta P_{\sigma}/\Delta T$	I _f = 6 mA				
		T= 0 °C to 70 °C		-2000		ppm/°C
Rise/Fall time ²	t _r	20% - 80%		80		psec
	t _f	20% - 80%		80		psec
Coupling efficiency	e _{fiber}			75		%
Average fiber coupled power	P _{OC}	I _f = 6 mA		0.50		mW
Relative intensity noise	RIN ₁₂	1 GHz BW, I _f = 6 mA		-130	-122	dB/Hz
Photocurrent	I _{PD}	P _{out} = 0.5 mW	0.15		0.6	mA
		V _r = 1.5 V				
Monitor current tracking ³	ΔI_m	P _{out} = 0.5 mW		0.05	0.1	%/°C
		T = 0 to 70 °C				
		V _{rm} = 1.5 V				
Monitor dark current	I _D	V _r = 5 V			20	nA
Monitor capacitance	C _M	V _{rm} = 1.5 V			100	pF
		Freq = 1 MHz				

1. Slope efficiency is defined as $\Delta P_o/\Delta I_f$.

2. Drive electronics and optical measurement hardware affect Rise/Fall Time measurement.

3. Change in optical power over temperature while operated in a feedback loop in which the monitor current is held constant.

Order Information

For more information on this or other products and their availability, please contact your local JDSU account manager or JDSU directly at 1-800-498-JDSU (5378) in North America and +800-5378-JDSU worldwide or via e-mail at customer.service@jdsu.com.

Sample: PL-SLC-00-SG0-C0

Part Number	Description
PL-SLC-00-SG0-C0	850 nm 4.25 G VCSEL LC TOSA with common cathode
PL-SLA-00-SG0-C0	850 nm 4.25 G VCSEL LC TOSA with common anode
PL-SLD-00-SG0-C0	850 nm 4.25 G VCSEL LC TOSA 4 pin differential drive
PL-SLD-00-SG0-C1	850 nm 4.25 G VCSEL LC TOSA 4 pin differential drive alternate pinout
PL-SLD-00-SG0-C3	850 nm 4.25 G VCSEL LC TOSA 4 pin differential drive alternate pinout