

PIC18F97J60 Data Sheet Errata

Clarifications/Corrections to the Data Sheet:

In the Device Data Sheet (DS39762D), the following clarifications and corrections should be noted. Any silicon issues related to the PIC18F97J60 will be reported in a separate silicon errata. Please check the Microchip web site for any existing issues.

1. Module: Electrical Characteristics

In Table 27-2, on page 428, the maximum Input Offset Voltage (Param No D300) is changed from ± 10 mV to ± 25 mV. The modified value is indicated in bold text in the following table:

TABLE 27-2: COMPARATOR SPECIFICATIONS

Operating Conditions: $3.0V \leq V_{DD} \leq 3.6V$, $-40^{\circ}C \leq T_A \leq +85^{\circ}C$ (unless otherwise stated)							
Param No	Sym	Characteristics	Min	Typ	Max	Units	Comments
D300	V _{IOFF}	Input Offset Voltage*	—	± 5.0	± 25	mV	
D301	V _{ICM}	Input Common-Mode Voltage*	0	—	$AV_{DD} - 1.5$	V	
D302	CMRR	Common-Mode Rejection Ratio*	55	—	—	dB	
300	T _{RESP}	Response Time ^{(1)*}	—	150	400	ns	
301	T _{MC2OV}	Comparator Mode Change to Output Valid*	—	—	10	μs	

* These parameters are characterized but not tested.

Note 1: Response time measured with one comparator input at $(AV_{DD} - 1.5)/2$, while the other input transitions from V_{SS} to AV_{DD}.

PIC18F97J60

REVISION HISTORY

Rev A Document (6/2008)

Initial release of this data sheet errata. Includes data sheet clarifications 1 (Electrical Characteristics).

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