

PIC16F87X Family Rev. B8 Silicon Errata

The PIC16F87X family (Rev. B8) parts you have received conform functionally to the Device Data Sheet (DS30292C), except for the anomalies described below. Any Data Sheet Clarification issues related to the PIC16F87X family will be reported in a separate Data Sheet errata. Please check the Microchip web site for any existing issues.

This errata applies to the PIC16F873 production version and all other production versions of the PIC16F87X family with the following Device/Revision IDs:

Part Number	Device ID	Revision ID
PIC16F873	0000 1001 011	0 0110

The Device IDs (DEVID and REVID) are located at the last two implemented addresses in program memory. They are shown in hexadecimal in the format "DEVID REVID".

1. Module: TMR1

When operating in External Clock mode (TMR1CS is set), reading either of the Timer1 registers (TMR1H or TMR1L) may cause the timer not to increment as expected. This occurs for both synchronous and asynchronous inputs.

The scenarios which display this are:

- When a read operation of the TMR1H register occurs, the TMR1L register may not increment.
- When a read operation of the TMR1L register occurs, the TMR1H register may not increment. This improper operation is only an issue when the TMR1L register increments from FFh to 00h (FFh → 00h) during the read of the TMR1L register.

Work around

Do not read either the TMR1H or the TMR1L registers when operating in External Clock mode (TMR1CS is set). If the application needs to read the 16-bit counter, evaluate if this function can be moved to the TMR0, or one of the other timer resources on the device.

2. Module: Capture/Compare/PWM Modules

While changing the mode of the module, when CCPxCON is loaded with 0x08, the CCPx pin is not pulled LOW as expected.

Work around

When changing the mode of the module, reset the CCP module (CCPxCON = 0x00) and load the new mode (CCPxCON = 0x08).

Note: x = 1 or 2 (either CCP1 or CCP2).

PIC16F87X

REVISION HISTORY

Rev A Document (12/2008)

Initial release of this document. Includes silicon issues
1 (TMR1) and 2 (Capture/Compare/PWM Modules).

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